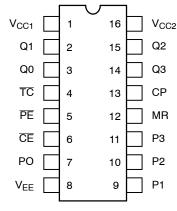
4-Bit Binary Counter

Description

The MC10H016 is a high-speed synchronous, pre-settable, cascadable 4-bit binary counter. It is useful for a large number of conversion, counting and digital integration applications.

Features

- Counting Frequency, 200 MHz Minimum
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10KTM Compatible
- Positive Edge Triggered
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant



Pin assignment is for Dual-in-Line Package

Figure 1. Pin Assignment

Table 1. TRUTH TABLE

CE	PE	MR	СР	Function
L H L H X	L H H X X	LLLLH	Z Z Z ZZ ZZ X	

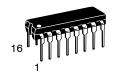
Z = Clock Pulse (Low to High); ZZ = Clock Pulse (High to Low)

Features include assertion inputs and outputs on each of the four master/ slave counting flip-flops. Terminal count is generated internally in a manner that allows synchronous loading at nearly the speed of the basic counter.



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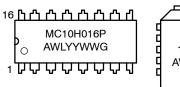
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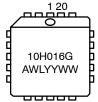




PDIP-16 P SUFFIX CASE 648-08 PLLC-20 FN SUFFIX CASE 775-02

MARKING DIAGRAMS*





A = Assembly Location
WL, L = Wafer Lot
YY, Y = Year
WW, W = Work Week
G = Pb-Fee Package

*For additional marking information, refer to Application Note <u>AND8002/D</u>.

ORDERING INFORMATION

Device	Package	Shipping [†]
MC10H016FNR2G	PLCC-20 (Pb-Free)	500/Tape & Reel
MC10H016PG	PDIP-16 (Pb-Free)	25 Units/Tube

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Table 2. MAXIMUM RATINGS

Symbol	Characteristic	Rating	Unit
V _{EE}	Power Supply (V _{CC} = 0)	-8.0 to 0	Vdc
VI	Input Voltage (V _{CC} = 0)	0 to V _{EE}	Vdc
l _{out}	Output Current Continuous Surge	50 100	mA
T _A	Operating Temperature Range	0 to +75	°C
T _{stg}	Storage Temperature Range Plastic Ceramic	−55 to +150 −55 to +165	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Table 3. ELECTRICAL CHARACTERISTICS (V_{EE} = -5.2 V $\pm 5\%$) (Note 1)

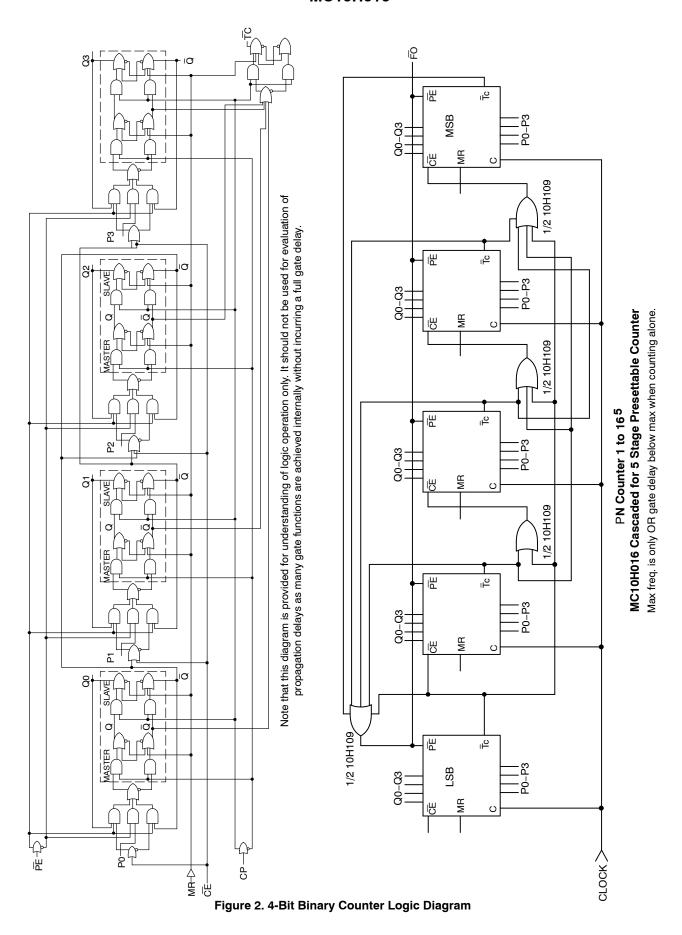
		C) °	2	5°	7	75°	
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
Ι _Ε	Power Supply Current	-	126	-	115	=	126	mA
I _{inH}	Input Current High All Except MR Pin 12 MR	- -	450 1190	- -	265 700	- -	265 700	μΑ
I _{inL}	Input Current Low	0.5	-	0.5	-	0.3	-	μΑ
V _{OH}	High Output Voltage	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
V _{OL}	Low Output Voltage	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
V _{IH}	High Input Voltage	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
V _{IL}	Low Input Voltage	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

^{1.} Each MECL 10H™ series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50 Ω resistor to −2.0 V.

Table 4. AC CHARACTERISTICS

		C) °	2:	5°		75°	
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
t _{pd}	Propagation Delay Clock to Q Clock to TC MR to Q	1.0 0.7 0.7	2.4 2.4 2.4	1.0 0.7 0.7	2.5 2.5 2.5	1.0 0.7 0.7	2.7 2.6 2.6	ns
t _{set}	Set-up Time P _n to Clock CE or PE to Clock	2.0 2.5	- -	2.0 2.5	- -	2.0 2.5	- -	ns
t _{hold}	Hold Time Clock to P _n Clock to CE or PE	1.0 0.5	- -	1.0 0.5	- -	1.0 0.5	- -	ns
f _{count}	Counting Frequency	200	-	200	-	200	-	MHz
t _r	Rise Time	0.5	2.0	0.5	2.1	0.5	2.2	ns
t _f	Fall Time	0.5	2.0	0.5	2.1	0.5	2.2	ns

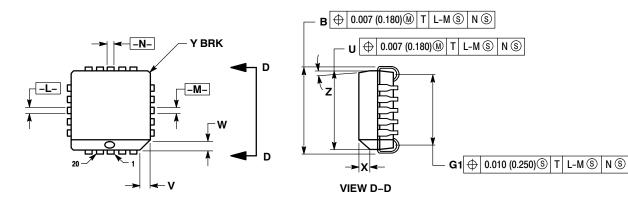
NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

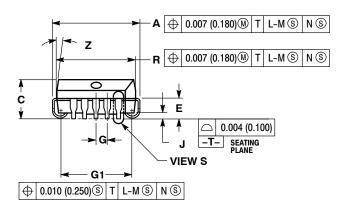


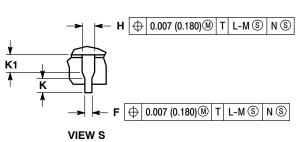
PACKAGE DIMENSIONS

20 LEAD PLLC CASE 775-02

ISSUE F







- 1. DIMENSIONS AND TOLERANCING PER ANSI Y14.5M,
- 2. DIMENSIONS IN INCHES.
 3. DATUMS -L-, -M-, AND -N- DETERMINED WHERE TOP OF LEAD SHOULDER EXITS PLASTIC BODY AT MOLD

 OF LEAD SHOULDER EXITS PLASTIC BODY AT MOLD PARTING LINE.

- PARTING LINE.

 4. DIMENSION G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.

 5. DIMENSIONS R AND U DO NOT INCLUDE MOLD FLASH. ALLOWABLE MOLD FLASH IS 0.010 (0.250) PER SIDE.

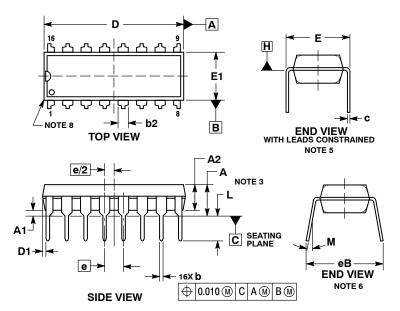
 6. DIMENSIONS IN THE PACKAGE TOP MAY BE SMALLER THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300). DIMENSIONS R AND U ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH BETWEEN THE TOP AND BOTTOM OF THE
- MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.

 7. DIMENSION H DOES NOT INCLUDE DAMBAR PROTRUSION OR INTRUSION. THE DAMBAR PROTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE GREATER THAN 0.037 (0.940). THE DAMBAR INTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE SMALLER THAN 0.025 (0.635).

	INC	HES	MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.385	0.395	9.78	10.03
В	0.385	0.395	9.78	10.03
С	0.165	0.180	4.20	4.57
E	0.090	0.110	2.29	2.79
F	0.013	0.021	0.33	0.53
G	0.050	BSC	1.27	BSC
н	0.026	0.032	0.66	0.81
J	0.020		0.51	-
K	0.025		0.64	-
R	0.350	0.356	8.89	9.04
Ω	0.350	0.356	8.89	9.04
V	0.042	0.048	1.07	1.21
W	0.042	0.048	1.07	1.21
Х	0.042	0.056	1.07	1.42
Υ		0.020		0.50
Z	2 °	10 °	2 °	10 °
G1	0.310	0.330	7.88	8.38
K1	0.040		1.02	

PACKAGE DIMENSIONS

PDIP-16 CASE 648-08 **ISSUE V**



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: INCHES. DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACKAGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.
- DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 0.10 INCH
- DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR TO DATUM C
- DIMENSION eB IS MEASURED AT THE LEAD TIPS WITH THE LEADS UNCONSTRAINED.
 DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE
- LEADS, WHERE THE LEADS EXIT THE BODY.
 PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE
- CORNERS)

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α		0.210		5.33	
A1	0.015		0.38		
A2	0.115	0.195	2.92	4.95	
b	0.014	0.022	0.35	0.56	
b2	0.060	TYP	1.52 TYP		
С	0.008	0.014	0.20	0.36	
D	0.735	0.775	18.67	19.69	
D1	0.005		0.13		
Е	0.300	0.325	7.62	8.26	
E1	0.240	0.280	6.10	7.11	
е	0.100 BSC		2.54 BSC		
eВ		0.430		10.92	
L	0.115	0.150	2.92	3.81	
М		10°		10°	

STYLE 1	:	STYLE 2	STYLE 2:		
PIN 1.	CATHODE	PIN 1.	COMMON DRAIN		
2.	CATHODE	2.	COMMON DRAIN		
3.	CATHODE	3.	COMMON DRAIN		
4.	CATHODE	4.	COMMON DRAIN		
5.	CATHODE	5.	COMMON DRAIN		
6.	CATHODE	6.	COMMON DRAIN		
7.	CATHODE	7.	COMMON DRAIN		
8.	CATHODE	8.	COMMON DRAIN		
9.	ANODE	9.	GATE		
10.	ANODE	10.	SOURCE		
11.	ANODE	11.	GATE		
12.	ANODE	12.	SOURCE		
13.	ANODE	13.	GATE		
14.	ANODE	14.	SOURCE		
15.	ANODE	15.	GATE		
16.	ANODE	16.	SOURCE		

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