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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HA17902A Series

Quad Operational Amplifier

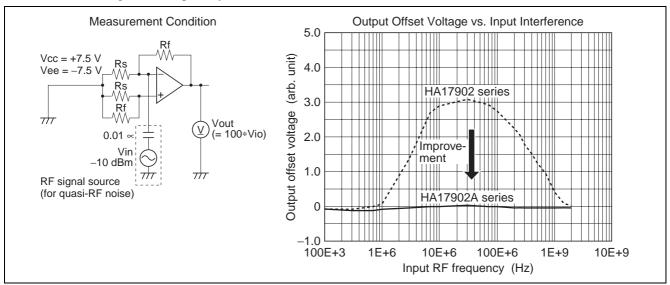
REJ03D0686-0200 Rev.2.00 Mar 10, 2006

Description

HA17902A series are quad operational amplifier that provide high gain and internal phase compensation, with single power supply. They can be widely used to control equipments.

Features

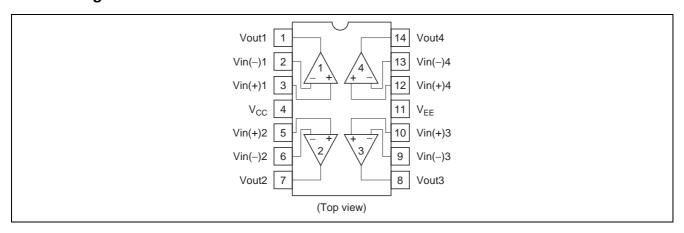
- Wide range of supply voltage, and single power supply used
- Internal phase compensation
- Wide range of common mode voltage, and possible to operate with an input about 0 V
- · Low electro-magnetic susceptibility level



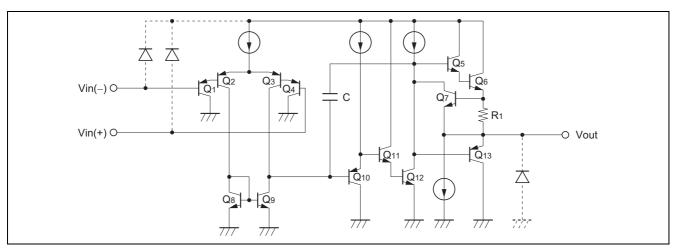
Ordering Information

Type No.	Application	Package Name	Package Code	
HA17902AP	Industrial use	DIP-14 pin	PRDP0014AB-B	
HA17902AFP		SOP-14 pin (JEITA)	PRSP0014DF-B	
HA17902ARP		SOP-14 pin (JEDEC)	PRSP0014DE-A	
HA17902AT		TSSOP-14 pin	PTSP0014JA-B	

Pin Arrangement



Circuit Schematic (1/4)



Note: If Input/Output terminals voltage over the absolute maximum ratings, there is possibility of mis-operation, characteristics deterioration and destruction, because of the current's flowing to parasitic diode in IC.

The Input/Output terminals are recommended to be protected with the clamp circuit which using the diode with low forward voltage (like schottky barrier diode) when there is a possibility for the Input/Output terminals voltage exceeds the absolute maximum ratings.

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Power supply voltage		V _{CC}	32	V
Output sink current		losink	50	mA
Common mode input voltage		V _{CM}	-0.3 to +V _{CC}	V
Differential input voltage		Vin(diff)	±V _{CC}	V
Output voltage		Vout	−0.3 to +V _{CC}	V
Allowable power dissipation DIP		P _T	625 * ²	mW
	SOP		625 * ³	
	TSSOP		400 *4	
Operating temperature		Topr	-40 to +85	°C
Storage temperature		Tstg	−55 to +125	°C

Notes: 1. HA17902AP:

This is the allowable values up to Ta = 50°C. Derate by 8.3 mW/°C.

2. HA17902AFP/ARP:

When it is mounted on glass epoxy board of 40 mm \cdot 40 mm \cdot 1.6 mmt with 10% wiring density, value at Ta \leq 25°C. If Ta > 25°C, derated by 6.25 mW/°C.

When it is mounted on glass epoxy board of 40 mm \cdot 40 mm \cdot 1.6 mmt with 30% wiring density. If Ta > 32°C, derated by 6.70 mW/°C.

3. HA17902AT:

These are the allowable values up to $Ta = 25^{\circ}C$. Derate by 4 mW/°C above that temperature.

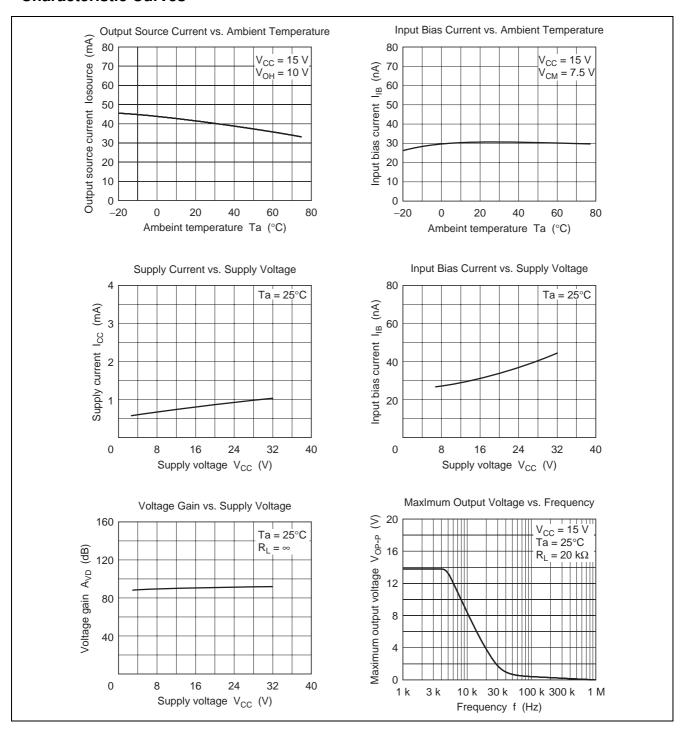
Electrical Characteristics

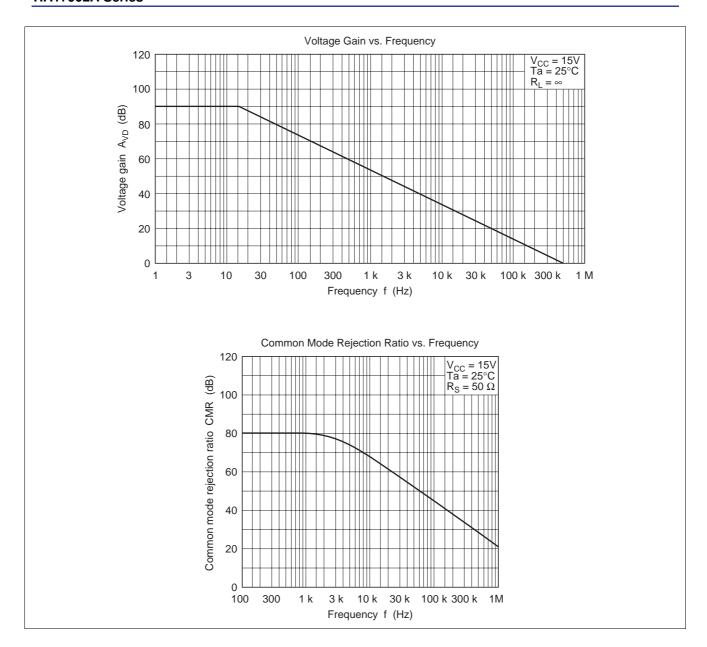
 $(V_{CC} = +15 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input offset voltage	V _{IO}	_	2	7	mV	$V_{CM} = 7.5 \text{ V}, R_S = 50 \Omega, Rf = 50 \text{ k}\Omega$
Input offset current	I _{IO}	_	5	50	nA	$V_{CM} = 7.5 \text{ V}, I_{IO} = I_{I(-)} - I_{I(+)} $
Input bias current	I _{IB}	_	30	500	nA	V _{CM} = 7.5 V
Power source rejection ratio	PSRR	_	93	_	dB	$f = 100 \text{ Hz}, R_S = 1 \text{ k}\Omega, Rj = 100 \text{ k}\Omega$
Voltage gain	A _{VD}	75	90	_	dB	$R_S = 1 \text{ k}\Omega, \text{ Rf} = 100 \text{ k}\Omega, R_L = \infty$
Common mode rejection ratio	CMR	_	80	_	dB	$R_S = 50 \Omega$, $Rf = 5 k\Omega$
Common mode input voltage range	V _{CM}	-0.3	_	13.5	V	$R_{S}=1~k\Omega,~Rf=100~k\Omega,~f=100~Hz$
Maximum output voltage	V _{OP-P}	_	13.6	_	V	$f=100~Hz,~R_S=1~k\Omega,~Rf=100~k\Omega, \\ R_L=20~k\Omega$
Output source current	Iosource	20	40	_	mA	$V_{IN}^{+} = 1 \text{ V}, V_{IN}^{-} = 0 \text{ V}, V_{OH} = 10 \text{ V}$
Output sink current	losink	10	20	_	mA	$V_{IN} = 0 \text{ V}, V_{IN} = 1 \text{ V}, V_{OL} = 2.5 \text{ V}$
Supply current	Icc	_	0.8	2	mA	$V_{IN} = GND, R_L = \infty$
Slew rate	SR	_	0.19	_	V/∝s	$f = 1.5 \text{ kHz}, V_{CM} = 7.5 \text{ V}, R_{L} = \infty$
Channel separation *1	CS	_	(120)	_	dB	f = 1 kHz
Output sink current	losink	15	50	_	∝A	$V_{IN}^{+} = 0 \text{ V}, V_{IN}^{-} = 1 \text{ V}, V_{OL} = 200 \text{ mV}$
		3	9	_	mA	$V_{IN}^{+} = 0 \text{ V}, V_{IN}^{-} = 1 \text{ V}, V_{OL} = 1 \text{ V}$
Output voltage	V _{OH1}	13.2	13.6	_	V	$I_{OH} = -1 \text{ mA}$
	V _{OH2}	12.0	13.3	_	V	$I_{OH} = -10 \text{ mA}$
Output voltage	V _{OL1}		0.8	1.0	V	I _{OL} = 1 mA
	V _{OL2}		1.1	1.8	٧	$I_{OL} = 10 \text{ mA}$

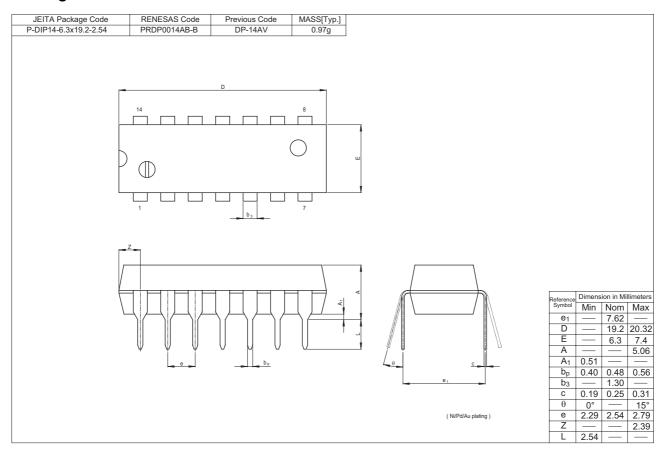
Note: 1. Design spec.

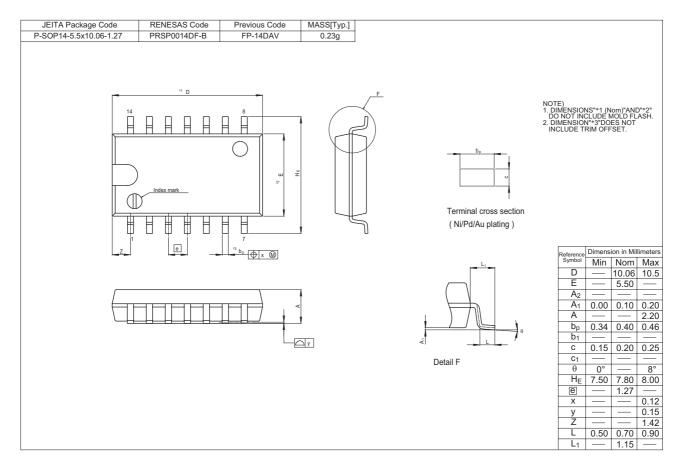
Characteristic Curves

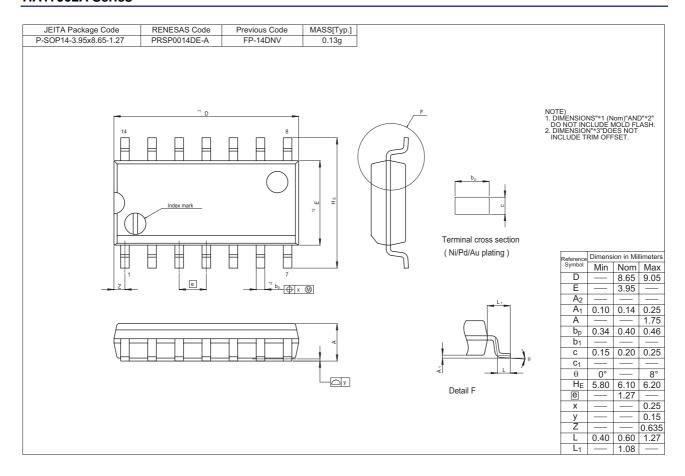


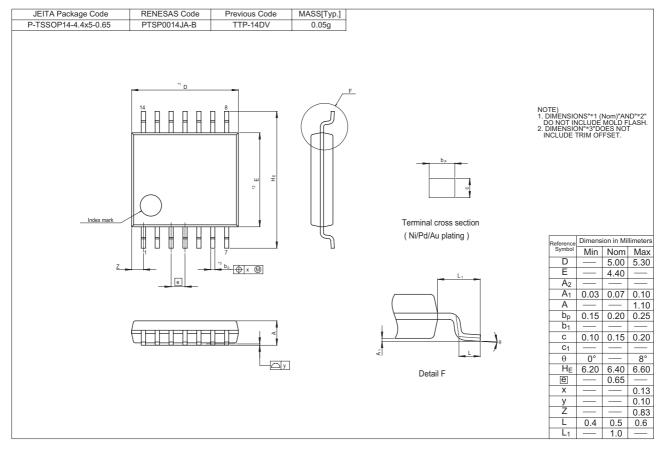


Package Dimensions









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