

SEMICONDUCTOR TM

KSC3569

High Speed Switching Application

- Low Collector Saturation VoltageSpecified of Reverse Biased SOA With Inductive Loads

NPN Epitaxial Silicon Transistor



1.Base 2.Collector 3.Emitter

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
сво	Collector-Base Voltage	500	V
CEO	Collector-Emitter Voltage	400	V
'EBO	Emitter-Base Voltage	7	V
c	Collector Current (DC)	2	А
CP	*Collector Current (Pulse)	4	А
3	Base Current	1	А
°c	Collector Dissipation (T _C =25°C)	15	W
- J	Junction Temperature	150	°C
- STG	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage	I _C = 0.5A, I _{B1} = 0.1A, L = 1mH	400		V
V _{CEX} (sus)1	Collector-Emitter Sustaining Voltage	$I_{C} = 0.5A, I_{B1} = -I_{B2} = 0.1A$ $T_{a} = 125^{\circ}C, L = 180\mu$ H, Clamped	450		V
V _{CEX} (sus)2	Collector-Emitter Sustaining Voltage	$I_{C} = 1A, I_{B1} = -I_{B2} = 0.2A,$ $T_{a} = 125^{\circ}C, L = 180\mu$ H, Clamped	400		V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 400 V, I_E = 0$		10	μΑ
I _{CER}	Collector Cut-off Current	$V_{CE} = 400V, R_{BE} = 51\Omega @$ $T_{C} = 125^{\circ}C$		1	mA
I _{CEX1}	Collector Cut-off Current	$V_{CE} = 400V, V_{BE} (off) = -5V$		10	μΑ
I _{CEX2}	Collector Cut-off Current	$V_{CE} = 400V, V_{BE} \text{ (off)} = -5V @$ $T_{C} = 125^{\circ}C$		1	mA
I _{EBO}	Emitter Cut-off Current	$V_{BE} = 5V, I_{C} = 0$		10	μA
h _{FE1} h _{FE2}	* DC Current Gain	$V_{CE} = 5V, I_C = 0.1A$ $V_{CE} = 5V, I_C = 0.5A$	20 10	80	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	$I_{\rm C} = 0.5 {\rm A}, I_{\rm B} = 0.1 {\rm A}$		1	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	$I_{\rm C} = 0.5 {\rm A}, \ I_{\rm B} = 0.1 {\rm A}$	-	1.2	V
t _{ON}	Turn ON Time	V _{CC} = 150V, I _C = 0.5A		1	μs
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 0.1A$		2.5	μs
t _F	Fall Time	$R_L = 300\Omega$		1	μs

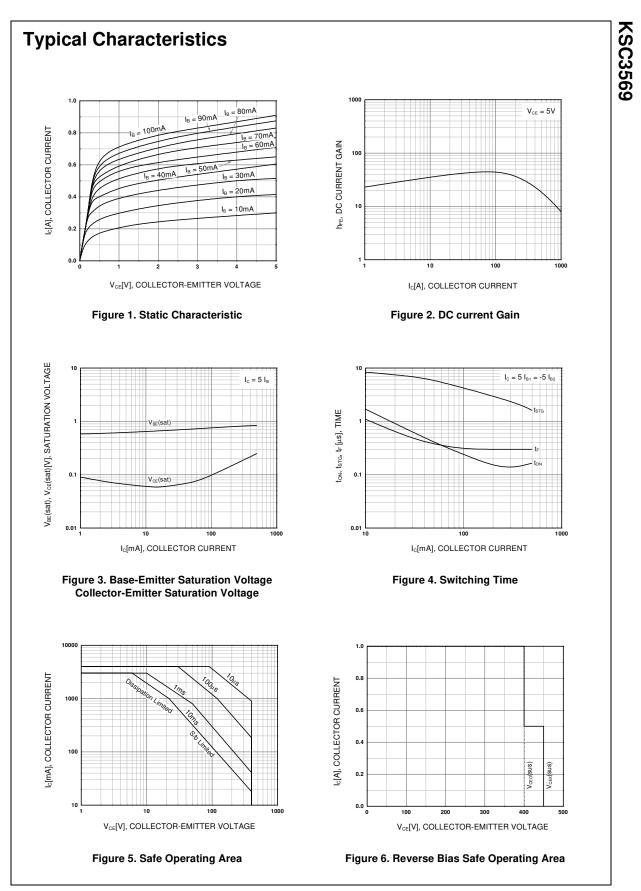
Pulse Test: PW≤350µs, Duty Cycle≤2% Pulsed

h_{FF} Classification

Classification	R	0	Y
h _{FE1}	20 ~ 40	30 ~ 60	40 ~ 80

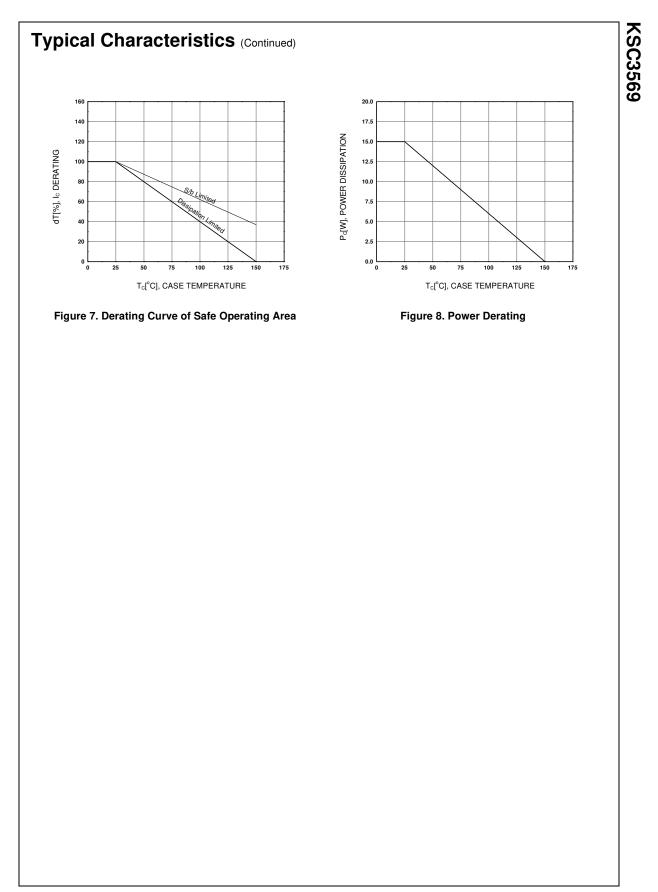
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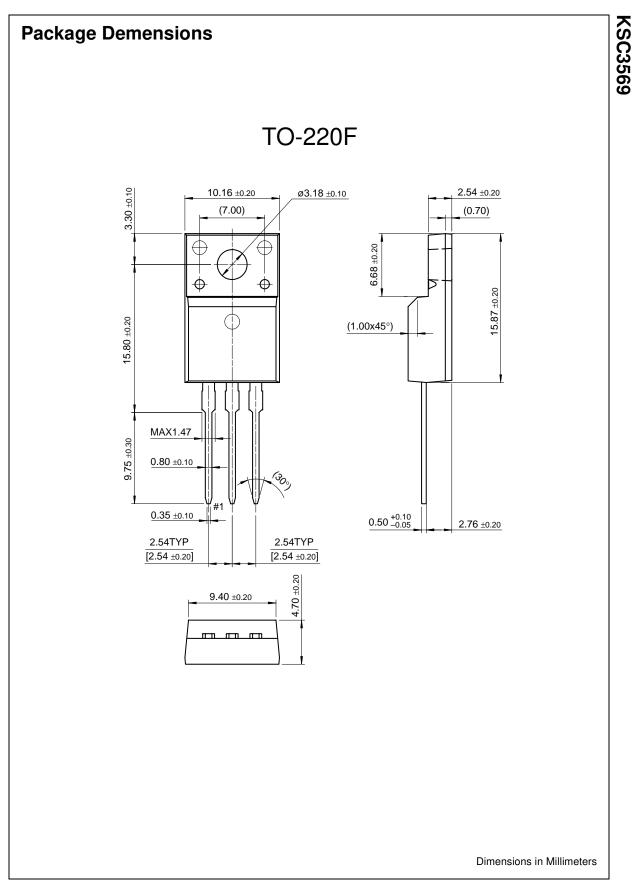
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