TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

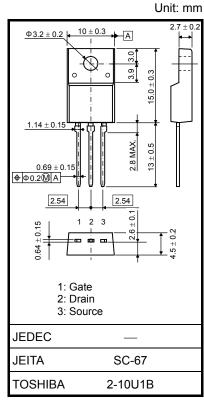
TK12A60D

Switching Regulator Applications

- Low drain-source ON resistance: RDS (ON) = 0.45Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 7.5 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 600 \ V)$
- Enhancement-mode: $V_{th} = 2.0$ to 4.0 V ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	Ι _D	12	А	
	Pulse (Note 1)	I _{DP}	48	A	
Drain power dissipation	on (Tc = 25°C)	PD	45	W	
Single pulse avalanche energy (Note 2)		E _{AS}	359	mJ	
Avalanche current		I _{AR}	12	А	
Repetitive avalanche energy (Note 3)		E _{AR}	4.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	–55 to 150	°C	

Absolute Maximum Ratings (Ta = 25°C)



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

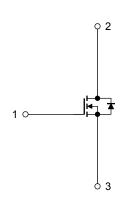
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 4.36 mH, $R_G = 25 \Omega$, $I_{AR} = 12 \text{ A}$ Note 3: Repetitive rating: pulse width limited by maximum channel temperature This transistor is an electrostatic sensitive device. Please handle with caution.

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Internal Connection



Start of commercial production 2009-01

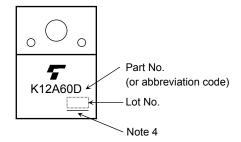
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off current		I _{DSS}	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600			V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		0.45	0.55	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	1.9	7.5		S
Input capacitance		C _{iss}		_	1800	—	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz$	_	9	—	pF
Output capacitance		C _{oss}		_	190	_	
Switching time	Rise time	tr	$10 V$ $I_D = 6 A$ V_{OUT}		40		
	Turn-on time	t _{on}	$\begin{array}{c} 0 \ V \ -1 \ \hline \\ 50 \ \Omega \ \end{array} \begin{array}{c} \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \end{array} \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	_	80	_	ns
	Fall time	t _f			15	_	
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 μ s		110	_	
Total gate charge		Qg			38		
Gate-source charge		Q _{gs}	$V_{DD}\approx 400~V,~V_{GS}=10~V,~I_{D}=12~A$	_	24		nC
Gate-drain charge		Q _{gd}	1	_	14		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	12	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	48	А
Forward voltage (diode)	V _{DSF}	$I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V},$	_	1200	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	13	_	μC

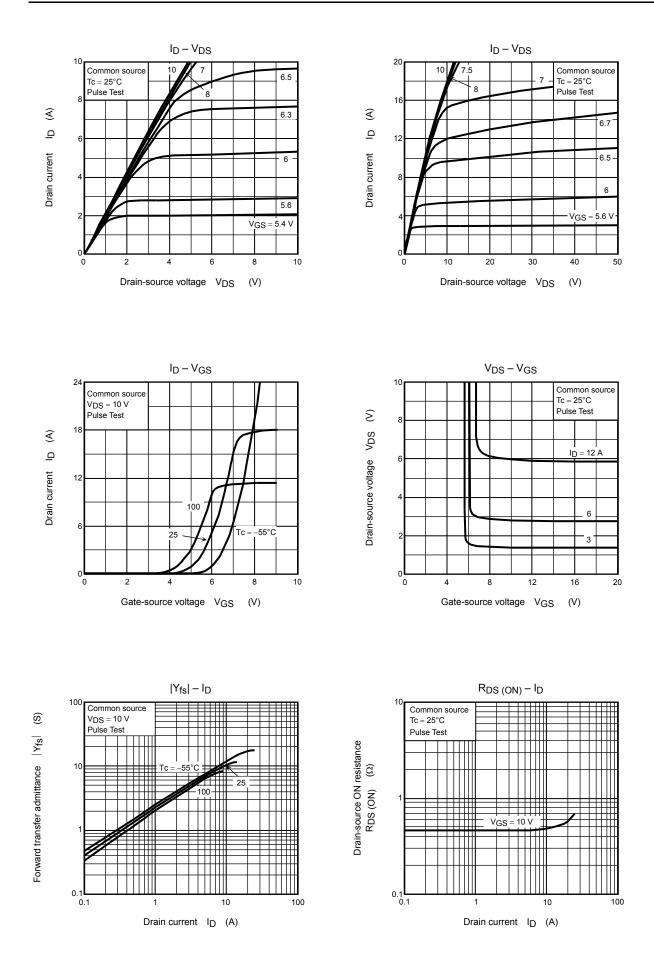
Marking



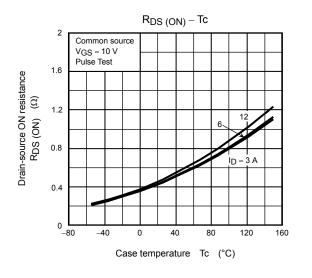
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

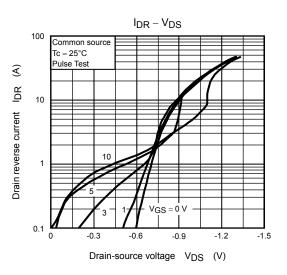
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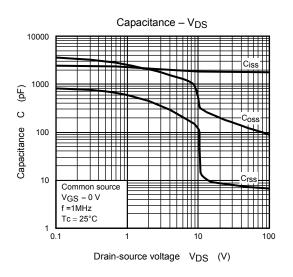
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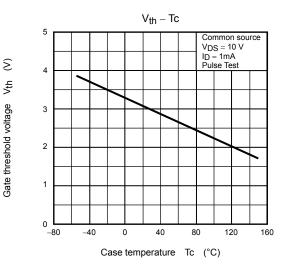


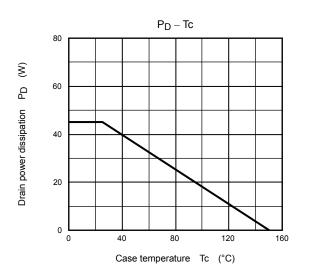
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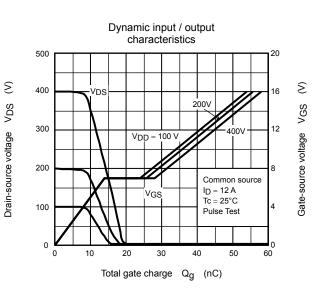


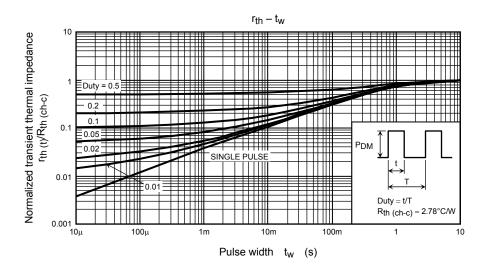


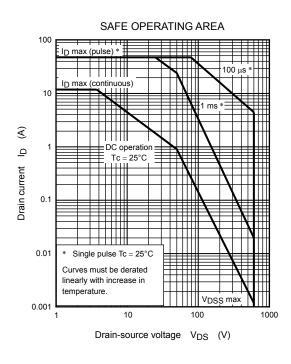


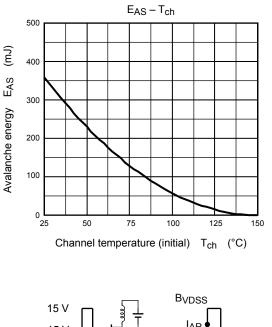


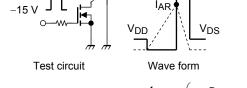














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