

KSA1156

High Voltage Switching Low Power Switching Regulator DC-DC Converter

- High Breakdown Voltage
- · Low Collector Saturation Voltage
- · High Speed Switching



PNP Silicon Transistor

Absolute Maximum Ratings $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{CBO}	Collector-Base Voltage	- 400	V
V _{CEO}	Collector-Emitter Voltage	- 400	V
V _{EBO}	Emitter-Base Voltage	- 7	V
I _B	Base Current	- 0.25	Α
I _C	Collector Current (DC)	- 0.5	Α
I _{CP}	Collector Current (Pulse)	- 1	Α
P _C	Collector Dissipation (T _a =25°C)	1	W
P _C	Collector Dissipation (T _C =25°C)	10	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

$\textbf{Electrical Characteristics} \ \textbf{T}_{\text{C}} = 25 ^{\circ} \textbf{C} \ \text{unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage	I _C = - 100mA, I _B = - 10mA L = - 20mH	- 400		V
V _{CEX} (sus)	Collector-Emitter Sustaining Voltage	$I_C = -200 \text{mA}, I_{B1} = -I_{B2} = -20 \text{mA}$ $V_{BE}(\text{off}) = 5 \text{V}, L = 10 \text{mH}$	- 400		V
I _{CBO}	Collector Cut-off Current	V _{CB} = - 400V, I _E = 0		- 100	μΑ
I _{EBO}	Emitter Cut-off Current	V _{EB} = - 5V, I _C = 0		- 10	μΑ
I _{CEX1}	Collector Cut-off Current	$V_{CE} = -400V, V_{BE}(off) = 1.5V$		- 100	μΑ
I _{CEX2}	Collector Cut-off Current	$V_{CE} = -400V, V_{BE}(off) = 1.5V$ $T_{C} = 125^{\circ}C$		- 1	mA
h _{FE}	DC Current Gain	V _{CE} = - 5V, I _C = - 100mA	30	200	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = - 100mA, I _B = - 10mA		- 1	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = - 100mA, I _B = - 10mA		- 1.2	V
t _{ON}	Turn On Time	V _{CC} = - 150V, I _C = - 100mA		1	μs
t _{STG}	Storage Time	$I_{B1} = -10 \text{mA}$, $I_{B2} = 20 \text{mA}$		4	μs
t _F	Fall Time	$R_L = 1.5K\Omega$		1	μs

h_{FE} Classification

Classification	N	R	0	Υ
h _{FE}	30 ~ 60	40 ~ 80	60 ~ 120	100 ~ 200

V_{CE} = -5V

Typical Characteristics

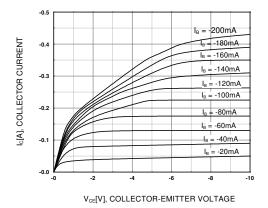
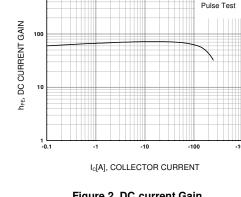


Figure 1. Static Characteristic



1000

Figure 2. DC current Gain

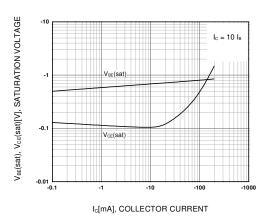


Figure 3. Collector-Emitter Saturation Voltage **Base-Emitter Saturation Voltage**

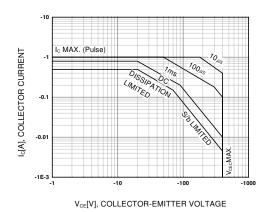


Figure 4. Safe Operating Area

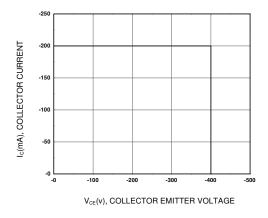


Figure 5. Reverse Bias Safe Operating Area

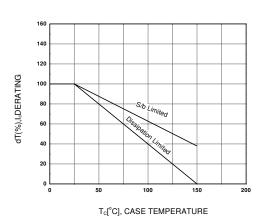


Figure 6. Derating Curve of Safe Operating Areas

©2000 Fairchild Semiconductor International Rev. A, February 2000

Typical characteristics (Continued)

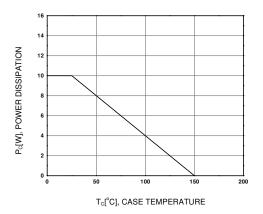
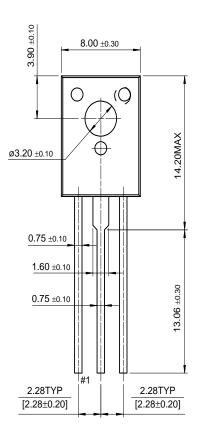


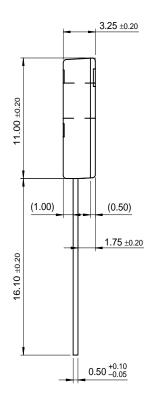
Figure 7. Power Derating

©2000 Fairchild Semiconductor International Rev. A, February 2000

Package Demensions

TO-126





Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

FACT™ QFET™ FACT Quiet Series™ QS™

FAST® Quiet Series TM SuperSOT TM -3 SuperSOT TM -6

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

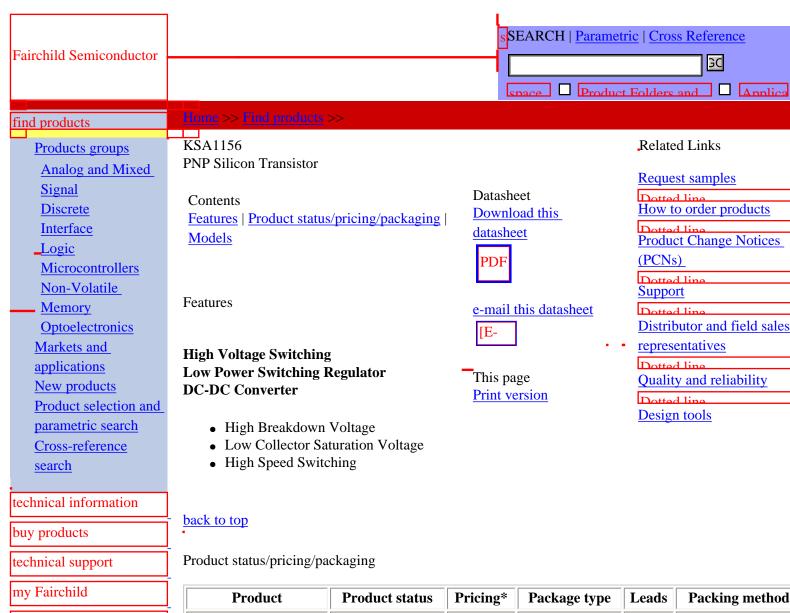
PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2000 Fairchild Semiconductor International Rev. E

company



Related Links

Request samples

Dotted line How to order products

Product Change Notices

ЭC

(PCNs)

Dotted line

Support

Distributor and field sales

representatives

Dotted line

Quality and reliability

Design tools

Product	Product status	Pricing*	Package type	Leads	Packing method
KSA1156OSTSTU	Full Production	\$0.193	<u>TO-126</u>	3	RAIL
KSA1156YS	Full Production	\$0.193	<u>TO-126</u>	3	BULK
KSA1156OSTU	Full Production	\$0.193	<u>TO-126</u>	3	RAIL
KSA1156YSTSTU	Full Production	\$0.193	<u>TO-126</u>	3	RAIL
KSA1156OS	Full Production	\$0.193	<u>TO-126</u>	3	BULK

^{* 1,000} piece Budgetary Pricing

back to top

Models

Package & leads Condition		Temperature range	Software version	Revision date	
PSPICE					
TO-126-3	Electrical	-25°C to 100°C	9.2	Aug 10, 2001	

file:///E|/new/html/KSA1156.html

back to top

Home | Find products | Technical information | Buy products |
Support | Company | Contact us | Site index | Privacy policy

© Copyright 2002 Fairchild Semiconductor