OSRAM SFH 7771 Datasheet

Discontinued

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Chip on board



Ambient Light and Proximity Sensor





Applications

- 3D Sensing

Features

- ESD: 2 kV acc. to ANSI/ESDA/JEDEC JS-001 (HBM, Class 2)
- Miniature package
- I²C interface (max. 400kHz)



Ordering Information

Type SFH 7771 Ordering Code Q65111A4189



Application diagram 1



- Cathode of the emitter is directly connected to the sensor (If max = 200 mA)
- Bypass capacitors for V_{DD} and V_{LED} are required for proper operation of the device.
- This example shows ADDR-Pin connected to VDD. Therefore, the I²C-Address is 0111001 binary.
- Proposed size for the pull-up resistors are 10kOhm.



Application diagram 2



- Emitter is driven externally over a PMOS transistor
- Bypass capacitors for VDD and VLED are required for proper operation of the device.
- This example shows ADDR-Pin connected to VDD. Therefore the I2C-Address is 0111000 binary.
- Proposed size for the pull-up resistors are 10kOhm.



Pin configuration

Pin	Name	Function
1	ADDR	I2C address pin; connect to
		GND for 0x38 (7 bit-address)
		V _{DD} for 0x39 (7 bit-address)
2	V _{DD}	Power supply pin
3	GND	Ground pin
4	TEST	Test pin; connected to GND
5	SCL	I ² C bus serial clock pin
6	SDA	I ² C bus serial data pin
7	INT	Interrupt pin; open drain output; configured via I ² C bus
8	LEDC	LED cathode pin;
		current and interval is defined via I ² C bus



Short Evaluation program

Register	Command	Action
0x42	0x3F	set LED pulse current to 200mA and ALS gain to 0128
0x41	0x06	activate ALS & PS with a measurement repetition time of 100ms
Wait 100ms		
0x44	read data	read LSB of proximity measurement data
0x45	read data	read MSB of proximity measurement data
0x46	read data	read LSB of ambient light measurement of VIS diode
0x47	read data	read MSB of ambient light measurement of VIS diode
0x48	read data	read LSB of ambient light measurement of IR diode
0x49	read data	read MSB of ambient light measurement of IR diode

I²C Interface

- I/O-pins are open drain type and logic high level is set with external pull-up resistors
- SFH 7771 operates in slave mode. Slave address is 0111000 (0x38) when ADDR-Pin is connected to GND or 0111001 (0x39) if ADDR-Pin is connected to V_{DD}
- Designed for the I2C Fast mode (400 kb/s)
- Interrupt pin (INT): open-drain output (like SDA and SCL)

Block diagram





Characteristics

T_A = 25 °C

Parameter	Symbol	Values	
Red Emitter			
Peak wavelength	λ _{pock}	typ.	660 nm
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$	треак		
Centroid Wavelength 6)	$\lambda_{centroid}$	min.	652 nm
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$		typ.	655 nm
		max.	658 nm
Spectral bandwidth at 50% Irel,max (FWHM)	Δλ	typ.	17 nm
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$			
Half angle	φ	typ.	± 60 °
Rise time (10%/ 90%)	tr	typ.	24 ns
I_F = 100 mA; R_L = 50 Ω			
Fall time (10%/ 90%)	t _f	typ.	24 ns
I_F = 100 mA; R_L = 50 Ω			
Forward voltage 7)	VF	min.	1.7 V
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$		typ.	1.9 V
		max.	2.2 V
Reverse current	IR		not designed for
V _R = 12V			reverse operation
Radiant intensity ⁸⁾	le	min.	3.6 mW / sr
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$		typ.	4.6 mW / sr
		max.	6.9 mW / sr
Total radiant flux	Φe	typ.	13.5 mW
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$			
Temperature coefficient of brightness	TCı	typ.	-0.7 % / K
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$			
Temperature coefficient of wavelength	TCλ	typ.	0.18 nm / K
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$			
Temperature coefficient of voltage	TCv	typ.	-1.7 mV / K
$I_F = 20 \text{ mA}; t_p = 20 \text{ ms}$			
Thermal resistance junction solder point real	Rthus	max.	265 K / W



Measurement modes

Mode	Description					
OFF	The device is inactive. Other units may use the I2C bus without any restrictions; I/O pins and INT are in					
	high Z state. There is no sink current through the LED					
STAND-BY	This is the initial mode after power-up. IDD is typ. 0.8µA. No measurement is performed. Device can be					
	activated by I2C bus communication. Data registers can be read and written.					
ALS / PS free	Measurements are triggered internally by the SFH 7771. Stand-by / active mode for ALS and PS					
running	measurement time, interrupt options and LED current can be adjusted via I2C register. Measurement					
	results can be read from the data register, the status from the interrupt register					



If VDD exceeds the threshold voltage, the sensor will switch from OFF mode to STAND-BY mode. As shown in the transitiondiagram above it is possible to switch between all modes without any restriction.



Maximum Ratings

T_A = 25 °C

Parameter	Symbol		Values
Operating temperature range	T _{op}	min.	-40 °C
		max.	85 °C
Storage temperature range	T _{stg}	min.	-40 °C
		max.	100 °C
Maximum supply voltage	V _{DD}	max.	4.5 V
between V _{DD} and GND			
Maximum voltage of SDA, SCL to GND	V_{SDA}/V_{SCL}	max.	4.5 V
Maximum voltage of INT to GND	V _{INT}	max.	7 V
Maximum voltage of VLED to GND	VLEDC	max.	7 V
Maximum Current of INT and SDA	I _{INT} / I _{SDA}	max.	7 mA
ESD withstand voltage	ESD	max.	2 kV
acc. to ANSI/ESDA/JEDEC JS-001 (HBM, Class 2)			



Operating conditions

Parameter	Symbol		Values	
Supply voltage	V _{DD}	min.	2.3 V	
		typ.	2.5 V	
		max.	3.6 V	
Ripple on supply voltage	V _{DD,rip}	max.	200 mV	
V_{DDmin} and V_{DDmax} must stay in the V_{DD} range,				
DC 100 MHz				
V _{DD} threshold voltage	V _{DD,th}	typ.	1.7 V	
voltage to initiate the start-up procedure		max.	2.3 V	
Pull-up Voltage for INT	V _{INT,pullup}	max.	5.5 V	
Pull-up Voltage for SCL and SDA	V _{IO}	min.	1.65 V	
		max:	3.6 V	
SDA and SCL input low level voltage	V _{SDA_low}	max.	0.54 V	
	V _{SCL_low}			
SDA and SCL input high level voltage	V_{SDA_high}	min.	1.26 V	
	V_{SCL} high			
SDA and SCL input current	ISDA_low	min.	-10 µA	
	ISCL_low	max.	10 µA	
INT output low level voltage (I _{INT} = 3 mA)	V _{INT_low}	max.	0.4 V	
When INT is active V_{INT} = low.				
When INT is inactive V_{INT} = high				
LEDC Terminal Voltage	V _{LED}	min.	0.7 V	
		typ.	2.5 V	
		max.	5.5 V	
Ripple V _{LED}	V _{LED,rip}	max.	200mV	



Characteristics

T_A = 25 °C

Parameter	Symbol		Values	
Conditions for OFF mode	$V_{\text{DD,off}}$	typ.	0.5 V	
Current consumption in OFF mode	I _{DD,off}	typ.	0 µA	
$V_{DD} < 0.5 V$				
STAND-BY mode current consumption	I _{DD,stby}	typ.	0.8 µA	
Mode_control(0x41) = 0x00; V_{DD} = 2.5 V		max.	1.5 µA	
Proximity Sensor (PS)				
Wavelength of maximum sensitivity	$\lambda_{S,max}$	typ.	850 nm	
Sensitivity range	Ee	typ.	1 5000	
λ = 850nm			µW/cm ²	
Proximity sensor output	PS _{out}	min.	187 counts	
Ee = 324 µW/cm ² ;		typ.	234 counts	
Ambient irradiance = 0 µW/cm ²		max.	281 counts	
LED on time for one measurement	t _{LED ON}	min.	80 µs	
		typ.	200 µs	
		max.	300 µs	
LED current, programmable	ILED	min.	25 mA	
		max.	200 mA	
Accuracy of LED current source	ILED	min.	22.5 mA	
ALS_PS_CONTROL: LED Current (0b00)		typ.	25 mA	
		max.	27.5 mA	
Mean current consumption in PS mode	I _{DD}	typ.	90 µA	
current consumption of the pulsed LED is not included;		max.	150 µA	
MODE_CONTROL(0x41) = 0x03; all other registers are default; V_{DD} =				
2.5 V				
Mean current consumption in PS mode during the 200 μs LED pulse	I _{DD}	typ.	6.5 mA	
(t _{led on})		max.	8.5 mA	
current consumption of the pulsed LED is not included				
Temperature coefficient of proximity sensor	TC _{PS}	typ.	0.15 % / K	



Example of Proximity Setup

When proximity sensing is performed, it is desirable that only light from a reflecting object reaches the SFH 7771. Depending on the optical setup, additional and unintended light paths from the IR-Emitter to the detector may exist, which is referred to as '(optical) crosstalk'. One measure to avoid such crosstalk is to add a separator between emitter and detector as drafted in the picture below. For details please refer to our SFH 7771 application note.





Characteristics

T_A = 25 °C

Parameter	Symbol	Values	
Ambient Light Sensor: ALS_VIS and ALS_ IR diode			
Wavelength of max. sensitivity for ALS_VIS	λ_{Smax}	typ.	520 nm
Spectral range of sensitivity (10 % of S _{max})	$\lambda_{S10\%}$	min.	380 nm
of ALS VIS		max.	950 nm
Wavelength of max. sensitivity of ALS_IR	λ_{Smax}	typ.	880 nm
Spectral range of sensitivity (10 % of S _{max})	$\lambda_{S10\%}$	min.	800 nm
of ALS IR		max.	1070 nm
Illuminance measurement range is programmable		min.	0.001 lx
(MODE_CONTROL (0x41) = 0x0A or 0x0B)		max.	43000 lx
ALS_VIS sensor output	ALS_{VIS_out}	min.	1275 counts
1000lx; white LED; VDD = 2.5V		typ.	1500 counts
MODE_CONTROL (0x41) = 0x08		max.	1725 counts
ALS_PS_CONTROL (0x42): Gain X1			
ALS_IR sensor output	ALS_{IR_out}	min.	516 counts
324µW/cm2; IRED 850nm; VDD = 2.5V		typ.	608 counts
MODE_CONTROL (0x41) = 0x08		max.	700 counts
ALS_PS_CONTROL (0x42): Gain = X1			
ALS_VIS sensor output at darkness	ALS_{VIS_out}	min.	0 counts
MODE_CONTROL (0x41) 0x08		typ.	0 counts
ALS_PS_CONTROL (0x42): Gain = X1		max.	2 counts
ALS_IR sensor output at darkness	ALS _{IR_out}	min.	0 counts
MODE_CONTROL (0x41) = 0x08		typ.	0 counts
ALS_PS_CONTROL (0x42): Gain = X1		max.	2 counts
Resolution of the digital output signal based on gain settings for	ALS_{VIS_out}		
ALS_VIS:			
MODE_CONTROL (0x41) = 0x08; $t_{int ALS}$ = 100ms			
Gain X1		typ.	0.68 lx/count
Gain X2		typ.	0.34 lx/count
Gain X64		typ.	0.01 lx/count
Gain X128		typ.	0.005 lx/count
High sensitive mode:			
MODE_CONTROL (0x41) = 0x08; $t_{int ALS}$ = 400ms			
Gain X128		tvp.	0.001 lx/count



Characteristics (continued)

T_A = 25 °C

Parameter	Symbol		Values
Ambient Light Sensor: ALS_VIS and ALS_ IR diode			
Typical temperature coefficient for ALS measurement	TC _{Ev}	typ.	0.2 % / K
1000lx; white LED; V_{DD} = 2.5V			
Mean current consumption	I _{DD}	typ.	90 µA
$MODE_CONTROL (0x41) = 0x08$		max.	150 µA
other registers are default			
Typical error by Flicker noise		max.	3 %
caused by bulbs (f=50 or 60 Hz) or fluorescent lamps			



Relative Spectral Sensitivity ^{1), 2)}

ALS_VIS S_{rel_VIS} = $f(\lambda)$



Relative Spectral Sensitivity 1), 2)

ALS_IR

 $\boldsymbol{S}_{_{rel_IR}}$ = f(\lambda); 100% = maximum sensitivity of ALS_VIS diode





Directional Characteristics ^{1), 2)}

ALS_VIS diode $S_{rel} = f(\phi)$



Current Consumption ^{1), 2)}

standby mode



Current Consumption ^{1), 2)}

PS mode





Current Consumption ^{1), 2)}

ALS mode





ALS_VIS sensitivity ranges

ALS_VIS output $f(E_V)$; white LED; f(sensitivity settings)

T_{int}: integration time (register 0x41); X: gain settings (register 0x42)



PS sensitivity f(Ee = irradiance)

V_{DD}=2.5V; λ=850nm



Registers Overview

Register	Туре	Name	Function
0x40	R/W	SYSTEM_CONTROL	System Control
0x41	R/W	MODE_CONTROL	ALS and PS General Control
0x42	R/W	ALS_PS_CONTROL	ALS Gain and PS current Control
0x43	R/W	PERSISTENCE	PS Interrupt Persistence Control
0x44	R	PS_DATA_LSB	Output data of PS measurement, LSB
0x45	R	PS_DATA_MSB	Output data of PS measurement, MSB
0x46	R	ALS_VIS_DATA_LSB	Output data of ALS_VIS measurement, LSB
0x47	R	ALS_VIS_DATA_MSB	Output data of ALS_VIS measurement, MSB
0x48	R	ALS_IR_DATA_LSB	Output data of ALS_IR measurement, LSB
0x49	R	ALS_IR_DATA_MSB	Output data of ALS_IR measurement, MSB
0x4A	R/W	INTERRUPT_CONTROL	Interrupt Control
0x4B	R/W	PS_TH_LSB	PS interrupt upper threshold level, LSB
0x4C	R/W	PS_TH_MSB	PS interrupt upper threshold level, MSB
0x4D	R/W	PS_TL_LSB	PS interrupt lower threshold level, LSB
0x4E	R/W	PS_TL_MSB	PS interrupt lower threshold level, MSB
0x4F	R/W	ALS_VIS_TH_LSB	ALS_VIS interrupt upper threshold level, LSB
0x50	R/W	ALS_VIS_TH_MSB	ALS_VIS interrupt upper threshold level, MSB
0x51	R/W	ALS_VIS_TL_LSB	ALS_VIS interrupt lower threshold level, LSB
0x52	R/W	ALS_VIS_TL_MSB	ALS_VIS interrupt lower threshold level, MSB



SYSTEM_CONTROL register (0x40)

The SYSTEM_CONTROL register is used to control the software (SW) reset and the interrupt function (INT). Manufacturer ID and Part ID can be read.

R/W-Register 0x40

Bit	7	6	5	4	3	2	1	0	
			Manuf	Manufacturer ID (Read			Part ID (Read		
	SW reset	INT reset		only)		only)			
default	0 Initial reset is not	0 INT pip status is pat initialized	001			001			
	started	o nor pin status is not initialized							
	0 Initial reset is not	0 INT pip status is pat initialized							
	started	o not pin status is not initialized	T pin status is not initialized						
	1 Initial resot started	1 INT pin become inactive (high							
		impedance)							

MODE_CONTROL register (0x41)

CONTROL of PS and ALS operating modes and time settings.

Repetition time is the time between two separate measurements. Integration time is the duration for one measurement. ALS high sensitivity modes are 1010 and 1011 with an increased integration time of 400ms. In PS operating mode: "normal mode" only one PS measurement is performed during one PS repetition time. In PS operating mode: "twice mode" two independent PS measurement are performed within one PS repetition time. Both measurements are independent and can trigger the interrupt. This feature can be used to decrease the interrupt update time if the persistence function (register 0x43) is used.

R/W-Regis	/W-Register 0x41									
	7	6	5	4	3	2	1	0	Repetition / Integration	Repetition time
Bit									time	
	Res	erved	l	PS operating mode					ALS	PS
default				0 normal mode		00	00		standby	standby
				0 normal mode		00	00		standby	standby
				1 twice mode		00	01		standby	10ms
						00	10		standby	40ms
						00	11		standby	100ms
						01	00		standby	400ms
						01	01		100ms / 100ms	standby
						01	10		100ms / 100ms	100ms
						0111			100ms / 100ms	400ms
						10	00		400ms / 100ms	standby
						10	01		400ms / 100ms	100ms
						10	10		400ms / 400ms	standby
						1011			400ms / 400ms	400ms
						1100 50ms / 50 ms 50ms			50ms	
					Rest forbidden					

ALS_PS_CONTROL register (0x42)

ALS and PS Control of set the PS output mode, the ALS gain and the LED current. In the "Infrared DC level output" PS mode (bit <6> = 1) the sensor measures the infrared DC ambient level. The proximity value of the reflected signal is not available in this mode.

R/W-Register 0x42

Bit	7	6	5 4 3 2			1	0
	Reserved (read only)	PS output	ALS Gain f	or ALS_	VIS and	LED c	urrent
			A	LS_IR			
default	write 0	0 proximity output	0000	X1	X1	11 20	0mA
		0 proximity output	0000	X1	X1	00 25	mA
		1 Infrared DC level output	0100	X2	X1	01 50	mA
			0101	X2	X2	10 10	0 mA
			1010	X64	X64	11 20	0 mA
			1110	X128	X64		
			1111	X128	X128		
			rest forbidden				

PERSISTENCE Register (0x43)

Setting of persistence interrupt function. Persistence function is only valid for the PS interrupt.

R/W-Register 0x43

Bit	7	6	5	4	3	2	1	0				
	Rese	erved (r	ead on	ly)		Persis	stence					
default	0000				0001 Interrupt state	us is updated after e	each measurement					
					0000 Interrupt becomes active after each measurement							
					(The mode indicate	es that a PS or ALS	measurement has b	een finished and				
					can be read via the	e register. It is indep	endent of the ALS &	PS measurement				
					value and threshol	d settings)						
					0001 Interrupt stat	us is updated after e	each measurement					
					(The interrupt statu	is is updated indepe	endently after each r	neasurement.				
					Active or Inactive s	status of the interrup	t is depending on th	e values of the last				
					measurement in co	ombination with the i	interrupt settings: "ir	iterrupt mode"				
_					(register 0x4A) and	d "thresholds" registe	er 0x4C and followir	ıg.)				
					0010 Interrupt state	us is updated if two	consecutive thresho	old judgement are				
					the same							
					(The interrupt statu	is only changes if th	e interrupt judgeme	nt of 2 consecutive				
					measurement resu	Its are the same and	d different to the cur	rent interrupt				
					status.)							
					0011 1111 Inter	rupt status is update	ed if threshold judge	ment are the				
					same over consect	utive set times (3	15)					
					(This is the same p	procedure like in the	0010 persistence m	ode, but instead				
					of 2 consecutive th	reshold judgments r	more are needed (3	to 15 depending				
					on the setting) to c	hange the interrupt	status.)					
					e.g.:							
					1010: 10 measurer	ment results in a rov	v need to fulfill the ir	nterrupt judgement				
					to update the inter	rupt status						



PS_DATA_LSBs Register (0x44)

LSB of the PS output.

R-Register 0x44

Bit	7	6	5	4	3	2	1	0
	27	26	25	24	2 ³	2 ²	2 ¹	20
default	0	0	0	0	0	0	0	0

PS_DATA_MSBs Register (0x45)

MSB of the PS output.

R-Register 0x45

Bit	7	6	5	4	3	2	1	0
	not used	not used	not used	not used	2 ¹¹	210	2 ⁹	2 ⁸
default	0	0	0	0	0	0	0	0



ALS_VIS_DATA_LSBs Register (0x46)

LSB of the ALS_VIS output.

R-Register 0x46

Bit	7	6	5	4	3	2	1	0
	27	26	25	24	2 ³	2 ²	2 ¹	20
default	0	0	0	0	0	0	0	0

ALS_VIS_DATA_MSBs Register (0x47)

MSB of the ALS_VIS output.

R-Register 0x47

-			-					
Bit	7	6	5	4	3	2	1	0
	2 ¹⁵	214	2 ¹³	2 ¹²	2 ¹¹	210	2 ⁹	2 ⁸
default	0	0	0	0	0	0	0	0

ALS_IR_DATA_LSBs Register (0x48)

LSB of the ALS_IR output.

R-Register 0x48

Bit	7	6	5	4	3	2	1	0
	27	26	2 ⁵	24	2 ³	2 ²	2 ¹	20
default	0	0	0	0	0	0	0	0

ALS_IR_DATA_MSBs Register (0x49)

MSB of the ALS_IR output.

R-Register 0x49

Bit	7	6	5	4	3	2	1	0
	2 ¹⁵	2 ¹⁴	2 ¹³	2 ¹²	2 ¹¹	2 ¹⁰	2 ⁹	2 ⁸
default	0	0	0	0	0	0	0	0

INTERRUPT_CONTROL register (0x4A)

Setting of the interrupt functions.

R/W-Register 0x4A

$\begin{array}{c c c c c c c } \hline Bit & 7 & 6 & 5 & 4 & 3 & 2 & 1 & 0 \\ \hline PS INT & ALS INT & status (read only) & status (read only) & only) & 0 & PS INT mode & INT assert & 0 & INT is latched & 0 & 0 &$	10/00/1	logiotor ox ir									
PS INT ALS INT PS INT mode INT assert INT latch INT trigger status (read only) status (read only) status (read only) 0 ingly 0 <th>Bit</th> <th>7</th> <th>6</th> <th>5</th> <th>4</th> <th>3</th> <th>2</th> <th>1</th> <th>0</th>	Bit	7	6	5	4	3	2	1	0		
status (read only) mean status (read only) mean status (read only) mean Mean <th< th=""><td></td><td>PS INT</td><td>ALS INT</td><td>PS INT</td><td>mode</td><td>INT assert</td><td>INT latch</td><td>INT tr</td><td>igger</td></th<>		PS INT	ALS INT	PS INT	mode	INT assert	INT latch	INT tr	igger		
only		status (read	status (read								
default 0 inactive 0 inactive 00 PS_TH is only active 0 INT "L" is stable 0 INT is latched 00 inactive 00 inactive 0 inactive 0 inactive 00 PS_TH (PS high threshold 0 x4B & 0x4C) is only active 0 INT "L" is stable if newer measurement results is also interrupt active 0 INT is latched until INT registers is read or initialize 00 INT pin is inactive 1 active 1 active 01 PS_TH & PS_TL (PS high & low threshold) are active as hysteresis 1 INT "L" is de-assert and re-assert if newer measurement results is also interrupt active 1 INT is updated after each measurement 01 is triggered by PS only 1 PS_TH & PS_TL (PS high & low threshold) are active as outside detection 10 PS_TH & PS_TL (PS high & low threshold) are active as outside detection 10 triggered by ALS only 11 forbidden 11 forbidden 11 forbidden 11 triggered by PS or ALS		only)	only)								
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1 active 01 PS_TH & PS_TL (PS high & l NT "L" is de-assert and re-assert if newer measurement results is also interrupt active 1 INT is updated after each measurement each measurement is also interrupt active 01 is triggered by PS only 1 INT is updated after each measurement results is also interrupt active 10 PS_TH & PS_TL (PS high & low threshold) are active as outside detection 10 PS_TH & PS_TL (PS high & low threshold) are active as outside detection 10 triggered by ALS only 1 INT is updated after each measurement results is also interrupt active 10 triggered by ALS only 10 triggered by ALS only 1 INT is updated after each measurement results is also interrupt active 10 regered by ALS only 11 triggered by ALS only 1 INT is updated after each measurement results is also interrupt active 11 INT is updated after each measurement results is also interrupt active 10 triggered by ALS only 1 INT is updated after each measurement results is also interrupt active 11 triggered by ALS only 11 triggered by PS or ALS						active	initialize				
1 active 1 active low threshold) are active as hysteresis newer measurement results is also interrupt active each measurement by PS only Image: Part of the part of th				01 PS_TH & PS_	_TL (PS high &	1 INT "L" is de-assert and re-assert if	1 INT is updated after	01 is trie	ggered		
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10 PS_TH & PS_TL (PS high & 10 triggered low threshold) are active as by ALS only outside detection 11 triggered 11 forbidden 11 triggered by PS or ALS				hysteresis		interrupt active					
low threshold) are active as by ALS only outside detection 11 forbidden 11 forbidden 11 triggered by PS or ALS				10 PS_TH & PS_	_TL (PS high &			10 trigg	ered		
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11 forbidden 11 triggered by PS or ALS				outside detection	1						
by PS or ALS				11 forbidden				11 trigg	ered		
								by PS o	or ALS		

PS INT and ALS INT status (bit <7;6>): Directly after reading the register the interrupt status for PS and ALS and the INT Pin of the sensor is automatically set back to inactive status independent on the measurement results.

PS INT mode (bit <5;4>): The INT modes are only valid for the PS interrupt function. For description please see extra chapter "PS INT Modes" (at the end of the register chapter).

INT assert (bit <3>): Is used to adjust the sensor behavior to the used micro controller trigger settings. In case a repeated trigger in low state is needed the INT assert can be set to 1.

INT trigger (bit <2>): defines the source / sources for the interrupt.

INT latched (bit <1>): In latched mode the interrupt status stays active after the first activation. It is only released by reading the status are performing an interrupt reset.



PS_TH_LSBs register (0x4B)

LSB for the PS threshold "HIGH".

R/W-Register 0x4B

Bit	7	6	5	4	3	2	1	0
	27	26	25	24	2 ³	2 ²	2 ¹	20
default	1	1	1	1	1	1	1	1

PS_TH_MSBs register (0x4C)

MSB for the PS threshold "HIGH".

R/W-Register 0x4C

Bit	7	6	5	4	3	2	1	0
					2 ¹¹	2 ¹⁰	2 ⁹	2 ⁸
default	0	0	0	0	1	1	1	1

PS_TL_LSBs register (0x4D)

LSB for the PS threshold "LOW".

R/W-Register 0x4D

0								
Bit	7	6	5	4	3	2	1	0
	27	26	2 ⁵	24	2 ³	2 ²	2 ¹	20
default	0	0	0	0	0	0	0	0

PS_TL_MSBs register (0x4E)

MSB for the PS threshold "LOW".

R/W-Register 0x4E

Bit	7	6	5	4	3	2	1	0
					2 ¹¹	2 ¹⁰	2 ⁹	2 ⁸
default	0	0	0	0	0	0	0	0

ALS_VIS_TH_LSBs register (0x4F)

LSB for the ALS_VIS threshold "HIGH".

R/W-Register 0x4F

Bit	7	6	5	4	3	2	1	0
	27	26	25	24	2 ³	2 ²	2 ¹	20
default	1	1	1	1	1	1	1	1

ALS_VIS_TH_MSBs register (0x50)

MSB for the ALS_VIS threshold "HIGH".

R/W-Register 0x50

Bit	7	6	5	4	3	2	1	0
	2 ¹⁵	2 ¹⁴	2 ¹³	2 ¹²	2 ¹¹	2 ¹⁰	2 ⁹	2 ⁸
default	1	1	1	1	1	1	1	1

ALS_VIS_TL_LSBs register (0x51)

LSB for the ALS_VIS threshold "LOW".

R/W-Register 0x51

Bit	7	6	5	4	3	2	1	0
	27	26	25	24	2 ³	2 ²	2 ¹	20
default	0	0	0	0	0	0	0	0

ALS_VIS_TL_MSBs register (0x52)

MSB for the ALS_VIS threshold "LOW".

R/W-Register 0x52

Bit	7	6	5	4	3	2	1	0
	2 ¹⁵	2 ¹⁴	2 ¹³	2 ¹²	2 ¹¹	2 ¹⁰	2 ⁹	2 ⁸
default	0	0	0	0	0	0	0	0



INT modes

The Interrupt function compares ALS and PS measurement values with the current interrupt threshold level. PS and ALS_VIS Interrupt status is readable via register 0x4A or at the INT pin of the sensor.

The Interrupt persistence function is only valid for PS measurements and is defined in register (0x43). The INT pin of the SFH 7771 is open drain output and should be pulled up to $V_{INT,pullup}$ by an external resistor. When V_{DD} is supplied the INT pin is high impedance (inactive). The INT status becomes inactive by writing INT reset command, reading the INT status register or performing a software reset. The INT status stays in its last state when the sensor is set to the standby mode. In the INT active state "low" the sensor consumes ~25µA extra current. Therefore OSRAM recommends to set the INT state to high impedance before setting the sensor in standby mode.

Following ALS and PS INT modes are described for the unlatched mode. In latched mode the switching back to the "inactive" INT state is depending on a interrupt reset or the read of the INT status register.

ALS INT mode:

The ALS_VIS threshold levels high (register 0x4F & 0x50) and low (register 0x4F & 0x50) are only valid for the ALS_VIS measurement values. The ALS_VIS INT mode is fixed and can not be adapted via register. The thresholds define a window with following functionality:

ALS INT is active, if the ALS_VIS measurement values are outside the window.

ALS INT is inactive, if the ALS_VIS measurement results are inside the window.

PS INT Modes: Bit <5;4> of INTERRUPT_CONTROL register (0x4A)

00 PS_TH is only active:

The INT state is active it the PS measurement result is equal or higher than the set PS_TH high threshold. The INT state is inactive, if the PS measurement result is lower than the set PS_TH high threshold.



01 PS_TH & PS_TL (PS high & low threshold) are active as hysteresis:

PS_TH and PS_TL are working as a hysteresis. If the PS measurement signal is higher than the PS high threshold (PS_TH) the INT state is switched to active. If the PS measurement signal is lower than the PS low threshold (PS_TL) the INT state is inactive. If once interrupt signal becomes active, INT status is kept active until measurement result becomes less than PS_TL register value.





10 PS_TH & PS_TL (PS high & low threshold) are active as outside detection:

In case of "PS outside detection" mode interrupt signal inactive means that measurement result is within registered threshold level and interrupt signal active means measurement result is out of registered threshold level.





Dimensional Drawing ³⁾



general tolerance ± 0.1 lead finish Au 2222

C63062-A4201-A1-03

Further Information:

Approximate Weight: 6.0 mg



Recommended Solder Pad ³⁾





Reflow Soldering Profile





Profile Feature	Symbol	Pb	Unit		
		Minimum	Recommendation	Maximum	
Ramp-up rate to preheat ^{*)} 25 °C to 150 °C			2	3	K/s
Time t _s T _{Smin} to T _{Smax}	t _s	60	100	120	S
Ramp-up rate to peak ^{*)} T_{Smax} to T_{P}			2	3	K/s
Liquidus temperature	TL		217		°C
Time above liquidus temperature	t		80	100	S
Peak temperature	Τ _Ρ		245	260	°C
Time within 5 °C of the specified peak temperature T_P - 5 K	t _P	10	20	30	S
Ramp-down rate* T _P to 100 °C			3	6	K/s
Time 25 °C to T _P				480	S

All temperatures refer to the center of the package, measured on the top of the component * slope calculation DT/Dt: Dt max. 5 s; fulfillment for the whole T-range

SFH 7771 DATASHEET



Taping ³⁾



C63062-A4201-B6 -03



Tape and Reel ⁴⁾



Reel Dimensions

А	W	N _{min}	W ₁	$W_{2\text{max}}$	Pieces per PU
180 mm	8 + 0.3 / - 0.1 mm	60 mm	8.4 + 2 mm	14.4 mm	4000



Barcode-Product-Label (BPL)



Dry Packing Process and Materials ³⁾





Notes

Subcomponents of this device contain, in addition to other substances, metal filled materials including silver. Metal filled materials can be affected by environments that contain traces of aggressive substances. Therefore, we recommend that customers minimize device exposure to aggressive substances during storage, production, and use. Devices that showed visible discoloration when tested using the described tests above did show no performance deviations within failure limits during the stated test duration. Respective failure limits are described in the IEC60810.

For further application related information please visit www.osram-os.com/appnotes



Disclaimer

Attention please!

The information describes the type of component and shall not be considered as assured characteristics. Terms of delivery and rights to change design reserved. Due to technical requirements components may contain dangerous substances.

For information on the types in question please contact our Sales Organization.

If printed or downloaded, please find the latest version on our website.

Packing

Please use the recycling operators known to you. We can also help you – get in touch with your nearest sales office. By agreement we will take packing material back, if it is sorted. You must bear the costs of transport. For packing material that is returned to us unsorted or which we are not obliged to accept, we shall have to invoice you for any costs incurred.

Product and functional safety devices/applications or medical devices/applications

Our components are not developed, constructed or tested for the application as safety relevant component or for the application in medical devices.

Our products are not qualified at module and system level for such application.

In case buyer – or customer supplied by buyer – considers using our components in product safety devices/ applications or medical devices/applications, buyer and/or customer has to inform our local sales partner immediately and we and buyer and /or customer will analyze and coordinate the customer-specific request between us and buyer and/or customer.



Glossary

- ¹⁾ **Typical Values:** Due to the special conditions of the manufacturing processes of semiconductor devices, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.
- ²⁾ **Testing temperature:** TA = 25°C (unless otherwise specified)
- ³⁾ **Tolerance of Measure:** Unless otherwise noted in drawing, tolerances are specified with ±0.1 and dimensions are specified in mm.
- ⁴⁾ **Tape and Reel:** All dimensions and tolerances are specified acc. IEC 60286-3 and specified in mm.



Revision History

Version	Date	Change
1.2	2022-11-17	New Layout Discontinued

Discontinued



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