

RQ3L090GN

Nch 60V 30A Power MOSFET

V_{DSS}	60V
R _{DS(on)} (Max.)	13.9mΩ
I _D	±30A
P_D	20W

Features

- 1) Low on resistance
- 2) Small Surface Mount Package
- 3) Pb-free plating; RoHS compliant
- 4) 100% Rg and UIS tested

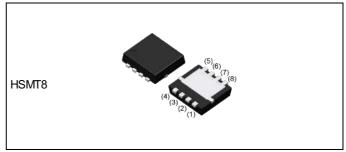
Application

Switching

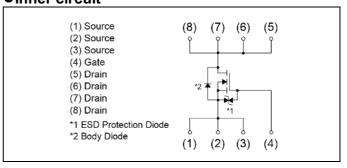
Moter drives

DC/DC converter

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Type	Tape width (mm)	12
	Quantity (pcs)	3000
	Taping code	ТВ
	Marking	L090GN

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V _{DSS}	60	V	
Continuous dusin summent	T _c = 25°C	I _D *1	±30	Α
Continuous drain current	T _a = 25°C	I _D	±9	Α
Pulsed drain current	l _{DP} *2	±36	Α	
Gate - Source voltage	V_{GSS}	±20	V	
Avalanche current, single pulse		I _{AS} *3	9	Α
Avalanche energy, single pulse		E _{AS} *3	12	mJ
Dower dissinction		P _D *1	20	W
Power dissipation		P _D *4	2.0	W
Junction temperature	T _j	150	°C	
Operating junction and storage te	T _{stg}	-55 to +150	°C	

●Thermal resistance

Doromotor	Curah al		Values		
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	-	6.2	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	62.5	°C/W

● Electrical characteristics (T_a = 25°C)

Davanastan	Cymaele ed	Conditions	Values			Unit	
Parameter	Symbol	Symbol Conditions –		Тур.	Max.	Uriit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1 \text{mA}$ referenced to 25°C		-	60	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V	-	-	1	μA	
Gate - Source leakage current	I _{GSS}	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 300 \mu A$	1.3	-	2.7	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	-5.6	-	mV/°C	
Static drain - source	D *5	V _{GS} = 10V, I _D = 9A	-	10.3	13.9	mO.	
on - state resistance	R _{DS(on)} *5	V _{GS} = 4.5V, I _D = 9A	-	14.6	21.4	mΩ	
Gate resistance	R_{G}	f=1MHz, open drain	-	0.7	-	Ω	
Forward Transfer Admittance	Y _{fs} *5	V _{DS} = 5V, I _D = 9A	9.5	-	-	S	

^{*1} T_c = 25°C, Limited only by maximum temperature allowed.



^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \simeq 0.2mH, V_{DD} = 30V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Mounted on a Cu board (40×40×0.8mm)

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C)

Daramatar	Symbol Conditions		Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	UIIIL	
Input capacitance	C _{iss}	V _{GS} = 0V	-	1260	-		
Output capacitance	C _{oss}	V _{DS} = 30V	-	245	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	65	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 30V, V_{GS} = 10V$	1	16	-		
Rise time	t r*5	I _D = 4.5A	1	14	1	no	
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 6.7\Omega$		41	_	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	14	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Daramatar	Cymahal	Canditions		Values			l lait
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Total gate charge	O *5		V _{GS} = 10V	-	24.5	-	
Total gate charge	Q_g^{*5}	V _{DD} ≃ 30V		-	13.0	-	
Gate - Source charge	Q _{gs} *5	I _D = 9A	V _{GS} = 4.5V	-	5.3	-	nC
Gate - Drain charge	Q _{gd} *5			-	4.7	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Darameter	Parameter Symbol Conditions		Values			Unit	
Parameter			Min.	Тур.	Max.	Offic	
Continuous forward current	I _S	T = 25°C	1	-	1.67	Α	
Pulse forward current	I _{SP} *2	T _a = 25°C	-	-	36	Α	
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 1.67A	-	-	1.2	V	
Reverse recovery time	t _{rr} *5	I _S = 9A, V _{GS} =0V	-	31	-	ns	
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	30	-	nC	

Fig.1 Power Dissipation Derating Curve

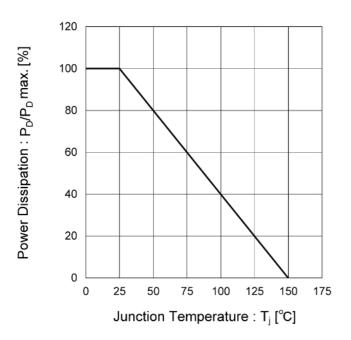
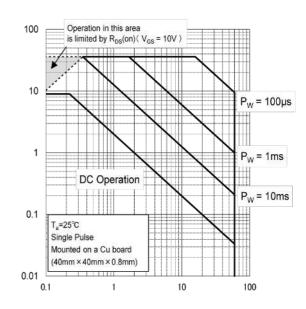


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

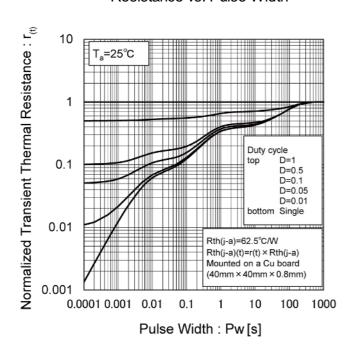
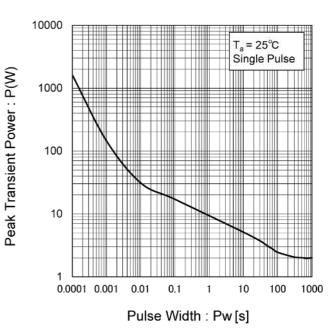


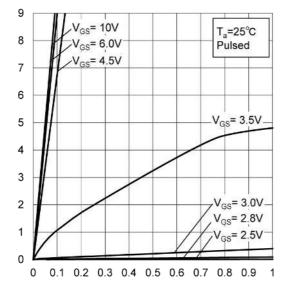
Fig.4 Single Pulse Maximum Power Dissipation



Drain Current : I_D [A]

• Electrical characteristic curves

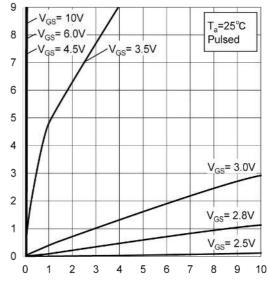
Fig.5 Typical Output Characteristics(I)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

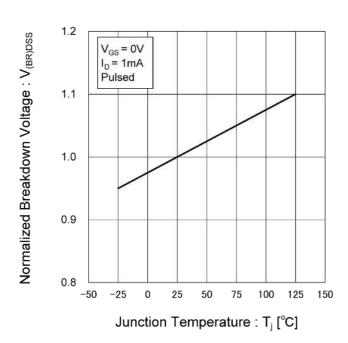


Fig.8 Typical Transfer Characteristics

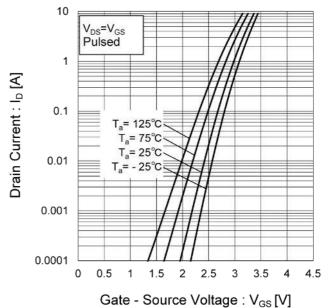


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

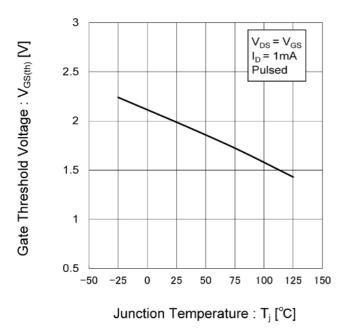


Fig.10 Forward Transfer Admittance vs.
Drain Current

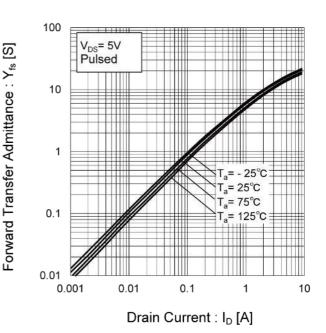


Fig.11 Drain Current Derating Curve

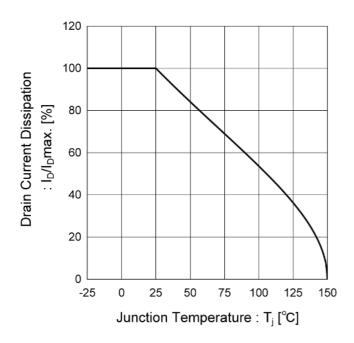
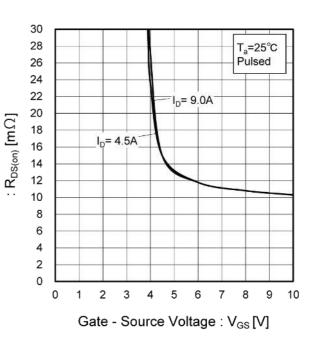


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Static Drain - Source On-State Resistance

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

20 Static Drain - Source On-State Resistance 19 18 V_{GS} = 10V 17 Pulsed 16 15 14 13 $: R_{\mathsf{DS}(\mathsf{on})} \left[\mathsf{m} \Omega \right]$ 12 11 10 9 8 7 6 5 4 3 2 $I_D = 9A$ 0 0 50 -50 -2525 75 100 125 150 Junction Temperature : T_i [°C]

Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

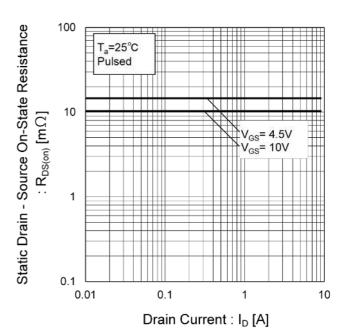


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

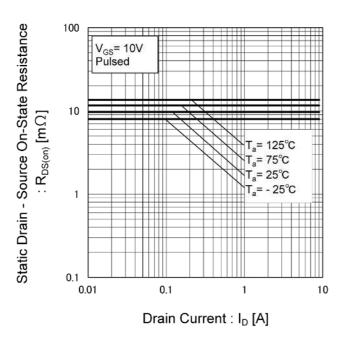
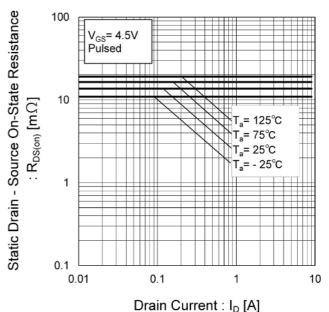


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



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Fig.17 Typical Capacitances vs. Drain - Source Voltage

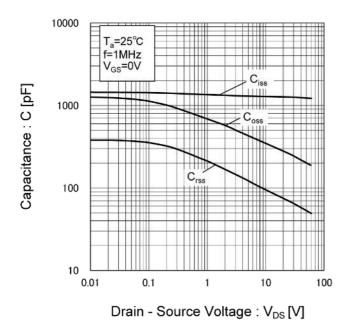


Fig.18 Switching Characteristics

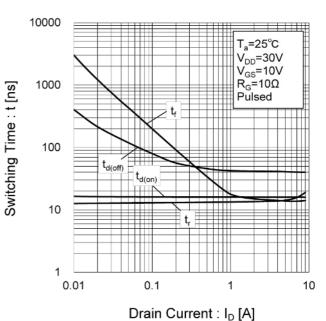


Fig.19 Typical Gate Charge

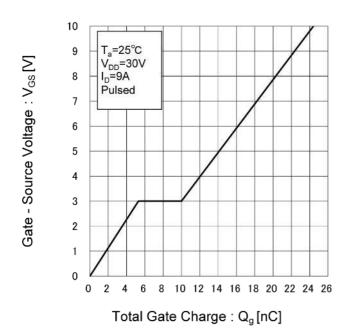
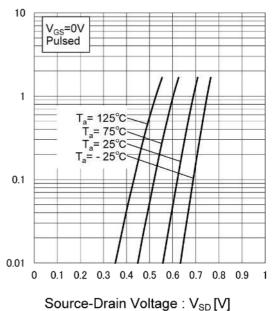


Fig.20 Source Current vs. Source Drain Voltage



Source Current : Is [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

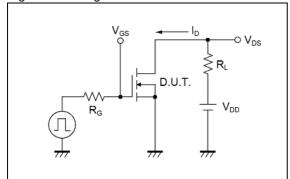


Fig.1-2 Switching Waveforms

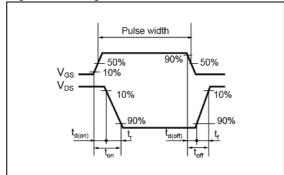


Fig.2-1 Gate Charge Measurement Circuit

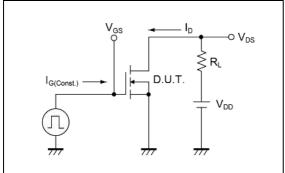


Fig.2-2 Gate Charge Waveform

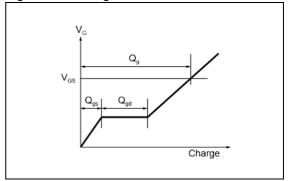


Fig.3-1 Avalanche Measurement Circuit

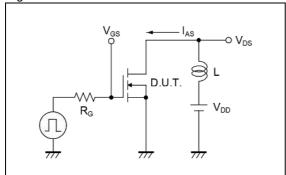
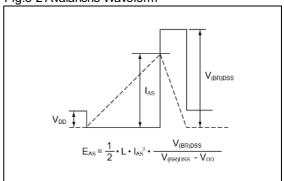


Fig.3-2 Avalanche Waveform



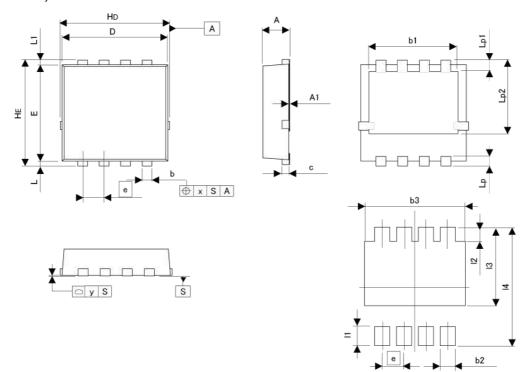
Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions

HSMT8

(3.3x3.3)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.70	0.90	0.028	0.035
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
b1	2.50	2.70	0.098	0.106
С	0.10	0.30	0.004	0.012
D	3.10	3.30	0.122	0.130
E	2.90	3.10	0.114	0.122
е	0.	65	0.026	
HD	3.20	3.40	0.126	0.134
HE	3.20	3.40	0.126	0.134
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.20	0.40	0.008	0.016
Lp1	0.25	0.45	0.010	0.018
Lp2	2.20	2.40	0.087	0.094
x	-	0.10	-	0.004
у	((+))	0.10	-	0.004

DIM	MILIME	MILIMETERS		HES
DIN	MIN	MAX	MIN	MAX
b2	122	0.43	9	0.017
b3	275	3.00	-	0.118
I1	(=)	0.60	-	0.024
12	(a)	0.45		0.018
13	(4 4)	2.45	7	0.096
14	8 3 8	3.70	" <u>=</u>	0.146

Dimension in mm/inches



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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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