

SURFACE MOUNT DUAL, IN SERIES HIGH VOLTAGE SILICON SWITCHING DIODES



Central semiconductor Corp.

www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPD2005S and CMPD2005SG each contain two (2) High Voltage Silicon Switching Diodes, manufactured by the epitaxial planar process, epoxy molded in a SOT-23 surface mount package, designed for applications requiring high voltage capability.

MARKING CODES: CMPD2005S: DB5 CMPD2005SG*: 5SG

* Device is *Halogen Free* by design

MAXIMUM RATINGS: (T _A =25 °C)	SYMBOL		UNITS
Continuous Reverse Voltage	VR	300	V
Peak Repetitive Reverse Voltage	V _{RRM}	350	V
Average Forward Current	Ι _Ο	200	mA
Continuous Forward Current	۱ _F	225	mA
Peak Repetitive Forward Current	IFRM	625	mA
Peak Forward Surge Current, tp=1.0µs	IFSM	4.0	А
Peak Forward Surge Current, tp=1.0s	IFSM	1.0	А
Power Dissipation	PD	350	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	357	°C/W

ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _R	V _R =280V		100	nA
I _R	V _R =280V, T _A =150°C		100	μA
вv _R	I _R =100μΑ	350		V
VF	I _F =20mA		0.87	V
VF	I _F =100mA		1.0	V
VF	I _F =200mA		1.25	V
с _Т	V _R =0, f=1.0MHz		5.0	pF
t _{rr}	$I_R=I_F=30$ mA, Rec. to 3.0mA, RL=100	Ω	50	ns

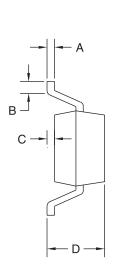
R3 (25-January 2010)

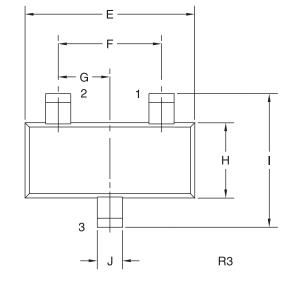




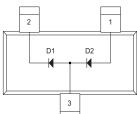
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SOT-23 CASE - MECHANICAL OUTLINE





PIN CONFIGURATION



LEAD CODE: 1) Anode D2 2) Cathode D1 3) Anode D1, Cathode D2

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MIN	HES MAX		ETERS
	MAX	N 41N L	
	1111 0 (MIN	MAX
0.003	0.007	0.08	0.18
0.006	-	0.15	-
-	0.005	-	0.13
0.035	0.043	0.89	1.09
0.110	0.120	2.80	3.05
0.075		1.90	
0.037		0.95	
0.047	0.055	1.19	1.40
0.083	0.098	2.10	2.49
0.014	0.020	0.35	0.50
	- 0.035 0.110 0.0 0.0 0.047 0.083	0.006 - - 0.005 0.035 0.043 0.110 0.120 0.075 0.037 0.047 0.055 0.083 0.098	0.006 - 0.15 - 0.005 - 0.035 0.043 0.89 0.110 0.120 2.80 0.075 1. 0.037 0. 0.047 0.055 1.19 0.083 0.098 2.10

SOT-23 (REV: R3)

R3 (25-January 2010)

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PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

Special wafer diffusions

· Custom product packing

- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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