



Small Plastic Package, Dual SPDT Analog Switch with -1.5V Signal Support for AC Coupled Audio Signals and D-Class Audio Signals

#### **Features**

- CMOS Technology for Bus and Analog Applications
- Low On-Resistance: 0.6Ω.
- Wide VDD Range: 2.5V to 4.2V
- High Off Isolation: -80dB @ 100kHz
- Crosstalk Rejection Reduces Signal Distortion: 72dB @ 100kHz
- Input signals can be from -1.5V up to VDD without distortion
- Break-Before-Make Switching
- Extended Industrial Temperature Range: -40°C to 85°C
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

- Packaging (Pb-free & Green):
  - $\circ$  10-contact UQFN (ZM10) 1.4 × 1.8

### Application(s)

- Cell Phones
- PDAs
- MP3 players
- Portable Instrumentation
- Computer Peripherals
- Speaker Headset Switching
- Power Routing
- Relay Replacement
- Audio and Video Signal Routing
- PCMCIA Cards
- Modems

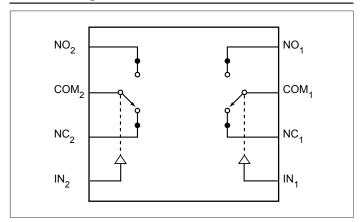
### **Description**

The DIODES<sup>™</sup> PI3A268C is a dual, fast single-pole double throw (SPDT) CMOS switch. It can be used as an analog switch or as a low-delay bus switch.

Break-before-make switching prevents both switches being enabled simultaneously. This eliminates signal disruption during switching.

With the use of 3rd party headsets, AC coupling is required to protect against EOS damage caused by DC offsets. The PI3A268C can support these AC coupled audio signals, since the switch can tolerate signals down to -1.5V without a negative power supply.

### **Block Diagram**



#### **Function Table**

Logic Input (IN <sub>X</sub> )	Function
0	NC <sub>X</sub> Connected to COM <sub>X</sub>
1	NO <sub>X</sub> Connected to COM <sub>X</sub>

Note: x = 1 or 2

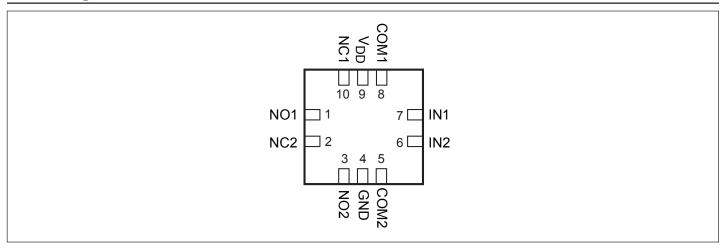
#### Notes:

- $1.\ No\ purposely\ added\ lead.\ Fully\ EU\ Directive\ 2002/95/EC\ (RoHS),\ 2011/65/EU\ (RoHS\ 2)\ \&\ 2015/863/EU\ (RoHS\ 3)\ compliant.$
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





# **Pin Configuration**



# **Pin Description**

Pin #	Pin Name	Description
1, 3	$NO_X$	Data Port (Normally open)
4	GND	Ground
2, 10	NCX	Data Port (Normally closed)
5, 8	$COM_X$	Common Output / Data Port
9	$V_{\mathrm{DD}}$	Positive Power Supply
6, 7	IN <sub>X</sub>	Logic Control





# Absolute Maximum Ratings(1)

$ \begin{array}{llllllllllllllllllllllllllllllllllll$	7
Continuous Current NO_NC_COM±300mA Peak Current NO_NC_COM_ (pulsed at 1ms 50% duty cycle)±400mA	
Peak Current NO_NC_COM_ (pulsed at 1ms 10% duty cycle)±500mA	١
Storage Temperature Range (T <sub>STG</sub> )65°C to +150°C Junction Temperature under Bias (T <sub>J</sub> )150°C Junction Lead Temperature (T <sub>L</sub> )	
(Soldering, 10 seconds)260°C	7

## Recommended Operating Conditions<sup>(3)</sup>

Supply Voltage Operating (V <sub>DD</sub> ) 2.5V to 4.2V
Thermal Resistance ( $\theta_{JA}$ )
Bump Temperature (soldering notes) Infared (15s)+220°C Vapor Phase (60ns)+215°C

#### **Notes:**

- 1. "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied.
- 2. The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.
- 3. Control input must be held HIGH or LOW; it must not float.

### **Power Supply**

Symbol	Parameter	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
I <sub>CC</sub>		$V_{DD} = 2.7V$ , $V_{IN} = 0V$ or $V_{DD}$			20	
		$V_{DD} = 3.3V$ , $V_{IN} = 0V$ or $V_{DD}$			36	μΑ
		$V_{DD} = 4.2V$ , $V_{IN} = 0V$ or $V_{DD}$			80	

### **DC Electrical Characteristics**

 $V_{DD} = 2.5V \text{ to } 2.7V \pm 10\%$ 

 $(T_A = -40^{\circ}\text{C to }85^{\circ}\text{C}, \text{ unless otherwise noted. Typical values are at }25^{\circ}\text{C}.)$ 

Symbol	Parameter	<b>Test Conditions</b>	Min.	Тур.	Max.	Units		
Analog Switch								
V <sub>NO</sub> , V <sub>NC</sub> , V <sub>COM</sub>	Analog Signal Range		-1.5		V <sub>DD</sub>	V		
R <sub>ON(NC)</sub>	NC On-Resistance	$V_{DD} = 2.25V, I_{COM} = 100mA,$ $V_{NC} = -1.5V \text{ to } V_{DD}$		0.9				
R <sub>ON(NO)</sub>	NO On-Resistance	$V_{DD} = 2.25V, I_{COM} = 100mA,$ $V_{NO} = -1.5V \text{ to } V_{DD}$		0.9				
$\Delta R_{ m ON}$	On-Resistance Match Between Channels	$V_{DD} = 2.25V, I_{COM} = 100mA,$ $V_{NO}$ or VNC = -1.5V to $V_{DD}$		0.1		Ω		
R <sub>ONF(NC)</sub>	NC On-Resistance Flatness	$V_{DD} = 2.25V, I_{COM} = 100mA,$ $V_{NC} = -1.5V \text{ to } V_{DD}$		0.25				
R <sub>ONF(NO)</sub>	NO On-Resistance Flatness	$V_{DD} = 2.25 \text{V}, I_{COM} = 100 \text{mA}, $ $V_{NO} = -1.5 \text{V to } V_{DD}$		0.25				
I <sub>OFF (NO)</sub> or I <sub>OFF (NC)</sub>	NO or NC Off Leakage Current	$V_{DD} = 2.25 \text{V}, V_{NO} \text{ or } V_{NC} = -1.5 \text{V to} +3.3 \text{V}$	-400		400	nA		





### $V_{DD} = 2.5 V$ to $2.7 V \pm 10\%$ Cont.

Symbol	Parameter	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
I <sub>COM (ON)</sub>	COM On Leakage Current	$V_{DD}$ = 2.25V, $V_{NO}$ or $V_{NC}$ = 0.3V, $V_{COM}$ = 3V, 0.3V, or floating	-250		250	nA
	Total Harmonic Distortion	Load = $8\Omega$ pulled to GND, $V_{DD} = 2.7V$ , freq = $20$ Hz to $20$ KHz, $V_{DD} = 2V_{PP}$		0.035		%
THD	Total Harmonic Distortion	Load = $16\Omega$ pulled to GND, $V_{DD} = 2.7V$ , freq = $20$ Hz to $20$ KHz, Vinput = $2V_{PP}$		0.025		<b>%</b> 0
Digital I/O						
V <sub>IH</sub>	Input Logic High		1.3			V
V <sub>IL</sub>	Input Logic Low				0.6	V
$V_{\mathrm{H}}$	Input Hysteresis	$V_{DD} = 2.7V$		100		mV
I <sub>IN</sub>	IN Input Leakage Current	$V_{IN} = 0$ or $V_{DD}$	-0.5		0.5	μΑ

### $V_{DD} = 2.7V$ to 3.3V

 $(T_A = -40^{\circ}\text{C to }85^{\circ}\text{C}, \text{ unless otherwise noted. Typical values are at }25^{\circ}\text{C}.)$ 

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Analog Swite	ch		·			'
V <sub>NO</sub> , V <sub>NC</sub> , V <sub>COM</sub>	Analog Signal Range		-1.5		$V_{\mathrm{DD}}$	V
R <sub>ON(NC)</sub>	NC On-Resistance	$V_{DD} = 2.7V, I_{COM} = 100mA,$ $V_{NC} = -1.5V \text{ to } V_{DD}$		0.7		
R <sub>ON(NO)</sub>	NO On-Resistance	$V_{DD} = 2.7V, I_{COM} = 100mA,$ $V_{NO} = -1.5V \text{ to } V_{DD}$		0.7		Ω
$\Delta R_{ m ON}$	On-Resistance Match Between Channels	$V_{DD} = 2.7V$ , $I_{COM} = 100$ mA, $V_{NO}$ or $VNC = -1.5V$ to $V_{DD}$		0.1		
R <sub>ONF(NC)</sub>	NC On-Resistance Flatness	$V_{DD} = 2.7V$ , $I_{COM} = 100$ mA, $V_{NC} = -1.5V$ to $V_{DD}$		0.2		
R <sub>ONF(NO)</sub>	NO On-Resistance Flatness	$V_{DD} = 2.7V, I_{COM} = 100mA,$ $V_{NO} = -1.5V \text{ to } V_{DD}$		0.2		
I <sub>OFF (NO)</sub> or I <sub>OFF (NC)</sub>	NO or NC Off Leakage Current	$V_{DD} = 3.3V$ , $V_{NO}$ or $V_{NC} = -1.5V$ to $+3.3V$	-400		400	A
I <sub>COM (ON)</sub>	COM On Leakage Current	$V_{DD} = 3.3 \text{V}$ , $V_{NO}$ or $V_{NC} = 0.3 \text{V}$ , $V_{COM} = 3 \text{V}$ , 0.3 V, or floating	-250		250	nA
	Tatal Hamania Distantian	Load = $8\Omega$ pulled to GND, $V_{DD}$ = 2.7V, freq = 20Hz to 20KHz, Vinput = $2V_{PP}$		0.04		0/
THD	Total Harmonic Distortion	Load = $16\Omega$ pulled to GND, $V_{DD} = 2.7V$ , freq = $20$ Hz to $20$ KHz, Vinput = $2V_{PP}$		0.035		%
Digital I/O						
V <sub>IH</sub>	Input Logic High		1.3			V
$V_{\rm IL}$	Input Logic Low				0.6	v
$V_{\mathrm{H}}$	Input Hysteresis	$V_{DD} = 2.7V$		100		mV
I <sub>IN</sub>	IN Input Leakage Current	$V_{IN} = 0$ or $V_{DD}$	-0.5		0.5	μΑ





 $V_{DD} = 3.3V$  to 4.4V

 $(T_A = -40^{\circ}\text{C to }85^{\circ}\text{C}, \text{ unless otherwise noted.}$  Typical values are at 25°C.)

Symbol	Parameter	<b>Test Conditions</b>	Min.	Typ.	Max.	Units	
Analog Swite	ch					,	
V <sub>NO</sub> , V <sub>NC</sub> , V <sub>COM</sub>	Analog Signal Range		-1.5		$V_{\mathrm{DD}}$	V	
R <sub>ON(NC)</sub>	NC On-Resistance	$V_{DD} = 4.2V, I_{COM} = 100mA,$ $V_{NC} = -1.5V \text{ to } V_{DD}$		0.6			
R <sub>ON(NO)</sub>	NO On-Resistance	$V_{DD} = 4.2V, I_{COM} = 100mA,$ $V_{NO} = -1.5V \text{ to } V_{DD}$		0.6		Ω	
$\Delta R_{ m ON}$	On-Resistance Match Between Channels	$V_{DD}$ = 4.2V, $I_{COM}$ = 100mA, $V_{NO}$ or VNC = -1.5V to $V_{DD}$		0.1			
R <sub>ONF(NC)</sub>	NC On-Resistance Flatness	$V_{DD}$ = 4.2V, $I_{COM}$ = 100mA, $V_{NC}$ = -1.5V to $V_{DD}$		0.2			
R <sub>ONF(NO)</sub>	NO On-Resistance Flatness	$V_{DD} = 4.2V, I_{COM} = 100mA,$ $V_{NO} = -1.5V \text{ to } V_{DD}$		0.2			
I <sub>OFF (NO)</sub> or I <sub>OFF (NC)</sub>	NO or NC Off Leakage Current	$V_{DD} = 4.2V$ , $V_{NO}$ or $V_{NC} = -1.5V$ to $+3.3V$	-700		700	A	
I <sub>COM</sub> (ON)	COM On Leakage Current	$V_{DD} = 4.2V$ , $V_{NO}$ or $V_{NC} = 0.3V$ , $V_{COM} = 3V$ , 0.3V, or floating	-550		550	nA	
	Total Harmonic Distortion	Load = $8\Omega$ pulled to GND, $V_{DD}$ = 3.3V, freq = 20Hz to 20KHz, Vinput = $2V_{PP}$		0.025		0/	
THD	Total Harmonic Distortion	Load = $16\Omega$ pulled to GND, $V_{DD} = 3V$ , freq = $20$ Hz to $20$ KHz, Vinput = $2V_{PP}$		0.02		%	
Digital I/O							
V <sub>IH</sub>	Input Logic High		1.3			V	
V <sub>IL</sub>	Input Logic Low				0.6	v	
$V_{\mathrm{H}}$	Input Hysteresis	$V_{DD} = 4.2V$		150		mV	
I <sub>IN</sub>	IN Input Leakage Current	$V_{IN} = 0$ or $V_{DD}$	-0.5		0.5	μΑ	





### **Switch and AC Characteristics**

Symbol	Parameter	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
t <sub>ON</sub>	Turn-On Time	$V_{DD}$ = 2.5V, $V_{NO}$ or $V_{NC}$ = 1.5V, $R_L$ = 50 $\Omega$ , $C_L$ = 35pF, See Test Circuit Figure 1 & 2.			85	
t <sub>OFF</sub>	Turn-Off Time	$V_{DD}$ = 2.5V, $V_{NO}$ or $V_{NC}$ = 1.5V, $R_L$ = 50 $\Omega$ , $C_L$ = 35pF, See Test Circuit Figure 1 & 2.			85	ns
$t_{ m BBM}$	Break-Before-Make Delay	$V_{DD}$ = 2.7V, $V_{NO}$ or $V_{NC}$ = 1.5V, $R_L$ = 50 $\Omega$ , $C_L$ = 35pF, See Test Circuit Figure 3.			20	
Q	Charge Injection	See Test Circuit Figure 4.		35		pC
O <sub>IRR</sub>	Off-Isolation	$C_L = 5 p F$ , $R_L = 50 \Omega$ , $f = 100 k Hz$ , $V_{DD} = 2.5 V$ to $4.2 V$ , $V_{COM} = 1 V_{RMS}$ , See Test Circuit Figure 5.		-80		1D
X <sub>TALK</sub>	Crosstalk	$C_L = 5 p F$ , $R_L = 50 \Omega$ , $f = 100 k Hz$ , $V_{DD} = 2.5 V$ to $4.2 V$ , $V_{COM} = 1 V_{RMS}$ , See Test Circuit Figure 6.		-72		dB
f <sub>3dB</sub>	3dB Bandwidth	V <sub>DD</sub> = 2.5V to 4.2V, See Test Circuit Figure 9		100		MHz

# **Capacitance** ( $V_{DD} = 2.5V \text{ to } 4.2V$ )

Symbol	Parameter	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
C <sub>NC (OFF)</sub>	NC Off Capacitance	f = 1MHz, See Test Circuit Figure 7.		18		
C <sub>NO (OFF)</sub>	NO Off Capacitance	f = 1MHz, See Test Circuit Figure 7.		18		"E
C <sub>NC (ON)</sub>	NC On Capacitance	f = 1MHz, See Test Circuit Figure 8.		55		pF
C <sub>NO (ON)</sub>	NO On Capacitance	f = 1MHz, See Test Circuit Figure 8.		55		





## **Test Circuits and Timing Diagrams**

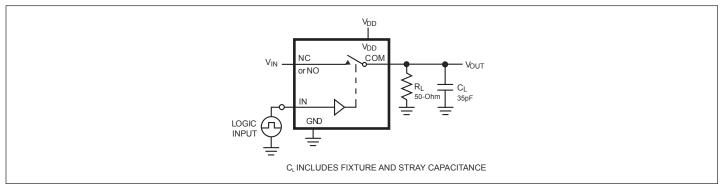


Figure 1. AC Test Circuit

#### Notes

1. Unused input (NC or NO) must be grounded.

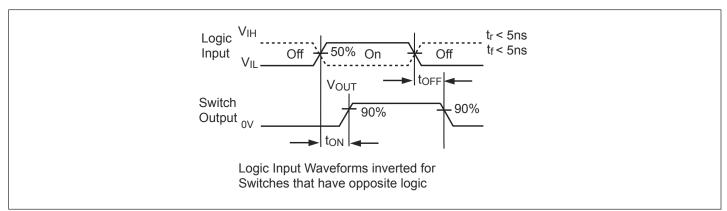


Figure 2. AC Waveforms

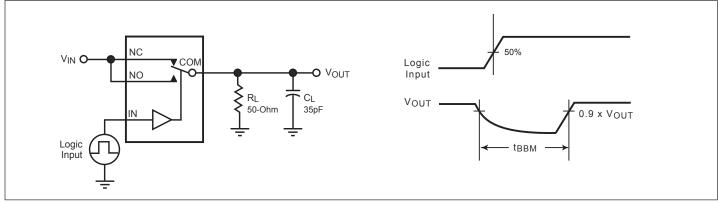


Figure 3. Break Before Make Interval Timing



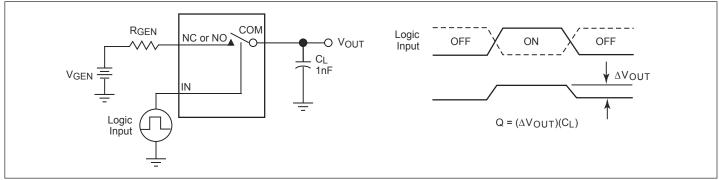
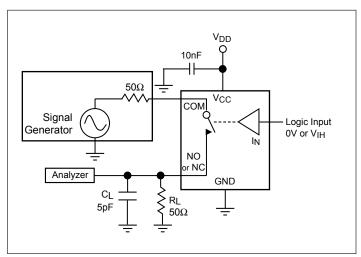


Figure 4. Charge Injection Test



 $V_{DD}$ 10nF 50Ω  $V_{DD}$ СОМ NC Signal Generator 50Ω NO Analyzer **GND** ΙN  $\mathsf{C}_\mathsf{L}$ 5pF 50Ω

Figure 5. Off Isolation

Figure 6. Crosstalk

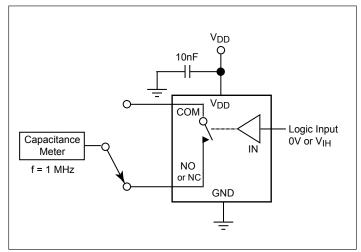


Figure 7. Channel Off Capacitance

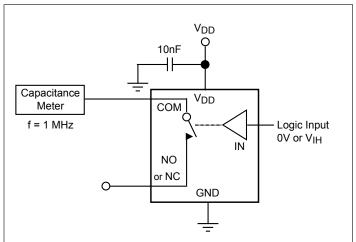


Figure 8. Channel On Capacitance





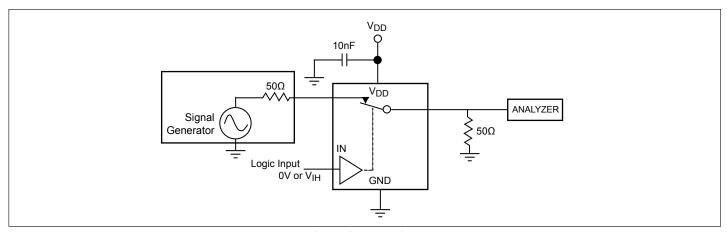
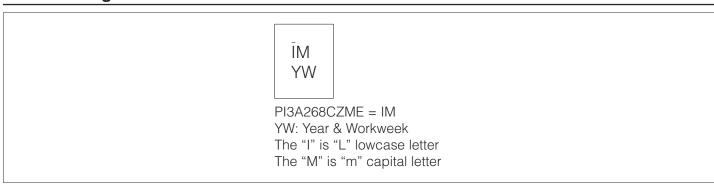


Figure 9. Bandwidth



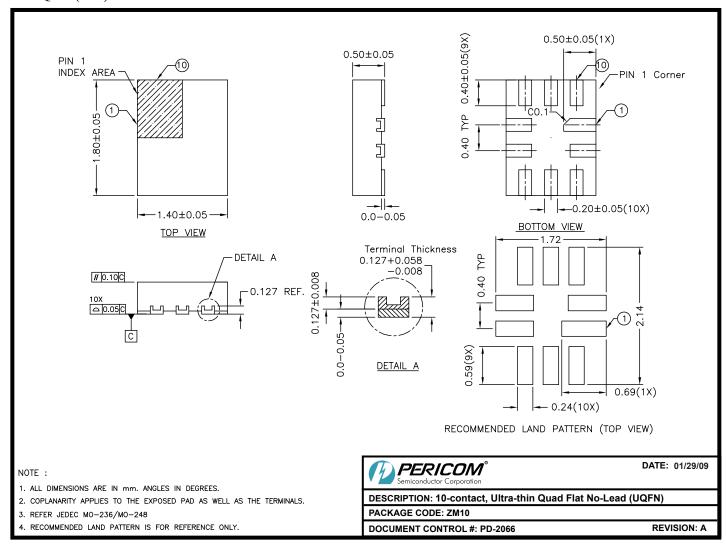






# **Packaging Mechanical**

### 10-UQFN (ZM)



09-0072

#### For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

### **Ordering Information**

<b>Ordering Code</b>	Packaging Code	Package Description	Top Mark
PI3A268CZMEX	ZM	10-contact, Ultra-thin Quad Flat No-Lead (UQFN)	BV

#### Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





#### IMPORTANT NOTICE

- 1. DIODESINCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKENO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSEORNON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes' products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.
- 9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-and-conditions/important-notice

DIODES is a trademark of Diodes Incorporated in the United States and other countries.

The Diodes logo is a registered trademark of Diodes Incorporated in the United States and other countries.

© 2022 Diodes Incorporated. All Rights Reserved.

www.diodes.com