

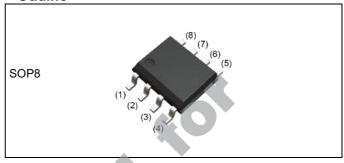
30V Nch+Pch Power MOSFET

Symbol	Tr1:Nch	Tr2:Pch
V_{DSS}	30V	-30V
R _{DS(on)} (Max.)	30mΩ	28mΩ
I _D	±6.0A	±7.0A
P_D	2.0	W

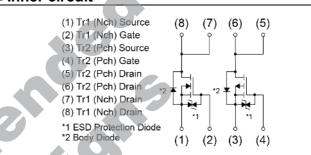
Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) AEC-Q101 Qualified

Outline



•Inner circuit



Packaging specifications

● F achai	Jing specifications	
	Packing	Embossed Tape
	Reel size (mm)	330
Type	Tape width (mm)	12
	Basic ordering unit (pcs)	2500
	Taping code	ТВ
	Marking	SP8M5

Application

Switching

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter		Cymbol	Va	lue	Unit
Falanetel		Symbol	Tr1:Nch	Tr2:Pch	Offit
Drain - Source voltage		V_{DSS}	30	-30	V
Continuous drain current		I _D	±6.0	±7.0	А
Pulsed drain current		I _{DP} *1	±24	±28	А
Gate - Source voltage		V_{GSS}	±20	±20	V
Dever discipation	total	P _D *2	2.0		10/
Power dissipation	total	P _D *3	1.4		W
Junction temperature		T _j	15	50	°C
Operating junction and storage temperature range		T _{stg}	-55 to	+150	°C

●Thermal resistance

Parameter		Cymahal		Values		I loit
		Symbol	Min.	Тур.	Max.	Unit
Thermal registance innetion, embient	total	R _{thJA} *2	-	-	62.5	°C/W
Thermal resistance, junction - ambient	เบเลเ	R _{thJA} *3	-	-	89.2	C/VV

● Electrical characteristics (T_a = 25°C)

Devenue de la	0	т	0		Values	,	Unit
Parameter	Symbol	Type	Type Conditions		Тур.	Max.	Unit
Drain - Source breakdown	V	Tr1	$V_{GS} = 0V$, $I_D = 1mA$	30	-	-	V
voltage	V _{(BR)DSS}	Tr2	$V_{GS} = 0V$, $I_D = -1mA$	-30	-	-	V
Breakdown voltage	ΔV _{(BR)DSS}	Tr1	I _D = 1mA, referenced to 25°C	-	29	ı	mV/°C
temperature coefficient	ΔT_{j}	Tr2	$I_D = -1 \text{ mA}$, referenced to 25°C	-	-20.7	-	IIIV/ C
Zero gate voltage	ı	Tr1	$V_{DS} = 30V, V_{GS} = 0V$		-	1	μA
drain current	I _{DSS}	Tr2	$V_{DS} = -30V, V_{GS} = 0V$		_	-1	μΑ
Gate - Source		Tr1	$V_{DS} = 0V$, $V_{GS} = \pm 20V$		ı	±10	
leakage current	I _{GSS}	Tr2	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	1	±10	μA
Gate threshold	V _{GS} (th)	$V_{DS} = 10V, I_{D} = 1mA$	1.0	-	2.5	V	
voltage	V GS(th)	Tr2	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	-	-2.5	V
Gate threshold voltage	$\Delta V_{GS(th)}$	Tr1	I _D = 1mA, referenced to 25°C	-	-1.6	-	mV/°C
temperature coefficient	ΔT_{j}	Tr2	I _D = -1mA, referenced to 25°C	1	3.1	ı	IIIV/ C
			$V_{GS} = 10V, I_D = 6A$	ı	21	30	
		Tr1	$V_{GS} = 4.5V, I_D = 6A$	-	30	42	
Static drain - source	D *4		$V_{GS} = 4.0V, I_D = 6A$	-	33	47	m0
on - state resistance	R _{DS(on)} *4		V _{GS} = 10V, I _D = -7A	-	20	28	mΩ
		Tr2	V_{GS} = -4.5V, I_{D} = -3.5A	-	25	35	
			V_{GS} = -4.0V, I_{D} = -3.5A	-	30	42	
Cata rasistanas	D	Tr1	f=1MUz anan drain	1	4.1	ı	
Gate resistance	R_{G}	Tr2	f=1MHz, open drain	-	3.7	-	Ω
Forward Transfer	Y _{fs} *4	Tr1	V _{DS} = 10V, I _D = 6A	4.0	•	-	
Admittance	l I tsi	Tr2	$V_{DS} = -10V, I_{D} = -3.5A$	6.0	-	-	S

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^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*2} Mounted on a ceramic board (30×30×0.8mm)

^{*3} Mounted on a FR4 (25×25×0.8mm)

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C)

<Tr1>

Parameter	Cumbal	Conditions	,	Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uniil
Input capacitance	C _{iss}	V _{GS} = 0V	1	520	-	
Output capacitance	C _{oss}	V _{DS} = 10V	-	150	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	95	1	
Turn - on delay time	$t_{d(on)}^{*4}$	$V_{DD} \simeq 15V$, $V_{GS} = 10V$	-	9		
Rise time	t _r *4	I _D = 3A	-	21	-	
Turn - off delay time	t _{d(off)} *4	$R_L = 5.0\Omega$	-	36	-	ns
Fall time	t _f *4	$R_G = 10\Omega$		13	-	

<Tr2>

Parameter	Symbol	Conditions		Values		Unit
	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	2600	ı	
Output capacitance	C _{oss}	V _{DS} = -10V	-	450	ı	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	350	ı	
Turn - on delay time	$t_{d(on)}^{*4}$	V _{DD} ≃ -15V, V _{GS} = -10V	-	20	ı	
Rise time	t _r *4	I _D = -3.5A	-	50	ı	no
Turn - off delay time	$t_{d(off)}^{*4}$	$R_L = 4.29\Omega$	-	110	•	ns
Fall time	t _f *4	$R_G = 10\Omega$	-	70	-	



● Gate charge characteristics (T_a = 25°C)

<Tr1>

Parameter Total gate charge	Cumbal	Conditions	Values			Unit
	Symbol Conditions	Min.	Тур.	Max.	Uriit	
Total gate charge	Q _g *4		-	7.2	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \approx 15V$, $I_{D} = 6A$ $V_{GS} = 5V$	-	1.8	-	nC
Gate - Drain charge	Q _{gd} *4	VGS OV	-	2.8		

<Tr2>

Darameter	Cumbal	Conditions	8	Values		Lloit
Parameter	Symbol Conditions	Min.	Тур.	Max.	Unit	
Total gate charge	Q _g *4		-	25	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \simeq -15V$, $I_D = -7A$ $V_{GS} = -5V$	-	5.5	-	nC
Gate - Drain charge	Q _{gd} *4	163	(10	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1>

Darameter	Cymahal	Conditions	,	Values	/alues Unit - 1.6 A	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uriit
Continuous forward current	Is	T - 25°C	-	-	1.6	^
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	24	A
Forward voltage	V _{SD} *4	$V_{GS} = 0V, I_S = 6.4A$	-	-	1.2	V

<Tr2>

Parameter	Symbol Conditions -	Values			Unit	
		Min.	Тур.	Max.	Offic	
Continuous forward current	I _S	T = 25°C	-	-	-1.6	_
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	-28	A
Forward voltage	V _{SD} *4	$V_{GS} = 0V, I_S = -1.6A$	-	-	-1.2	V

Fig.1 Power Dissipation Derating Curve

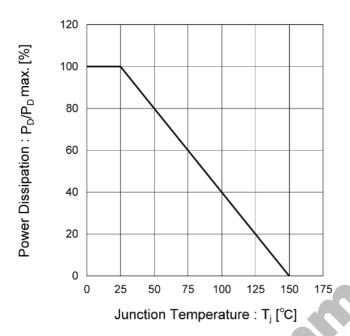
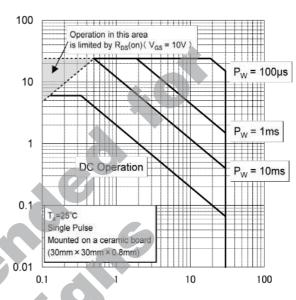


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage: VDS [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

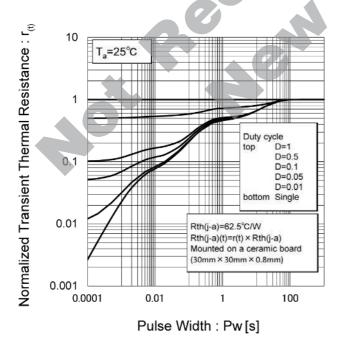
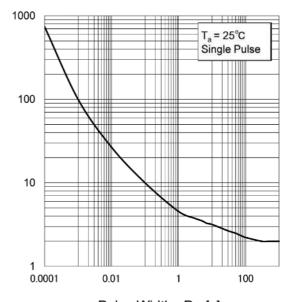


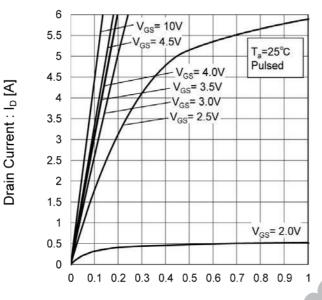
Fig.4 Single Pulse Maximum Power dissipation



Pulse Width : Pw [s]

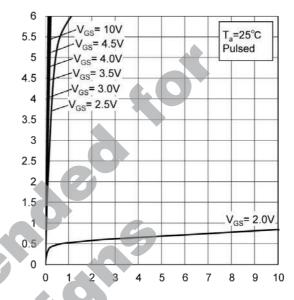
Peak Transient Power: P(W)

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage: VDS [V]

Fig.6 Typical Output Characteristics(II)



Drain Current: Ip [A]

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Drain - Source Voltage: VDS [V]

Fig.7 Breakdown Voltage vs **Junction Temperature**

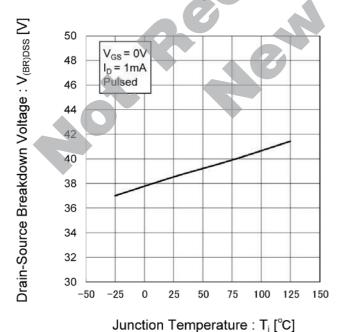


Fig.8 Typical Transfer Characteristics

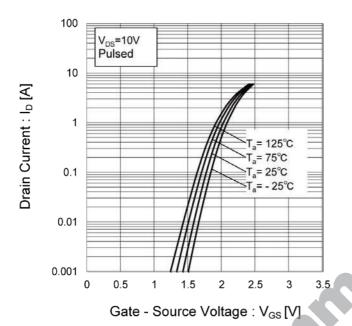


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

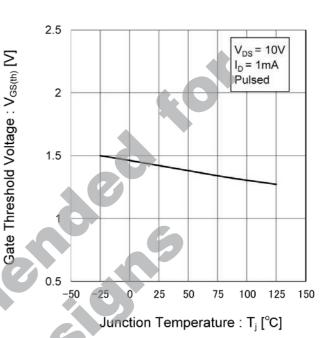


Fig.10 Forward Transfer Admittance vs.
Drain Current

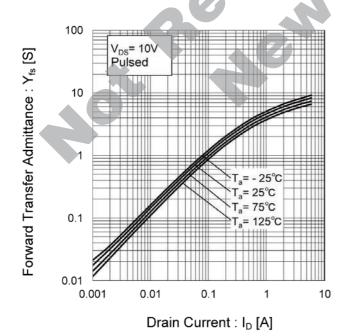


Fig.11 Drain Current Derating Curve

120 100 **Drain Current Dissipation** 80 : I_D/I_Dmax. [%] 60 40 20 0 -25 0 25 75 100 125 Junction Temperature : T_i [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

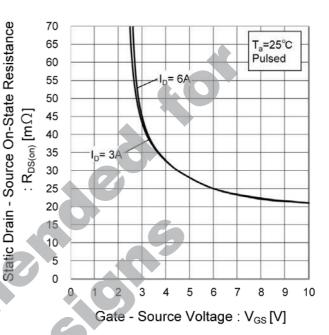


Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature

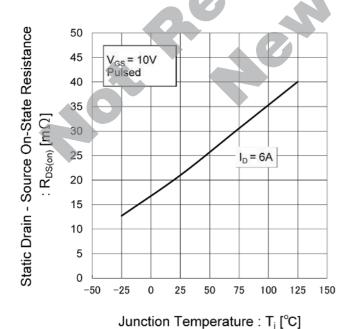


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

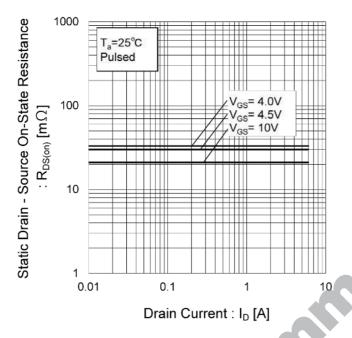


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

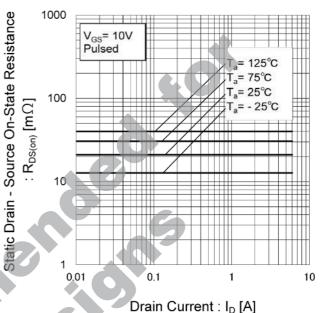


Fig.16 Static Drain - Source On - State
Resistance vs. Drain Current (III)

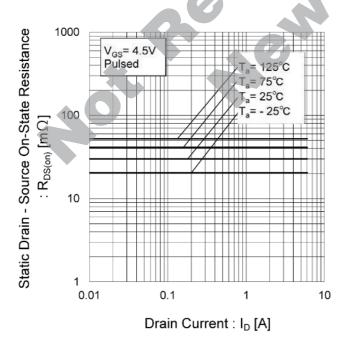


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

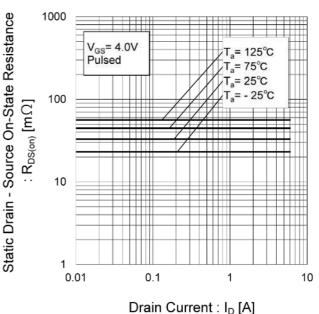


Fig.18 Typical Capacitance vs.

Drain - Source Voltage

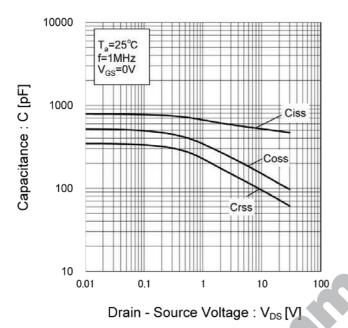


Fig.19 Switching Characteristics

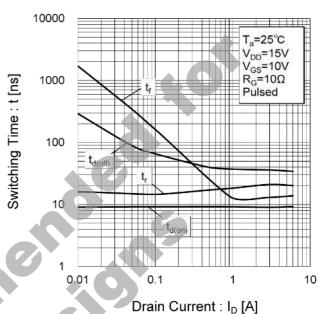


Fig.20 Dynamic Input Characteristics

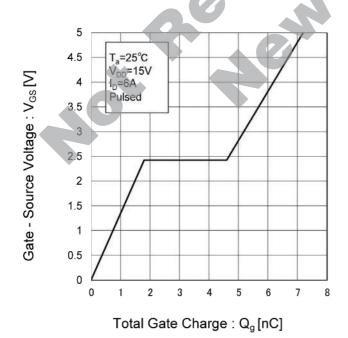
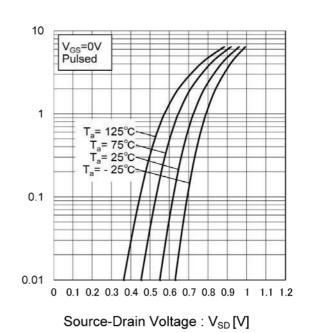


Fig.21 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

Fig.1 Power Dissipation Derating Curve

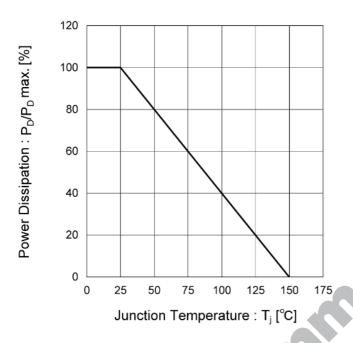
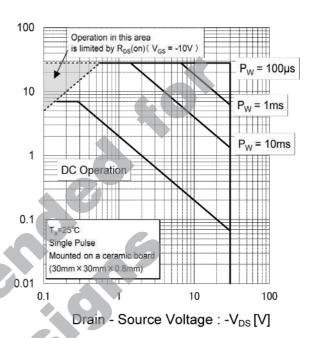


Fig.2 Maximum Safe Operating Area



Drain Current: -I_D [A]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

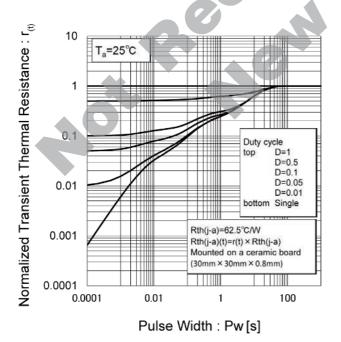
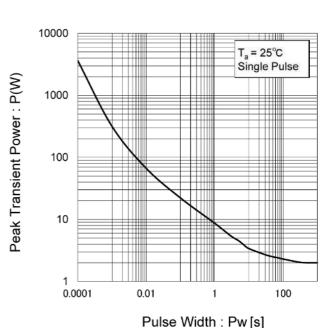


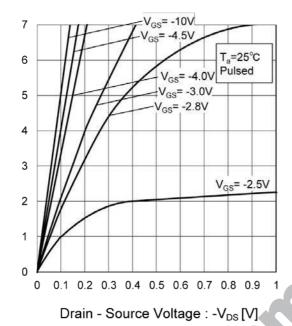
Fig.4 Single Pulse Maximum Power dissipation



Drain Current: -I_D [A]

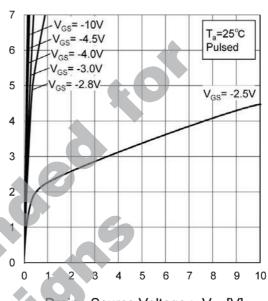
• Electrical characteristic curves < Tr2>

Fig.5 Typical Output Characteristics(I)



Drain Current : -I_D [A]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : -VDS [V]

Fig.7 Breakdown Voltage vs **Junction Temperature**

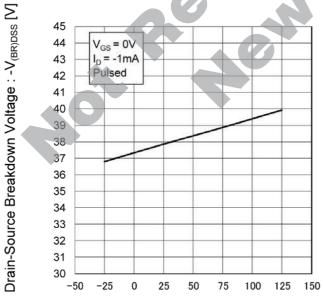


Fig.8 Typical Transfer Characteristics

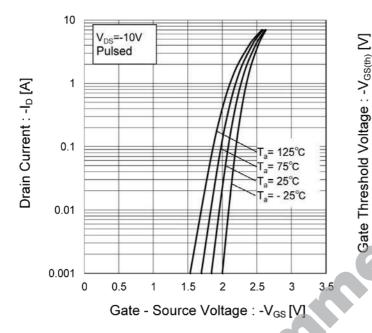


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

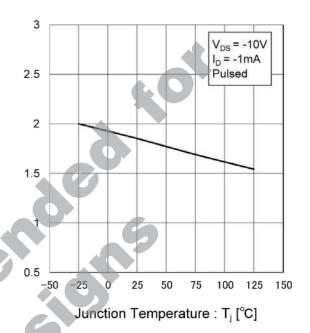


Fig.10 Forward Transfer Admittance vs.
Drain Current

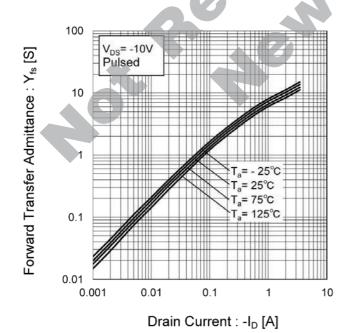


Fig.11 Drain Current Derating Curve

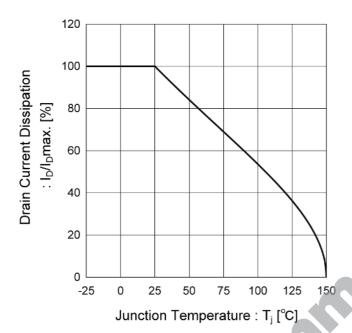


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

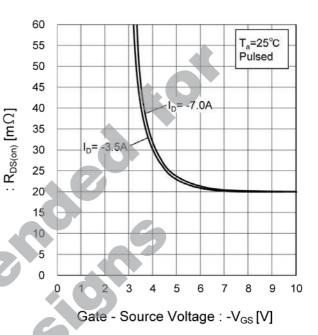
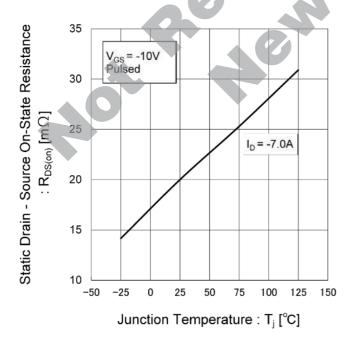


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



Static Drain - Source On-State Resistance

Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

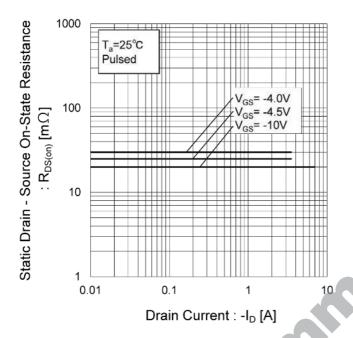


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

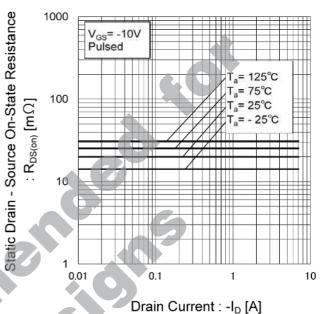


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

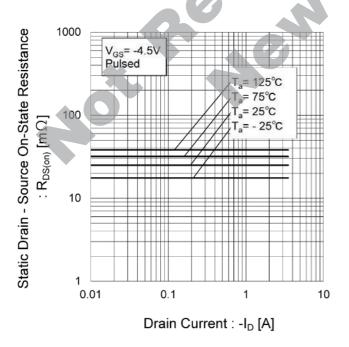


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

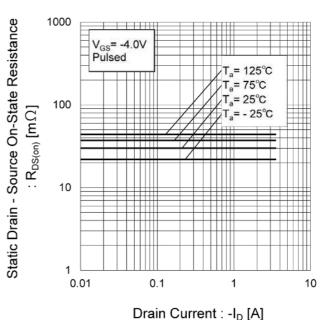


Fig.18 Typical Capacitance vs.

Drain - Source Voltage

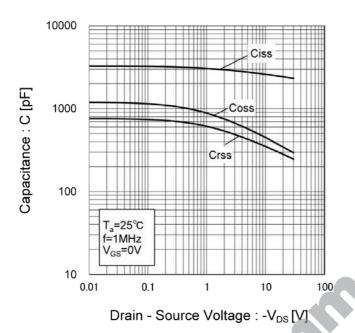


Fig.19 Switching Characteristics

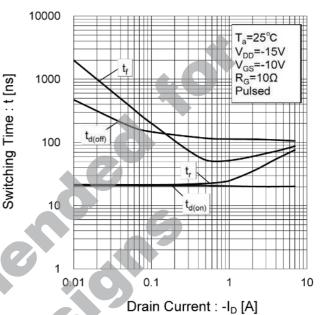


Fig.20 Dynamic Input Characteristics

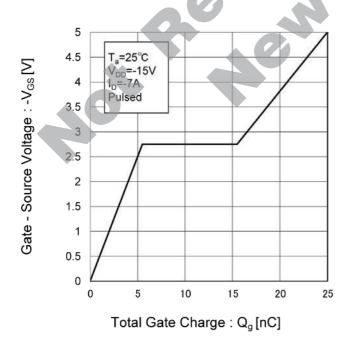
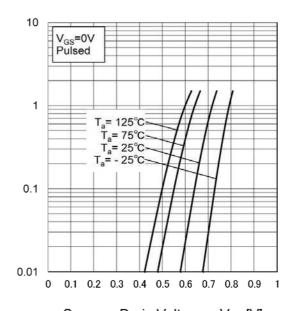


Fig.21 Source Current vs.

Source Drain Voltage



Source - Drain Voltage : - V_{SD} [V]

Source Current : -Is [A]

● Measurement circuits < Tr1>

Fig.1-1 Switching Time Measurement Circuit

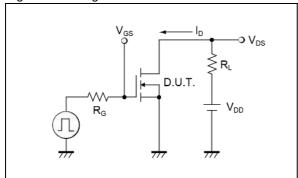


Fig.2-1 Gate Charge Measurement Circuit

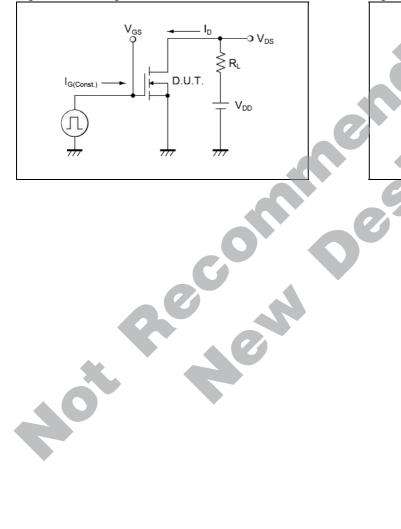


Fig.1-2 Switching Waveforms

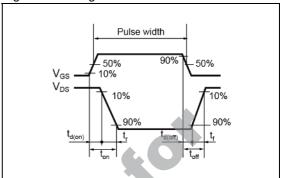
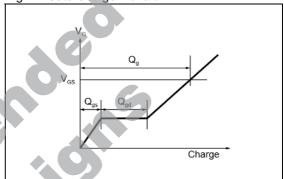


Fig.2-2 Gate Charge Waveform



● Measurement circuits < Tr2>

Fig.3-1 Switching Time Measurement Circuit

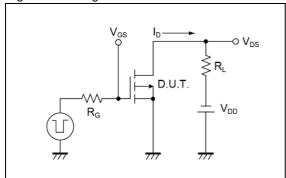


Fig.4-1 Gate Charge Measurement Circuit

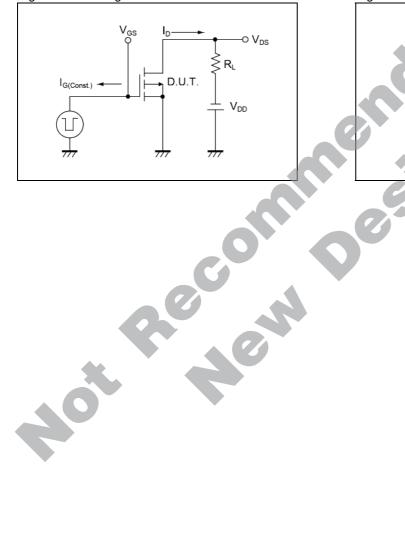


Fig.3-2 Switching Waveforms

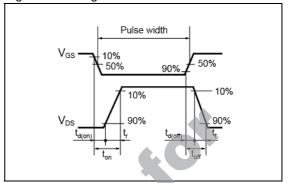
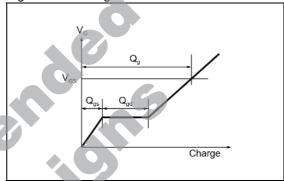
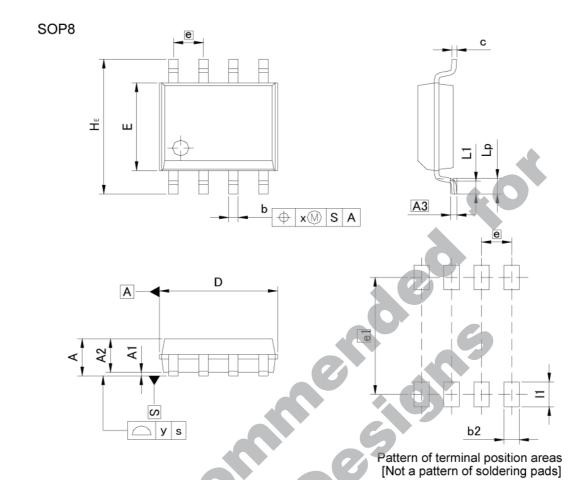


Fig.4-2 Gate Charge Waveform





Dimensions



DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	= 1	1.75	- 61	0.069
A1	0.1	5	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.2	5	0.0	10
b	0.30	0.50	0.012	0.020
С	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.2	7	0.0	50
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
х	0.1	5	0.0	06
У	0.1	0	0.0	04

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	-8	0.65		0.026
e1	5.15		0.203	
11		1,15	7 8	0.045

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Dimension in mm/inches

Notice

Precaution on using ROHM Products

1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

(1.1010 1) 1110 and a = quipinoni ciacomication of the operation				
JAPAN	USA	EU	CHINA	
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSⅢ	
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII	

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - [a] Installation of protection circuits or other protective devices to improve system safety
 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3. Our Products are not designed under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

Precaution Regarding Intellectual Property Rights

- 1. All information and data including but not limited to application example contained in this document is for reference only. ROHM does not warrant that foregoing information or data will not infringe any intellectual property rights or any other rights of any third party regarding such information or data.
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Other Precaution

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General Precaution

- 1. Before you use our Products, you are requested to care fully read this document and fully understand its contents. ROHM shall not be in an y way responsible or liable for failure, malfunction or accident arising from the use of a ny ROHM's Products against warning, caution or note contained in this document.
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