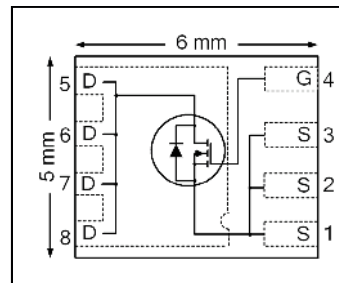


HEXFET® Power MOSFET

V_{DSS}	25	V
$R_{DS(on) \max}$ (@ $V_{GS} = 10V$)	4.6	mΩ
(@ $V_{GS} = 4.5V$)	7.3	
Qg (typical)	8.2	nC
I_D (@ $T_C (Bottom) = 25^\circ C$)	60	A



Applications

- Control MOSFET for Sync Buck Converters
- Secondary Synchronous Rectifier MOSFET for isolated DC-DC converters

Features

Low Charge (typical 8.2 nC)
Low $R_{DS(on)}$ (<4.6 mΩ)
Low Thermal Resistance to PCB (<4.6 °C/W)
Low Profile (<0.9 mm)
Industry-Standard Pinout
Compatible with Existing Surface Mount Techniques
RoHS Compliant, Halogen-Free
MSL1, Industrial Qualification

results in
⇒

Benefits

Low Switching Losses
Lower Conduction Losses
Enable better Thermal Dissipation
Increased Power Density
Multi-Vendor Compatibility
Easier Manufacturing
Environmentally Friendlier
Increased Reliability

Base part number	Package Type	Standard Pack		Orderable Part Number
		Form	Quantity	
IRFH4234PbF	PQFN 5mm x 6 mm	Tape and Reel	4000	IRFH4234TRPbF

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{GS}	Gate-to-Source Voltage	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	22	A
$I_D @ T_{C(Bottom)} = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	60	
$I_D @ T_{C(Bottom)} = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	38	
I_{DM}	Pulsed Drain Current ①	240	
$P_D @ T_A = 25^\circ C$	Power Dissipation ⑤	3.5	W
$P_D @ T_{C(Bottom)} = 25^\circ C$	Power Dissipation ⑤	27	
	Linear Derating Factor ⑤	0.028	W/°C
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	°C

Notes ① through ⑤ are on page 9

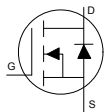
Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	25	—	—	V	V _{GS} = 0V, I _D = 250μA
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	20	—	mV/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	3.5	4.6	mΩ	V _{GS} = 10V, I _D = 30A ③
		—	5.6	7.3		V _{GS} = 4.5V, I _D = 30A ③
V _{GS(th)}	Gate Threshold Voltage	1.1	1.6	2.1	V	V _{DS} = V _{GS} , I _D = 25μA
ΔV _{GS(th)}	Gate Threshold Voltage Coefficient	—	-5.6	—	mV/°C	
I _{DSS}	Drain-to-Source Leakage Current	—	—	1.0	μA	V _{DS} = 20V, V _{GS} = 0V
I _{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V _{GS} = 20V
	Gate-to-Source Reverse Leakage	—	—	-100		V _{GS} = -20V
g _{fs}	Forward Transconductance	60	—	—	S	V _{DS} = 5.0V, I _D = 30A
Q _g	Total Gate Charge	—	17	—	nC	V _{GS} = 10V, V _{DS} = 13V, I _D = 30A
Q _g	Total Gate Charge	—	8.2	12.3	nC	V _{DS} = 13V V _{GS} = 4.5V I _D = 30A
Q _{gs1}	Pre-V _{th} Gate-to-Source Charge	—	1.6	—		
Q _{gs2}	Post-V _{th} Gate-to-Source Charge	—	1.6	—		
Q _{gd}	Gate-to-Drain Charge	—	3.1	—		
Q _{godr}	Gate Charge Overdrive	—	1.9	—		
Q _{sw}	Switch Charge (Q _{gs2} + Q _{gd})	—	4.7	—		
Q _{oss}	Output Charge	—	7.7	—	nC	V _{DS} = 16V, V _{GS} = 0V
R _G	Gate Resistance	—	1.8	—	Ω	
t _{d(on)}	Turn-On Delay Time	—	7.8	—	ns	V _{DD} = 13V, V _{GS} = 4.5V I _D = 30A R _G = 1.8Ω
t _r	Rise Time	—	30	—		
t _{d(off)}	Turn-Off Delay Time	—	8.0	—		
t _f	Fall Time	—	5.3	—		
C _{iss}	Input Capacitance	—	1011	—	pF	V _{GS} = 0V V _{DS} = 13V f = 1.0MHz
C _{oss}	Output Capacitance	—	286	—		
C _{rss}	Reverse Transfer Capacitance	—	83	—		

Avalanche Characteristics

	Parameter	Typ.	Max.
E _{AS}	Single Pulse Avalanche Energy ②	—	42
I _{AR}	Avalanche Current ①	—	30

Diode Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	60	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	240		
V _{SD}	Diode Forward Voltage	—	—	1.0	V	T _J = 25°C, I _S = 30A, V _{GS} = 0V ③
t _{rr}	Reverse Recovery Time	—	10	15	ns	T _J = 25°C, I _F = 30A, V _{DD} = 13V
Q _{rr}	Reverse Recovery Charge	—	11	17	nC	di/dt = 200A/μs ③

Thermal Resistance

	Parameter	Typ.	Max.	Units
R _{θJC} (Bottom)	Junction-to-Case ④	—	4.6	°C/W
R _{θJC} (Top)	Junction-to-Case ④	—	24	
R _{θJA}	Junction-to-Ambient ⑤	—	36	
R _{θJA} (<10s)	Junction-to-Ambient ⑤	—	24	

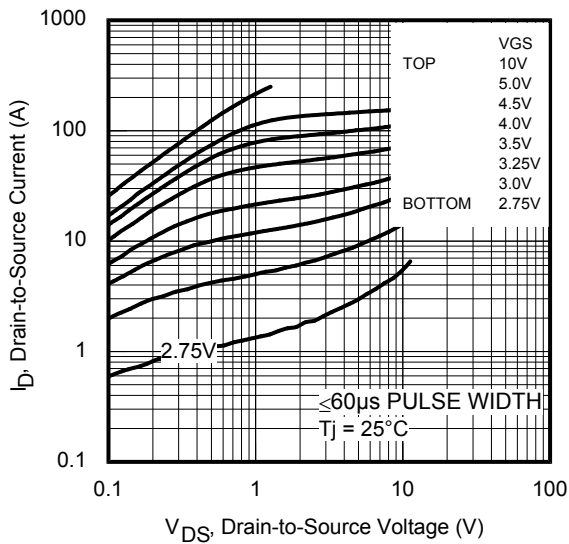


Fig 1. Typical Output Characteristics

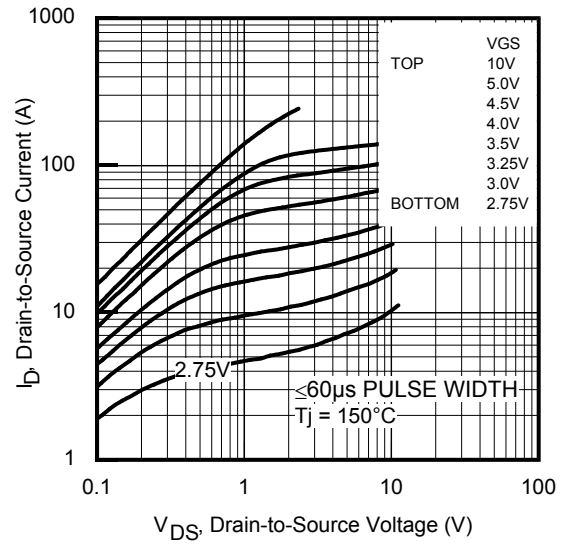


Fig 2. Typical Output Characteristics

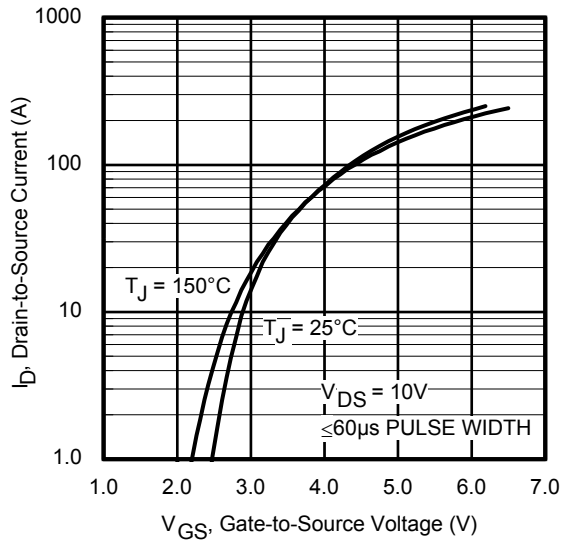


Fig 3. Typical Transfer Characteristics

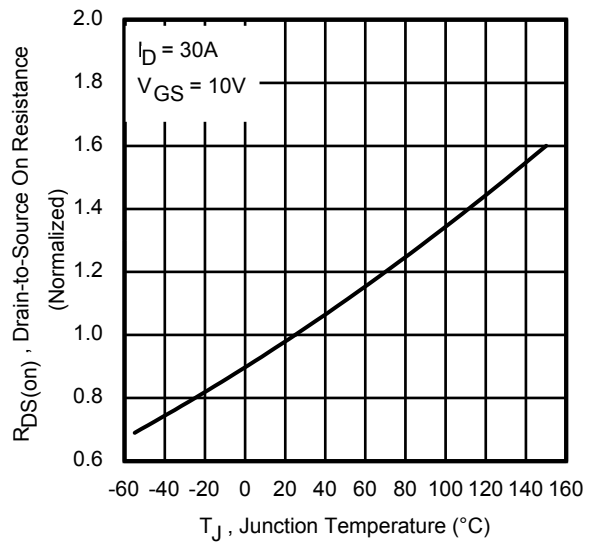


Fig 4. Normalized On-Resistance vs. Temperature

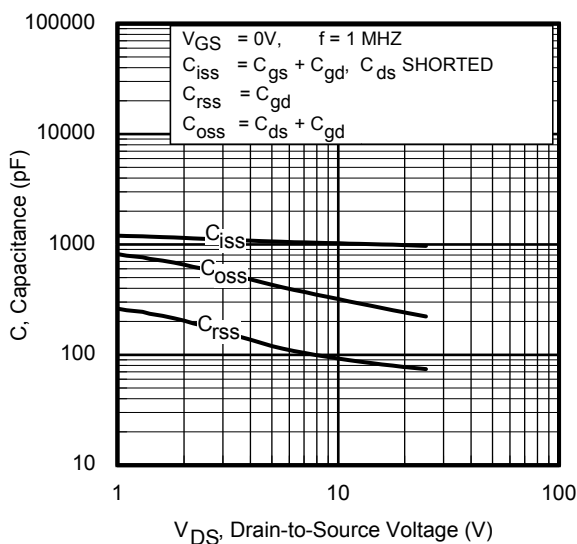


Fig 5. Typical Capacitance vs. Drain-to-Source Voltage

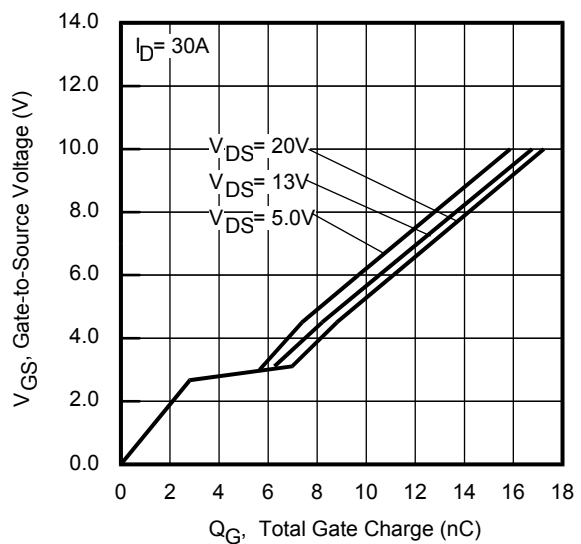


Fig 6. Typical Gate Charge vs. Gate-to-Source Voltage

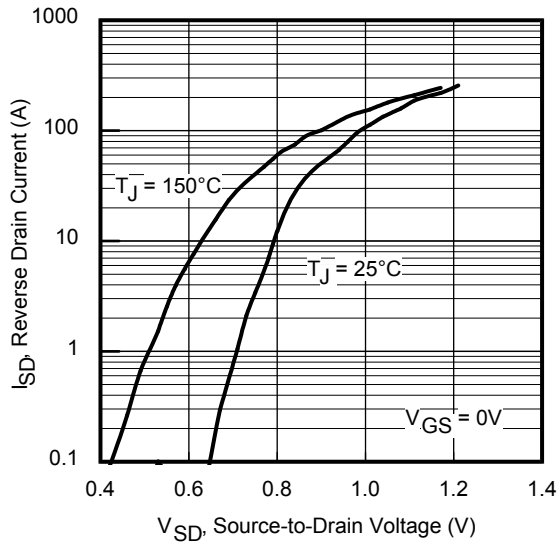


Fig 7. Typical Source-Drain Diode Forward Voltage

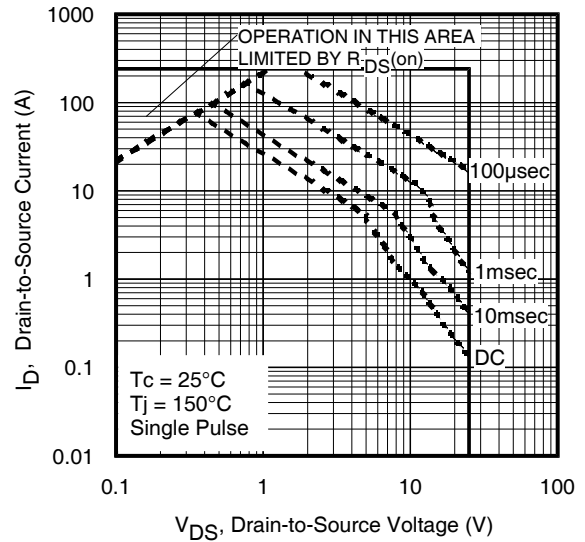


Fig 8. Maximum Safe Operating Area

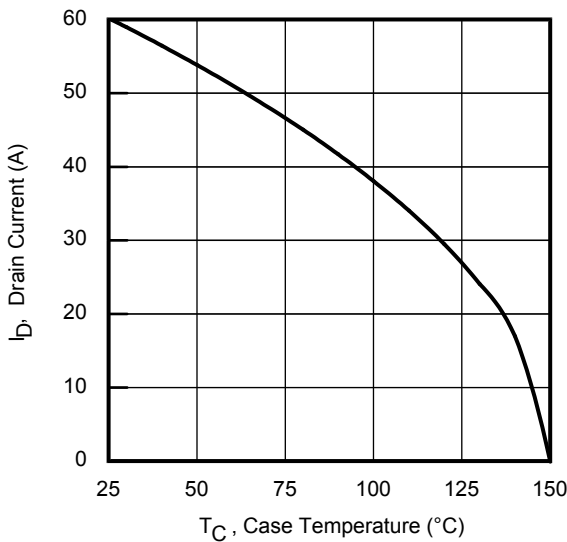


Fig 9. Maximum Drain Current vs. Case Temperature

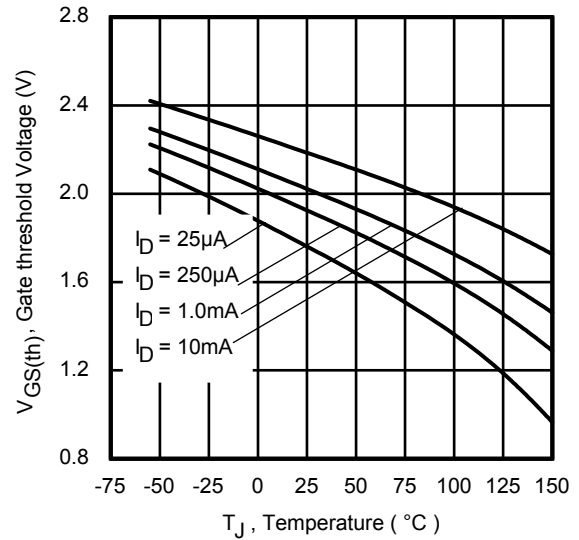


Fig 10. Drain-to-Source Breakdown Voltage

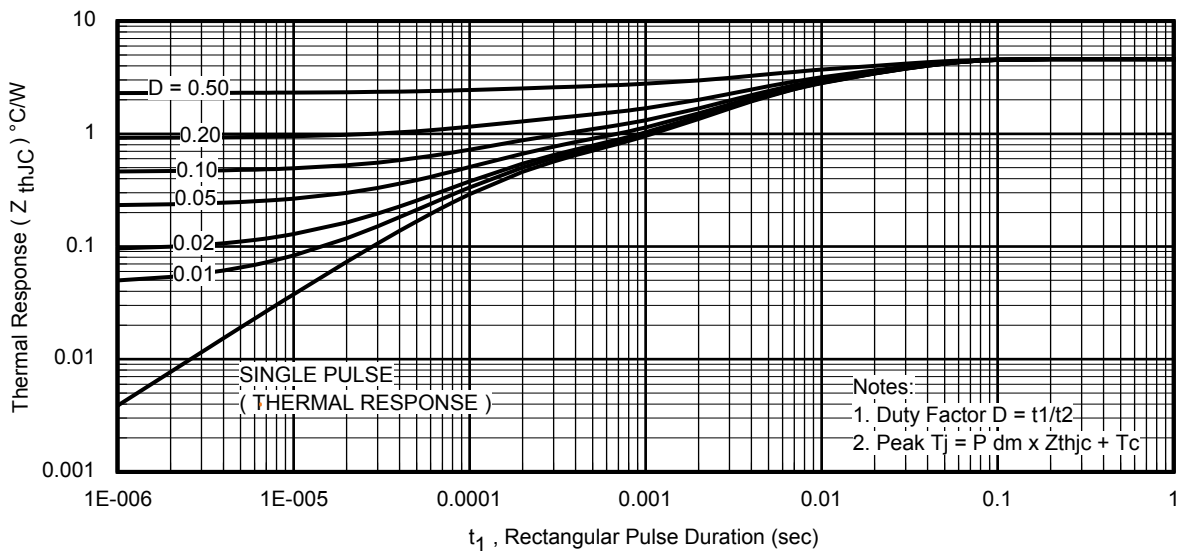


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

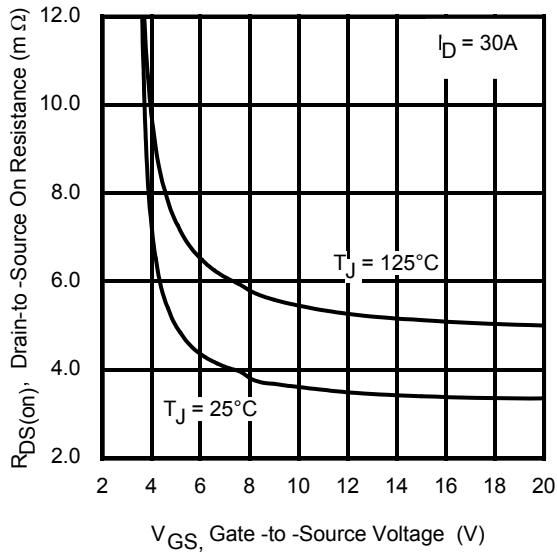


Fig 12. On-Resistance vs. Gate Voltage

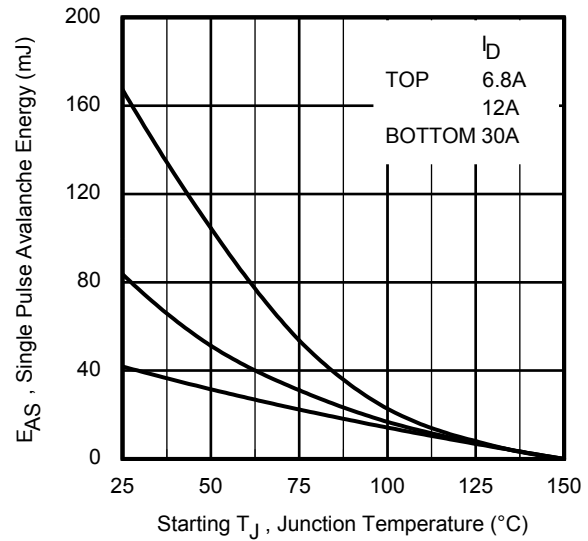


Fig 13. Maximum Avalanche Energy vs. Drain Current

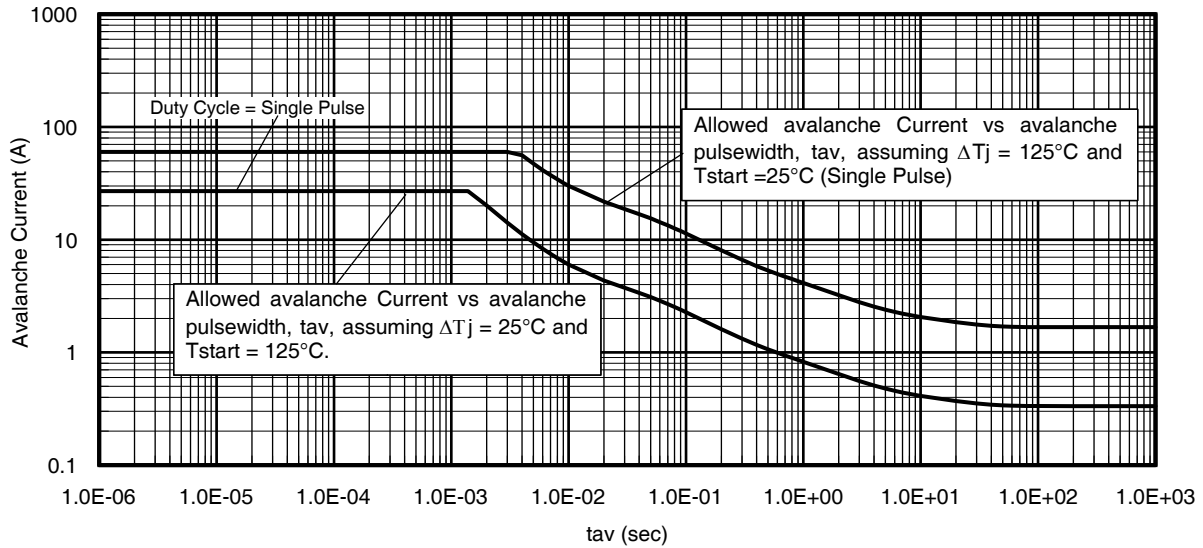
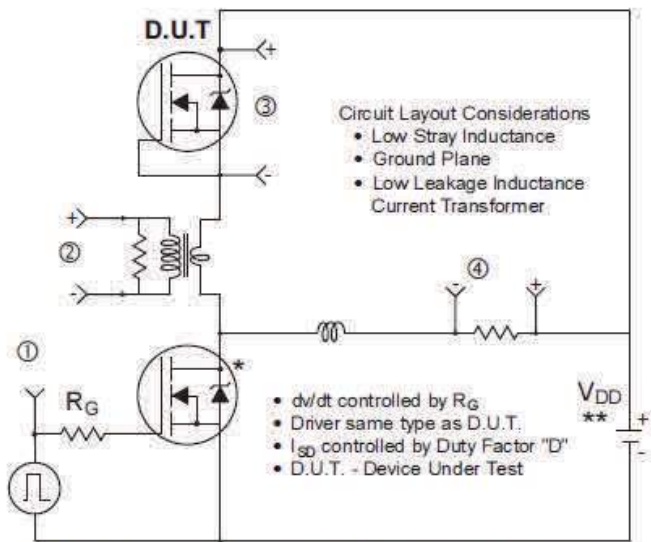
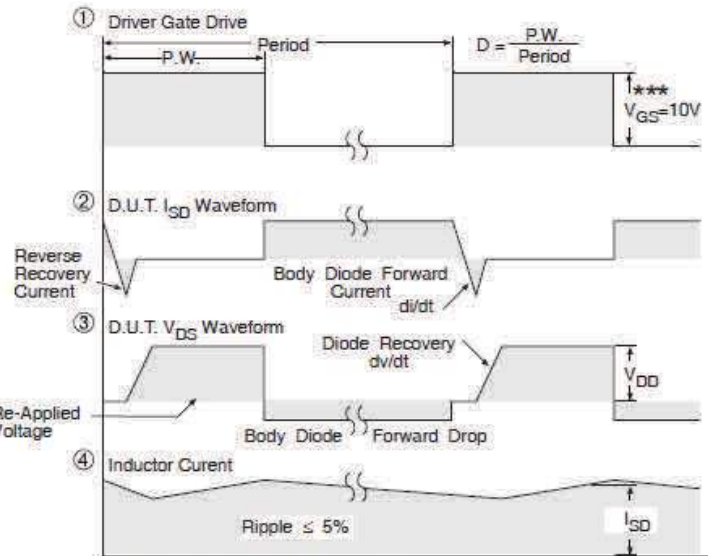


Fig 14. Typical Avalanche Current vs. Pulsewidth



* Use P-Channel Driver for P-Channel Measurements
 ** Reverse Polarity for P-Channel

Fig 15. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs



*** V_{GS} = 5V for Logic Level Devices

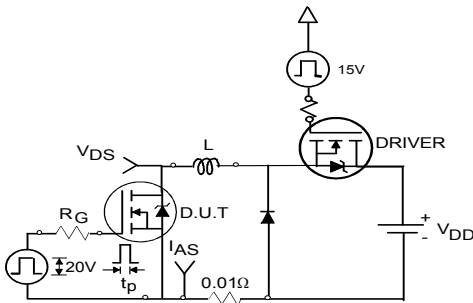


Fig 16a. Unclamped Inductive Test Circuit

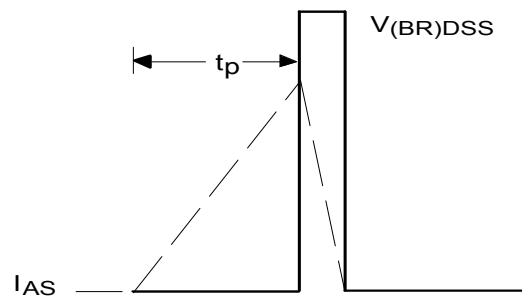


Fig 16b. Unclamped Inductive Waveforms

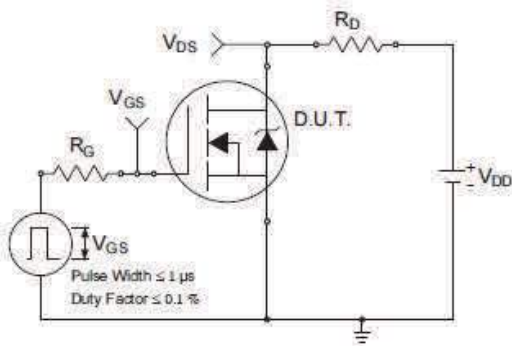


Fig 17a. Switching Time Test Circuit

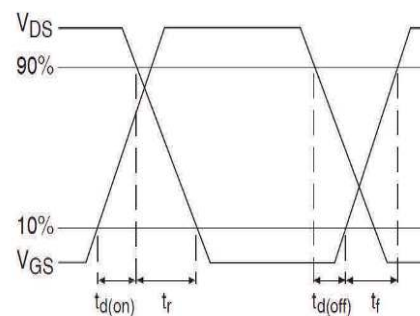


Fig 17b. Switching Time Waveforms

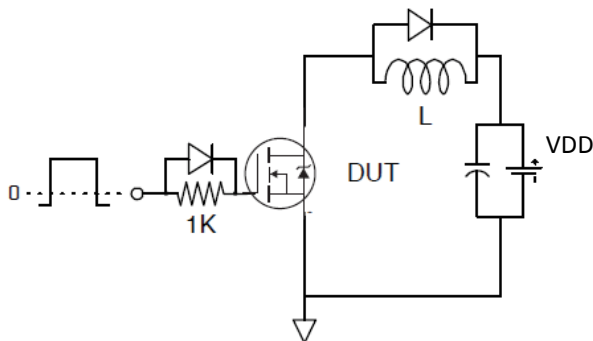


Fig 18. Gate Charge Test Circuit

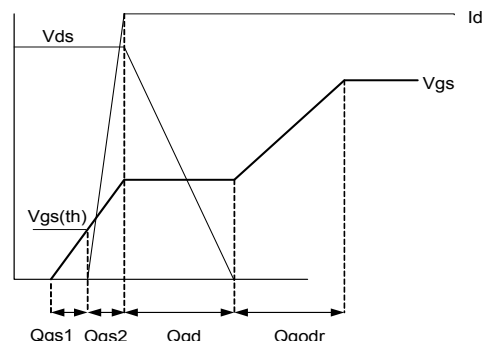
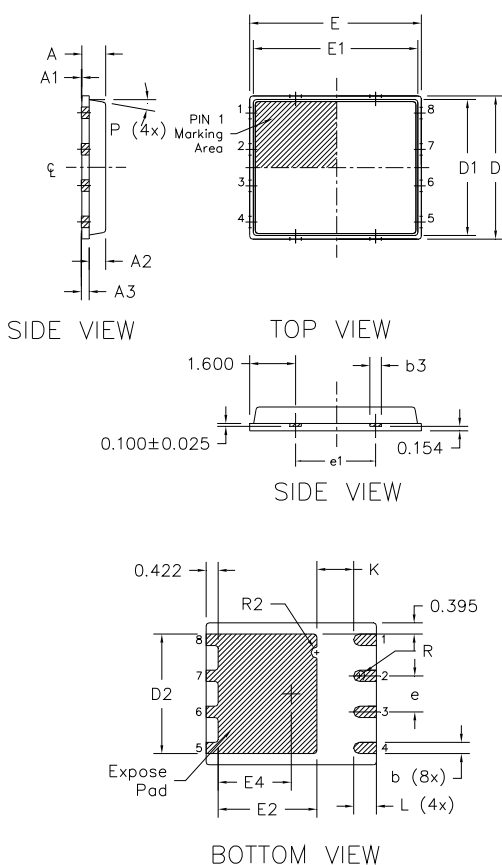


Fig 19. Gate Charge Waveform

PQFN 5x6 Outline "B" Package Details



DIM SYMBOL	MILLIMETERS		INCH	
	MIN	MAX	MIN	MAX
A	0.800	0.900	0.0315	0.0543
A1	0.000	0.050	0.0000	0.0020
A3	0.200	REF	0.0079	REF
b	0.350	0.470	0.0138	0.0185
b1	0.025	0.125	0.0010	0.0049
b2	0.210	0.410	0.0083	0.0161
b3	0.150	0.450	0.0059	0.0177
D	5.000	BSC	0.1969	BSC
D1	4.750	BSC	0.1870	BSC
D2	4.100	4.300	0.1614	0.1693
E	6.000	BSC	0.2362	BSC
E1	5.750	BSC	0.2264	BSC
E2	3.380	3.780	0.1331	0.1488
e	1.270	REF	0.0500	REF
e1	2.800	REF	0.1102	REF
K	1.200	1.420	0.0472	0.0559
L	0.710	0.900	0.0280	0.0354
P	0°	12°	0°	12°
R	0.200	REF	0.0079	REF
R2	0.150	0.200	0.0059	0.0079

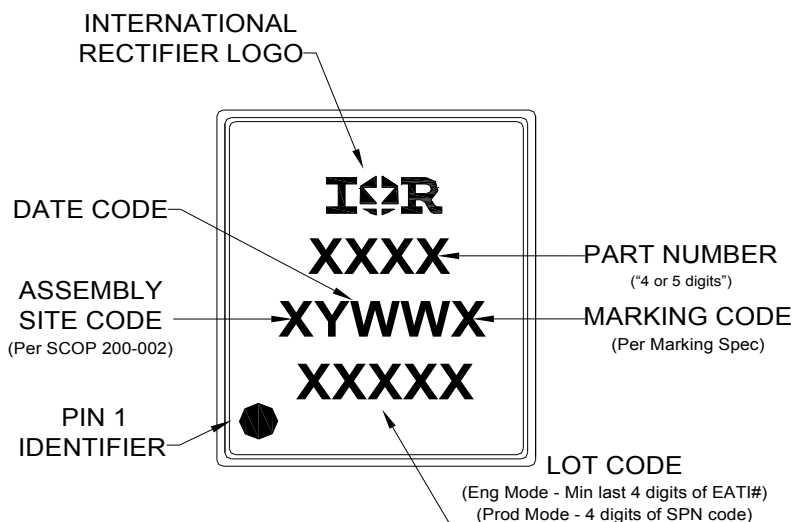
Note:

1. Dimensions and tolerancing confirm to ASME Y14.5M-1994
2. Dimension L represents terminal full back from package edge up to 0.1mm is acceptable
3. Coplanarity applies to the expose Heat Slug as well as the terminal
4. Radius on terminal is Optional

For more information on board mounting, including footprint and stencil recommendation, please refer to application note AN-1136: <http://www.irf.com/technical-info/appnotes/an-1136.pdf>

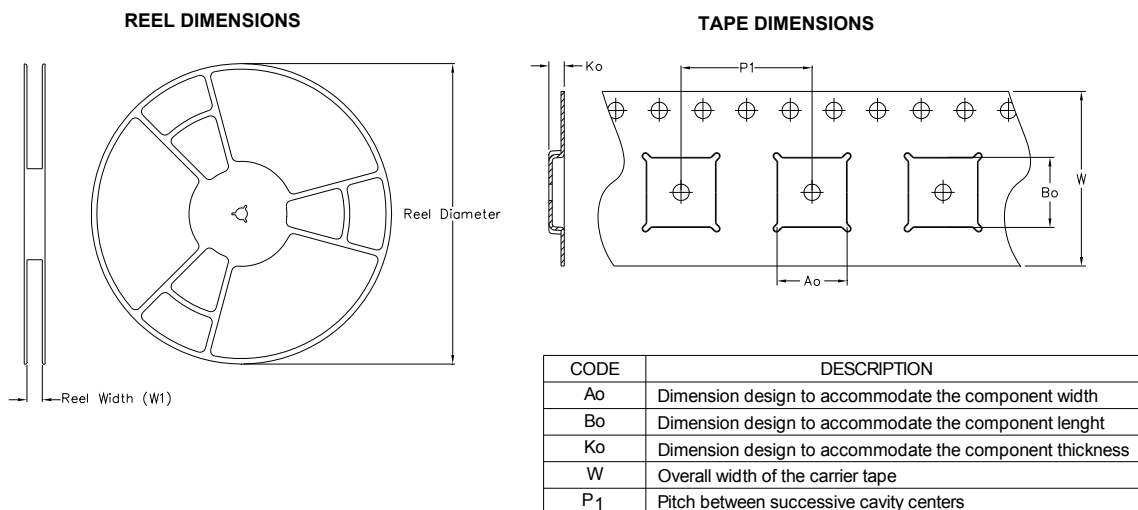
For more information on package inspection techniques, please refer to application note AN-1154: <http://www.irf.com/technical-info/appnotes/an-1154.pdf>

PQFN 5x6 Part Marking

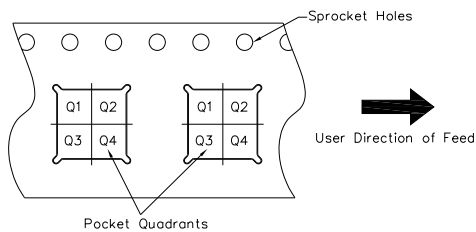


Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

PQFN 5x6 Tape and Reel



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



Note: All dimension are nominal

Package Type	Reel Diameter (Inch)	QTY	Reel Width W1 (mm)	Ao (mm)	Bo (mm)	Ko (mm)	P1 (mm)	W (mm)	Pin 1 Quadrant
5 X 6 PQFN	13	4000	12.4	6.300	5.300	1.20	8.00	12	Q1

Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

Qualification Information†

Qualification Level		Industrial† (per JEDEC JESD47F†† guidelines)
Moisture Sensitivity Level	PQFN 5mm x 6mm	MSL1 (per JEDEC J-STD-020D††)
RoHS Compliant		Yes

† Qualification standards can be found at International Rectifier’s web site: <http://www.irf.com/product-info/reliability>

†† Applicable version of JEDEC standard at the time of product release.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^\circ\text{C}$, $L = 0.093\text{mH}$, $R_G = 50\Omega$, $I_{AS} = 30\text{A}$.
- ③ Pulse width $\leq 400 \mu\text{s}$; duty cycle $\leq 2\%$.
- ④ R_θ is measured at T_J of approximately 90°C .
- ⑤ When mounted on 1 inch square PCB (FR-4). Please refer to AN-994 for more details:
<http://www.irf.com/technical-info/appnotes/an-994.pdf>

Revision History

Date	Comments
1/23/2013	<ul style="list-style-type: none"> • Updated Rg typical from TBD to 1.8Ω.
5/15/2013	<ul style="list-style-type: none"> • Remove note 6, on page 8. • Remove reference to note 6 on maximum current rating, on page 1. • Updated package 3D drawing, on page 1. • Updated IS rating from 30 A to 60 A, on page 2. • Updated package outline drawing, on page 7.
8/15/2013	<ul style="list-style-type: none"> • Added “Fast/RFET™” above the part number, on page 1.
3/16/2015	<ul style="list-style-type: none"> • Updated package outline and tape and reel on pages 7 and 8.