

#### Nch 600V 9A Power MOSFET

$V_{DSS}$	600V
R <sub>DS(on)</sub> (Max.)	0.535Ω
I <sub>D</sub>	±9A
P <sub>D</sub>	94W

# <del>-</del> --

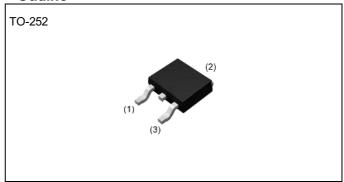
#### Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 4) Parallel use is easy.
- 5) Pb-free plating; RoHS compliant

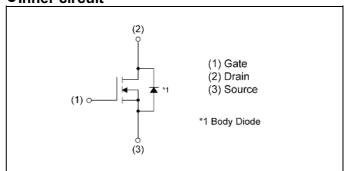


Switching

#### Outline



#### •Inner circuit



Packaging specifications

Packing	Embossed Tape
Packing code	TL1
Marking	R6009E
Basic ordering unit (pcs)	2500

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V <sub>DSS</sub>	600	V	
Continuous drain current (T <sub>c</sub> = 25°C)	I <sub>D</sub> *1	±9	Α	
Pulsed drain current	I <sub>DP</sub> *2	±18	Α	
Cata Sauma valtaga	static	.,	±20	V
Gate - Source voltage	AC(f>1Hz)	V <sub>GSS</sub>	±30	V
Avalanche current, single pulse		I <sub>AS</sub>	1.4	Α
Avalanche energy, single pulse		E <sub>AS</sub> *3	153	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	94	W	
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage tempera	ature range	T <sub>stg</sub>	-55 to +150	°C

#### ●Thermal resistance

Downwortow	Cymah al	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *4	-	-	1.3	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *5	-	-	147	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	600	-	-	V
		V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V				
Zero gate voltage drain current	I <sub>DSS</sub>	$T_j = 25^{\circ}C$	-	-	100	μΑ
		$T_j = 125^{\circ}C$	-	-	1000	
Gate - Source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	2	-	4	V
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.8A				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *6	$T_j = 25^{\circ}C$	-	0.500	0.535	Ω
		$T_j = 125^{\circ}C$	-	1.00	-	
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	9.6	-	Ω

## ● Electrical characteristics (T<sub>a</sub> = 25°C)

Downston	Cyronia al	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Urlit	
Forward Transfer Admittance	$ Y_{fs} ^{*6}$ $V_{DS} = 10V, I_D = 4.5A$		2.3	4.5	-	S	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	430	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25V	-	470	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	55	-		
Turn - on delay time	t <sub>d(on)</sub> *6	V <sub>DD</sub> ≃ 300V, V <sub>GS</sub> = 10V	-	25	-		
Rise time	t <sub>r</sub> *6	I <sub>D</sub> = 4.5A	-	35	-		
Turn - off delay time	rn - off delay time ${\sf t_{d(off)}}^{*6}$		-	75	-	ns	
Fall time	t <sub>f</sub> *6	$R_G = 10\Omega$	-	30	-		

## ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Davanatas	Cymaela al	Conditions	Values			l limit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*6}$	V <sub>DD</sub> ≈ 300V	-	23	-	
Gate - Source charge	Q <sub>gs</sub> *6	I <sub>D</sub> = 9A	-	4	-	nC
Gate - Drain charge	Q <sub>gd</sub> *6	V <sub>GS</sub> = 10V	-	15	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> ≈ 300V, I <sub>D</sub> = 9A	-	6.4	-	V

<sup>\*1</sup> Limited only by maximum channel temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L $\doteqdot$ 100mH, V<sub>DD</sub>=50V, R<sub>G</sub>=25 $\Omega$ , STARTING T<sub>i</sub>=25 $^{\circ}$ C

<sup>\*4</sup> T<sub>C</sub>=25°C

<sup>\*5</sup> Mounted on a epoxy PCB FR4 (25mm x 27mm x 0.8mm)

<sup>\*6</sup> Pulsed

## ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Source current I <sub>S</sub> *1		T <sub>C</sub> = 25°C	1	-	9	Α
Pulsed source current	l <sub>SP</sub> *2	1C - 23 C	1	-	18	Α
Source-Drain voltage V <sub>SD</sub> *6		$V_{GS} = 0V, I_{S} = 9.0A$	-	-	1.5	V
Reverse recovery time	t <sub>rr</sub> *6		-	380	-	ns
Reverse recovery charge	Q <sub>rr</sub> *6	I <sub>S</sub> = 9A di/dt = 100A/µs	-	3.8	-	μC
Peak reverse recovery current	<sub>rr</sub> *6		-	20	-	А

Fig.1 Power Dissipation Derating Curve

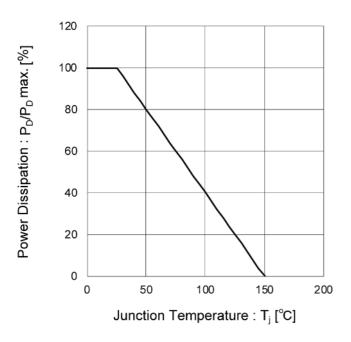


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

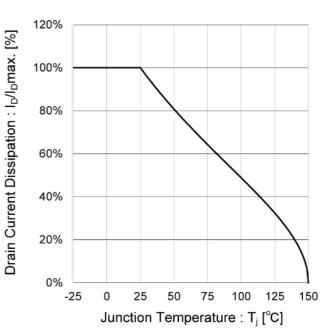


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

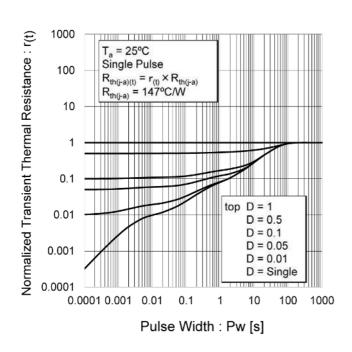
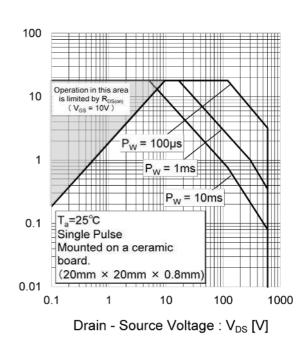


Fig.4 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

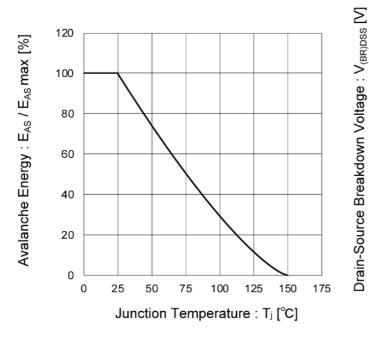


Fig.6 Breakdown Voltage vs.

Junction Temperature

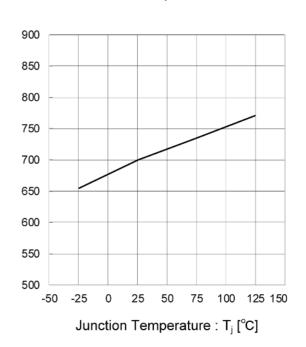


Fig.7 Typical Output Characteristics(I)

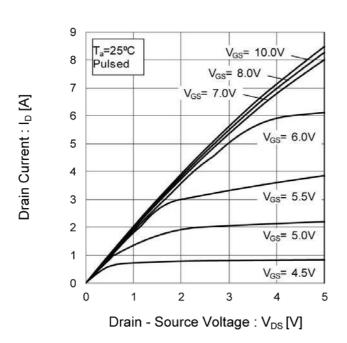
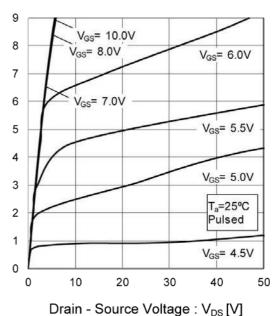


Fig.8 Typical Output Characteristics(II)



Drain Current: Ip [A]

Fig.9 Typical Transfer Characteristics

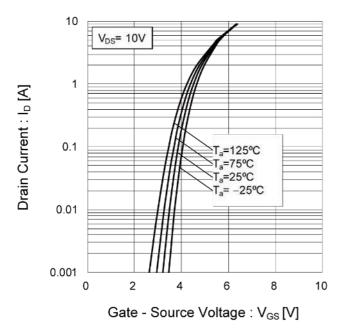
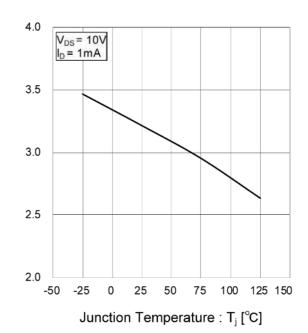


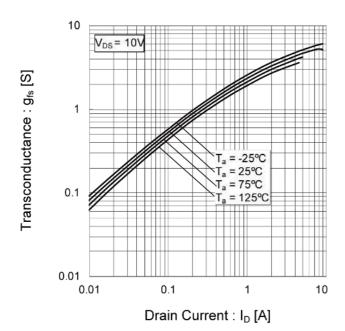
Fig.10 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage: VGS(th) [V]

Fig.11 Forward Transfer Admittance vs.

Drain Current



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Fig.12 Static Drain - Source On - State Resistance vs. Drain Current

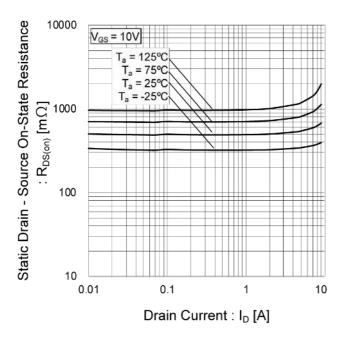
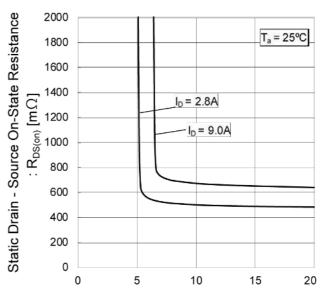


Fig.13 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature

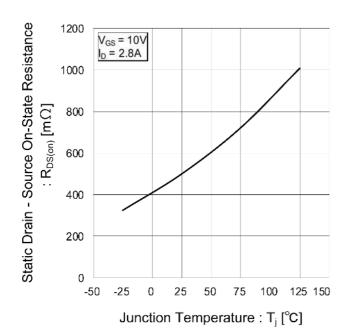


Fig.15 Typical Capacitance vs.

Drain - Source Voltage

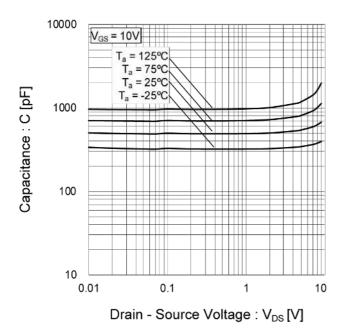


Fig.16 Switching Characteristics

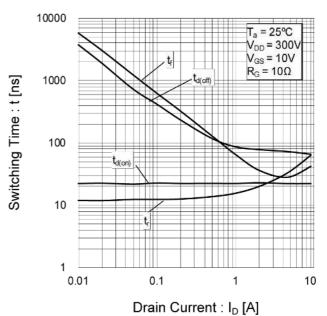


Fig.17 Typical Gate Charge

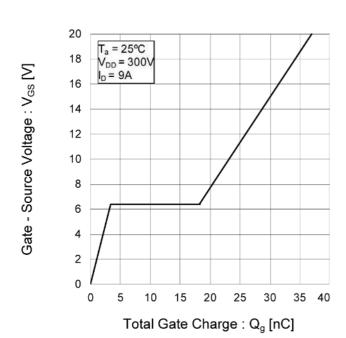


Fig.18 Source Current vs. Source - Drain Voltage

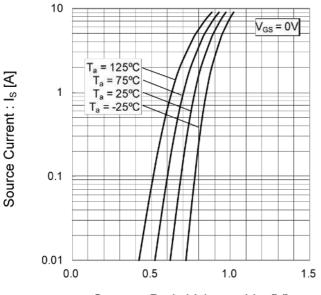
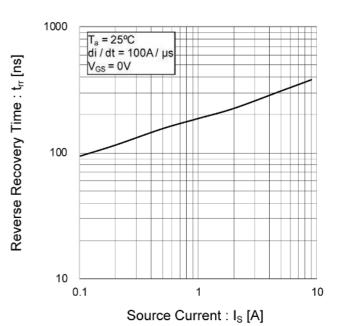


Fig.19 Reverse Recovery Time vs. Source Current



#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

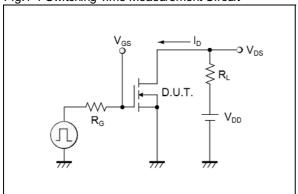


Fig.2-1 Gate Charge Measurement Circuit

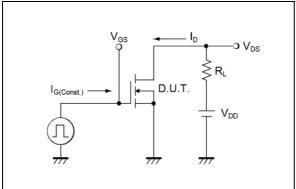


Fig.3-1 Avalanche Measurement Circuit

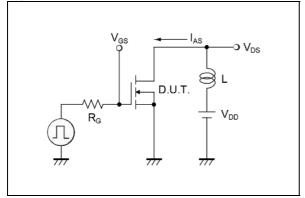


Fig.4-1 trr Measurement Circuit

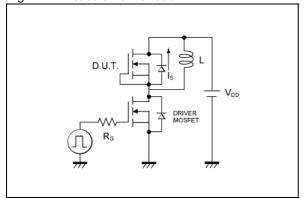


Fig.1-2 Switching Waveforms

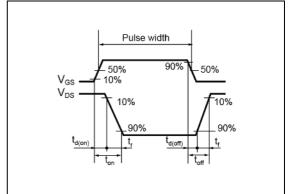


Fig.2-2 Gate Charge Waveform

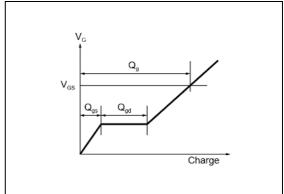


Fig.3-2 Avalanche Waveform

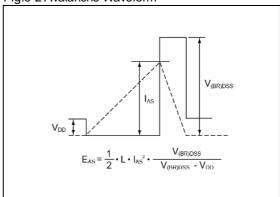
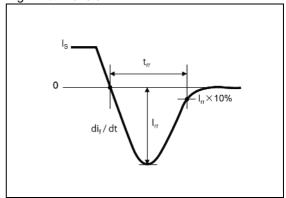
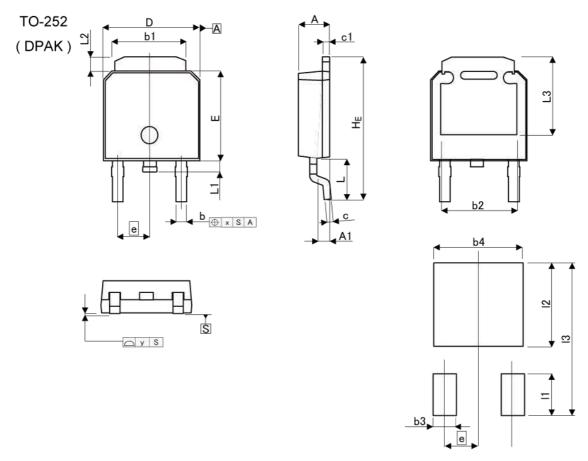


Fig.4-2 trr Waveform



#### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	ETERS	INCHES		
ן ואוט	MIN	MAX	MIN	MAX	
Α	2.20	2.40	0.087	0.094	
A1	0.70	1.10	0.028	0.043	
b	0.60	0.90	0.024	0.035	
b1	5.20	5.50	0.205	0.217	
b2	4.	80	0.1	89	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.	30	0.0	91	
E	6.00	6.40	0.236	0.252	
HE	9.40	10.40	0.370	0.409	
L	2.	90	0.114		
L1	0.60	1.00	0.024	0.039	
L2	0.70	1.30	0.028	0.051	
L3	5.	30	0.209		
х	¥	0.25	\$ 150 miles	0.010	
у	7.	0.10	0 <b>7</b> %	0.004	
DIA 1	MILIME	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
b3		1.15	9 <b>4</b> 6	0.045	
b4		5.55	0.70	0.219	
11	¥	2.77	S(#2))	0.109	
12	-	5.50	2 <b>5</b> %	0.217	
13	¥1	10.40	220	0.409	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### **Precaution for Mounting / Circuit board design**

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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