

2SC5508

NPN SILICON RF TRANSISTOR
FOR LOW-NOISE, HIGH-GAIN AMPLIFICATION
FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M04)

R09DS0055EJ0200 Rev.2.00 Mar 5, 2013

FEATURES

- Ideal for low-noise, high-gain amplification applications
- NF = 1.1 dB TYP., $G_a = 16$ dB TYP. @ $V_{CE} = 2$ V, $I_C = 5$ mA, f = 2 GHz
- Maximum available power gain: MAG = 19 dB TYP. @ V_{CE} = 2 V, I_{C} = 20 mA, f = 2 GHz
- $f_T = 25$ GHz technology adopted
- Flat-lead 4-pin thin-type super minimold (M04) package

<R> ORDERING INFORMATION

Part Number	Order Number	Quantity	Package	Supplying Form
2SC5508	2SC5508-A	50 pcs (Non reel)	Flat-lead 4-pin	8 mm wide embossed taping
2SC5508-T2	2SC5508-T2-A	3 kpcs/reel	thin-type super minimold (M04)	Pin 1 (Emitter), Pin 2 (Collector) face the perforation side of the tape
2SC5508-T2B	2SC5508-T2B-A	15 kpcs/reel	(Pb-Free)	the perioration side of the tape

Remark To order evaluation samples, please contact your nearby sales office.

The unit sample quantity is 50 pcs.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$)

Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	V_{CBO}	15	V
Collector to Emitter Voltage	V_{CEO}	3.3	V
Emitter to Base Voltage	V_{EBO}	1.5	V
Collector Current	Ic	35	mA
Total Power Dissipation	P _{tot} Note	115	mW
Junction Temperature	Tj	150	°C
Storage Temperature	T _{stg}	-65 to +150	°C

Note Free air.

THERMAL RESISTANCE

Parameter	Symbol	Ratings	Unit
Junction to Case Resistance	R _{th j-c}	150	°C /W
Junction to Ambient Resistance	R _{th j-a}	650	°C /W

CAUTION

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.



ELECTRICAL CHARACTERISTICS $(T_A = +25 \text{ °C})$

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics			•			
Collector Cut-off Current	I _{CBO}	V _{CB} = 5 V, I _E = 0	_	_	200	nA
Emitter Cut-off Current	I _{EBO}	V _{EB} = 1 V, I _C = 0	_	_	200	nA
DC Current Gain	h _{FE} Note 1	V _{CE} = 2 V, I _C = 5 mA	50	70	100	_
RF Characteristics						
Gain Bandwidth Product	f _T	V _{CE} = 3 V, I _C = 30 mA, f = 2 GHz	20	25	_	GHz
Insertion Power Gain	$ S_{21e} ^2$	V _{CE} = 2 V, I _C = 20 mA, f = 2 GHz	14	17	_	dB
Noise Figure	NF	$V_{CE} = 2 \text{ V}, I_{C} = 5 \text{ mA}, f = 2 \text{ GHz},$	_	1.1	1.5	dB
		$Z_S = Z_{opt}$				
Reverse Transfer Capacitance	C _{re} Note 2	$V_{CB} = 2 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$	_	0.18	0.24	pF
Maximum Available Power Gain	MAG Note 3	$V_{CE} = 2 \text{ V}, I_{C} = 20 \text{ mA}, f = 2 \text{ GHz}$	_	19	-	dB
Maximum Stable Power Gain	MSG Note 4	V _{CE} = 2 V, I _C = 20 mA, f = 2 GHz	_	20	_	dB
Gain 1 dB Compression Output	P _{O (1 dB)}	$V_{CE} = 2 \text{ V}, I_{C} = 20 \text{ mA}^{\text{Note 5}}, f = 2 \text{ GHz}$	_	11	_	dBm
Power						
3rd Order Intermodulation	OIP ₃	$V_{CE} = 2 \text{ V}, I_{C} = 20 \text{ mA}^{\text{Note 5}}, f = 2 \text{ GHz}$	_	22	_	dBm
Distortion Output Intercept Point						

Notes 1. Pulse measurement: PW \leq 350 μ s, Duty Cycle \leq 2%

2. Collector to base capacitance when the emitter grounded

3. MAG =
$$\left| \frac{S_{21}}{S_{12}} \right| (K - \sqrt{(K^2 - 1)})$$

4. MSG =
$$\frac{S_{21}}{S_{12}}$$

5. Collector current when $P_{O\,(1\,dB)}$ is output

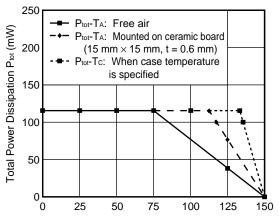
h_{FE} CLASSIFICATION

Rank	FB/YFB
Marking	T79
h _{FE} Value	50 to 100

TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

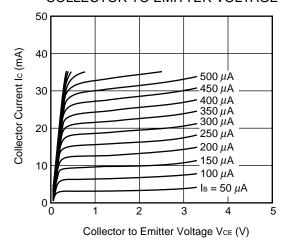
Thermal/DC Characteristics

TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE, CASE TEMPERATURE



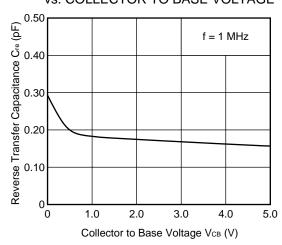
Ambient Temperature TA (°C), Case Temperature Tc (°C)

COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



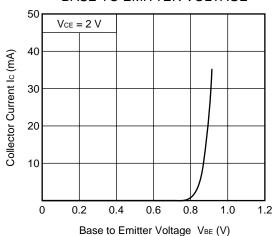
Capacitance/f_T Characteristics

REVERSE TRANSFER CAPACITANCE vs. COLLECTOR TO BASE VOLTAGE

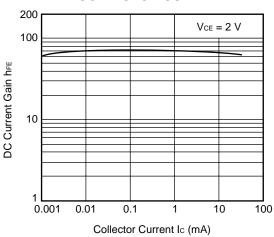


Remark The graphs indicate nominal characteristics.

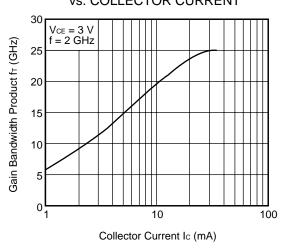
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



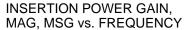
DC CURRENT GAIN vs. COLLECTOR CURRENT

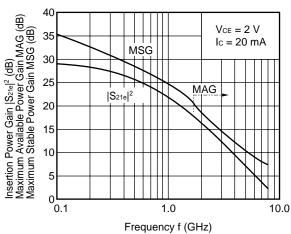


GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT

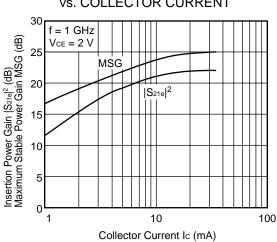


Gain Characteristics

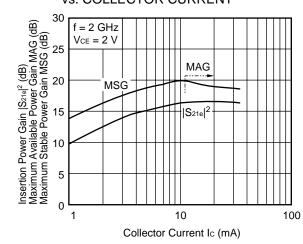




INSERTION POWER GAIN, MSG vs. COLLECTOR CURRENT

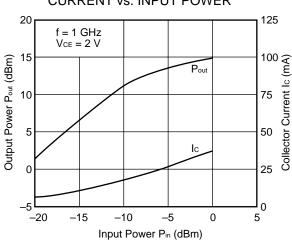


INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT

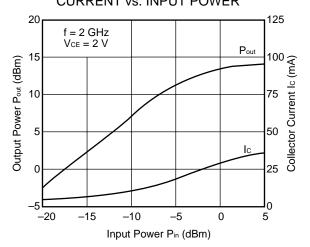


Output Characteristics

OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER

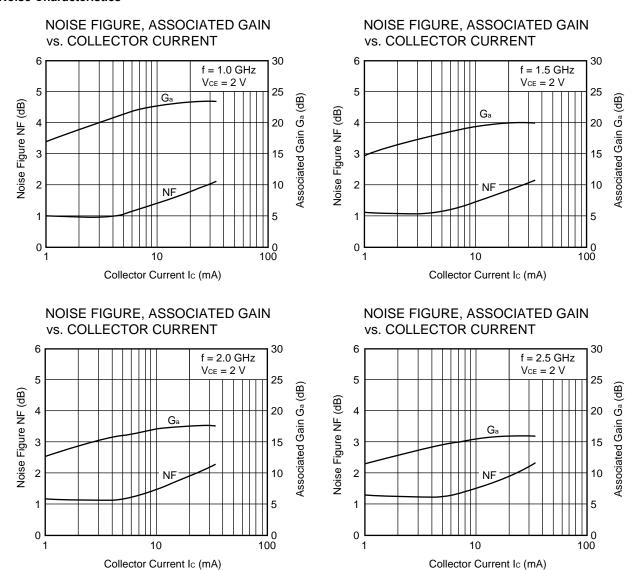


OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER



Remark The graphs indicate nominal characteristics.

Noise Characteristics



Remark The graphs indicate nominal characteristics.

<r> S-PARAMETERS

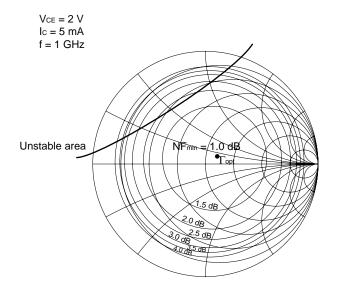
S-parameters and noise parameters are provided on our web site in a form (S2P) that enables direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.

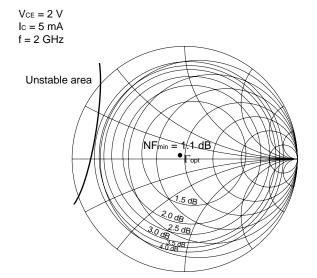
Click here to download S-parameters.

 $[Products] \rightarrow [RF Devices] \rightarrow [Device Parameters]$

URL http://www.renesas.com/products/microwave/

EQUAL NF CIRCLE





NOISE PARAMETERS

 V_{CE} = 2 V, I_{C} = 3 mA

f	NF _{min}	$NF_{min} \mid G_{a} \mid \Gamma_{G}$		opt	D=/50	
(GHz)	(dB)	(dB)	MAG.	ANG.	Rn/50	
0.8	0.78	21.4	0.26	31.7	0.17	
0.9	0.80	20.7	0.26	32.7	0.17	
1.0	0.82	20.0	0.26	34.7	0.17	
1.5	0.93	17.0	0.23	57.0	0.16	
1.8	1.00	15.6	0.20	78.0	0.14	
1.9	1.02	15.2	0.19	86.0	0.14	
2.0	1.04	14.8	0.19	94.2	0.13	
2.5	1.15	13.5	0.20	138.3	0.10	

 V_{CE} = 2 V, I_{C} = 5 mA

f	NF _{min}	Ga	Γ	opt	D=/50
(GHz)	(dB)	(dB)	MAG.	ANG.	Rn/50
0.8	0.93	22.5	0.12	28.1	0.15
0.9	0.94	21.8	0.12	28.8	0.15
1.0	0.96	21.1	0.12	31.7	0.15
1.5	1.03	18.1	0.09	71.1	0.14
1.8	1.07	16.7	0.08	106.2	0.13
1.9	1.09	16.3	0.08	118.5	0.13
2.0	1.10	15.9	0.08	130.5	0.12
2.5	1.17	14.3	0.14	-179.7	0.11

 V_{CE} = 2 V, I_{C} = 10 mA

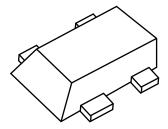
f	NF _{min}	Ga	Γ_{opt}		Rn/50
(GHz)	(dB)	(dB)	MAG.	ANG.	KII/30
0.8	1.28	23.7	0.07	-159.4	0.13
0.9	1.29	23.0	0.07	-157.5	0.13
1.0	1.30	22.3	0.08	-155.7	0.13
1.5	1.37	19.3	0.13	-149.2	0.13
1.8	1.41	17.8	0.16	-146.1	0.13
1.9	1.43	17.3	0.17	-145.0	0.13
2.0	1.44	16.9	0.19	-143.9	0.13
2.5	1.51	15.3	0.25	-136.7	0.13

 V_{CE} = 2 V, I_{C} = 20 mA

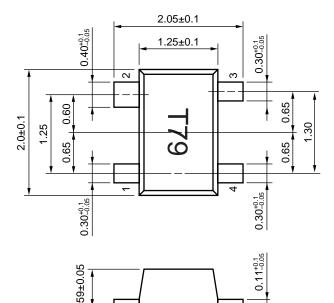
f	NF _{min}	Ga	Γ_{opt}		Rn/50
(GHz)	(dB)	(dB)	MAG.	ANG.	KII/30
8.0	1.59	24.5	0.26	-158.1	0.12
0.9	1.61	23.7	0.26	-155.5	0.13
1.0	1.63	23.0	0.27	-153.1	0.13
1.5	1.72	19.9	0.30	-142.6	0.14
1.8	1.78	18.3	0.33	-137.3	0.15
1.9	1.79	17.9	0.34	-135.7	0.06
2.0	1.81	17.5	0.35	-134.1	0.16
2.5	1.90	15.8	0.40	-126.5	0.18

<R> PACKAGE DIMENSIONS

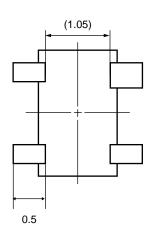
FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M04) PACKAGE (UNIT: mm)



(Top View)



(Bottom View)



PIN CONNECTIONS

- 1. Emitter
- 2. Collector
- 3. Emitter
- 4. Base

Revision History

2SC5508 Data Sheet

		Description				
Rev.	Date	Page	Page Summary			
1.00	Sep 9, 2004	_	First edition issued			
2.00	Mar 5, 2013	Throughout	Throughout Renesas format is applied to this data sheet.			
		p.1	p.1 ORDERING INFORMATION is modified.			
		p.5	Up to date S-PARAMETERS.			
		p.8	Added a drawing backside to PACKAGE DIMENSIONS.			

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California Eastern Laboratories, Inc. 4590 Patrick Henry Drive, Santa Clara, California 95054, U.S.A. Tel: +1-408-919-2500, Fax: +1-408-988-0279

Renesas Electronics Europe Limited
Dukes Meadow, Milliboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-651-700, Fax: +44-1628-651-804 Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germar Tel: +49-211-65030, Fax: +49-211-6503-1327 Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-9235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd. Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852-2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Tai Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Ini Tel: +65-6213-0200, Fax: +65-6213-0300 Innovation Centre Singapore 339949

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tei: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 11F., Samik Lavied' or Bildg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea Tel: 482-2-558-3737, Fax: +82-2-558-5141