

GaN Enhancement Mode Power Transistor

| V_{DSS} | 650V |
|----------------------------|--------|
| R _{DS(on)} (Typ.) | 150mΩ |
| Q_G , typ. | 2.7nC |
| I _{D(Tc=25°C)} *1 | 11A |
| Q _{oss} @ 400V | 18.5nC |
| Q_{rr} | 0nC |

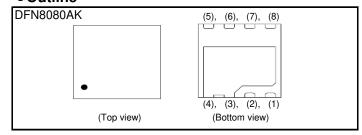
Features

- 650V E-mode GaN FET
- 150mΩ Resistance
- · 2.7nC Gate Charge

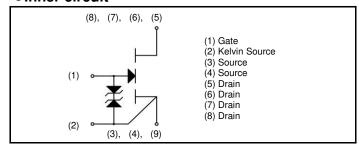
Application

- High switching frequency converter
- · High density converter

Outline



•Inner circuit



Packaging specifications

| | Packing | Embossed tape |
|------|---------------------------|---------------|
| | Reel size (mm) | 330 |
| Type | Tape width (mm) | 16 |
| Туре | Basic ordering unit (pcs) | 3500 |
| | Taping code | E2 |
| | Marking | GNP1150TCA |

● Absolute maximum ratings (T_a = 25°C)

| Parameter | Symbol | Value | Unit | |
|----------------------------------|--------------------------------|--------------------------------|------|----|
| Continuous Drain surrent | $T_c = 25^{\circ}C$ | - I _D *1 | 11 | Α |
| Continuous Drain current | T _c = 125°C | 'D | 5 | Α |
| Pulso Drain aurrant | T _c = 25°C | ı *1*2 | 35 | Α |
| Pulse Drain current | T _c = 125°C | D,pulse | 17 | А |
| Drain - Source Voltage | | V _{DSS} | 650 | V |
| Transient Drain - Source Voltage | V _{DSS(transient)} *3 | 750 | V | |
| Gate - Source voltage (DC) | V_{GSS} | -10 to +6 | V | |
| Transient Gate - Source voltage | | V _{GSS(transient)} *4 | 8.5 | V |
| Power dissipation(Tc=25°C) | | P _{tot} | 62.5 | W |
| Junction temperature | | T _j | 150 | °C |

•Electrical characteristics ($T_a = 25$ °C)

| Parameter | Symbol | Conditions | Values | | | Unit | |
|--|-----------------------|---|--------|------|------|-------|--|
| - raiametei | Symbol | TIDOI CONDITIONS | | Тур. | Max. | Offic | |
| Drain - Source breakdown | V | $V_{GS} = 0V$ | | | | V | |
| voltage | $V_{(BR)DSS}$ | $T_j = 25$ °C | 650 | - | - | V | |
| | | $V_{GS} = 0V$, $V_{DS} = 650V$ | | | | | |
| Zero Gate voltage Drain current | I_{DSS} | $T_j = 25$ °C | - | 1 | 100 | μΑ | |
| Drain ourion | | T _j = 150°C | - | 90 | - | | |
| Gate - Source leakage current | I _{GSS+} | $V_{GS} = 6.0V$, $VDS = 0V$ | - | 0.1 | 3 | mA | |
| Gate threshold voltage | V _{GS (th)} | $V_{DS} = 50 \text{mV}, I_{D} = 18 \text{mA}$ | 1 | 1.45 | 2.4 | V | |
| | | $V_{GS} = 5.0V, I_D = 1.9A$ | | | | | |
| | | $T_j = 25$ °C | - | 155 | 202 | mΩ | |
| Static Drain - Source on - state resistance | B _{D0} () "5 | T _j = 150°C | - | 388 | - | | |
| | | $V_{GS} = 5.5V, I_D = 1.9A$ | | | | | |
| | | T _j = 25°C | - | 150 | 195 | mΩ | |
| | | T _j = 150°C | - | 375 | - | | |
| Gate input resistance | R_{G} | f = 100MHz, open drain | - | 2.6 | - | Ω | |

●Thermal resistance

| Parameter | Symbol | Values | | | Unit |
|--|------------------------|--------|------|------|-------|
| raiametei | Symbol | Min. | Тур. | Max. | Offic |
| Thermal resistance, junction - ambient | R_{thJA} | - | 46.5 | - | °C/W |
| Thermal resistance, junction - case | R_{thJC} | - | 2.0 | 1 | °C/W |
| Reflow soldering temperature | T _{solder} *6 | - | - | 260 | °C |

●Electrical characteristics (T_a = 25°C)

| Davamatav | Cumbal | Conditions | Values | | | Unit | |
|--|------------------------|---|--------|------|------|-------|--|
| Parameter | Symbol Conditions | | Min. | Тур. | Max. | UIIIL | |
| Input capacitance | C_{iss} | $V_{GS} = 0V$ | - | 112 | - | | |
| Output capacitance | C _{oss} | $V_{DS} = 400V$ | - | 19 | ı | pF | |
| Reverse transfer capacitance | C_{rss} | f = 1MHz | - | 0.3 | - | | |
| Effective output capacitance, energy related | $C_{o(er)}$ | $V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 400V$ | - | 29 | - | pF | |
| Effective output capacitance, time related | C _{o(tr)} | $V_{GS} = 0V$ $V_{DS} = 0V$ to 400V | - | 47 | ı | pF | |
| Output charge | Q _{oss} | $V_{GS} = 0V$ $V_{DS} = 0V$ to 400V | - | 18.5 | ı | nC | |
| Total Gate charge | Qg *7 | $V_{DS} = 400V$ $I_{D} = 5A$ | - | 2.7 | - | | |
| Gate - Source charge | Q _{gs} *7 | $V_{GS} = 6V/0V$ | - | 0.3 | - | nC | |
| Gate - Drain charge | Q _{gd} *7 | | - | 1.1 | - | • | |
| Gate plateau voltage | V _{plat} *7 | | - | 2.4 | - | V | |
| Turn - on delay time | t _{d(on)} *7 | $V_{DS} = 400V$ | - | 4.7 | - | | |
| Rise time | t _r *7 | $I_D = 5A$ $V_{GS} = 6V/0V$ | - | 5.3 | - | no | |
| Turn - off delay time | t _{d(off)} *7 | $R_G = 10\Omega$ | _ | 6.2 | - | ns | |
| Fall time | t _f *7 | | - | 8.3 | - | | |

• Reverse conduction electrical characteristics ($T_a = 25$ °C)

| Doromator | Symbol | Canditions | Values | | | Unit |
|-------------------------------|-----------------------------|------------------------------|--------|------|------|-------|
| rarameter | Parameter Symbol Conditions | | Min. | Тур. | Max. | Offic |
| Source-Drain reverse voltage | V_{SD} | $V_{GS} = 0V, I_{SD} = 1.9A$ | - | 2.3 | - | V |
| Reverse recovery time | t _{rr} *7 | | - | 0 | ı | ns |
| Reverse recovery charge | Q _{rr} *7 | | - | 0 | ı | nC |
| Peak reverse recovery current | I _{rrm} *7 | | - | 0 | - 1 | А |

^{*1} Limited and calculated by maximum temperature allowed..

^{*2} V_{GS} =6V,Duty=0.1, t_{pulse} =1 μs .

^{*3} t_{pulse} =1 μ s, <10 hrs of total time.

^{*4} t_{pulse} 20ns, <0.5 hr of total time.

^{*5} Maximum Id applied at Final Test is 1.9A.

^{*6} MSL 3.

^{*7} Pulsed.

Fig.1 Power Dissipation Derating Curve

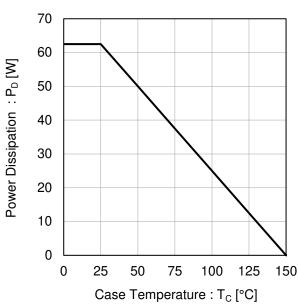
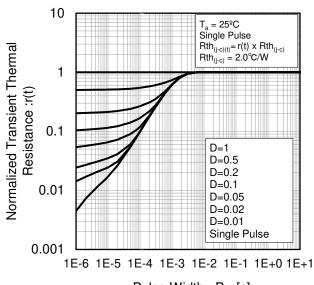


Fig.2 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: Pw [s]

Fig.3 Maximum Safe Operating Area(Ta=25°C)

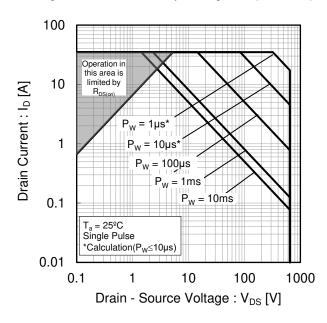


Fig.4 Maximum Safe Operating Area $(T_a=125^{\circ}C)$

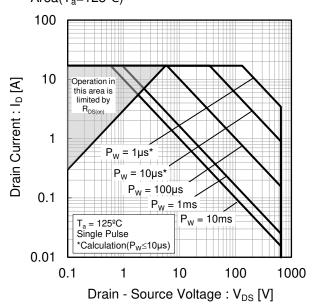


Fig.5 $T_j = 25^{\circ}C$ Typical Output Characteristics

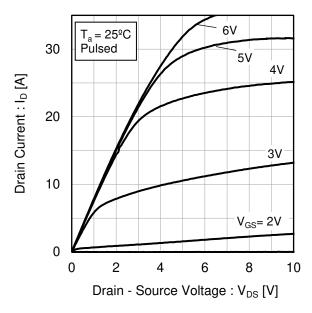


Fig.6 $T_j = 125^{\circ}C$ Typical Output Characteristics

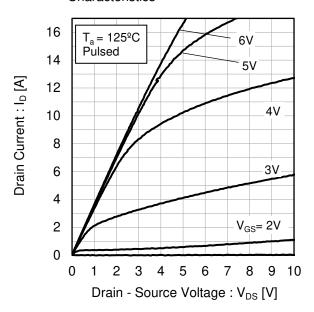


Fig.7 Typical Transfer Characteristics

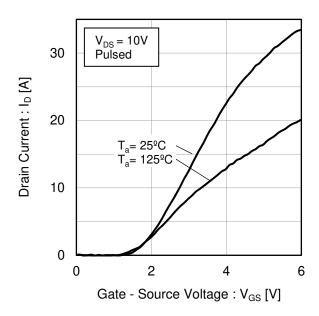


Fig.8 Static Drain - Source On - State
Resistance vs. Drain - Source Current

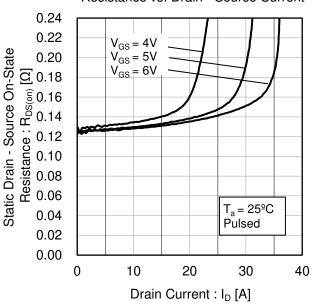


Fig.9 T_i = 25°C 3rd Quadrant Characteristics

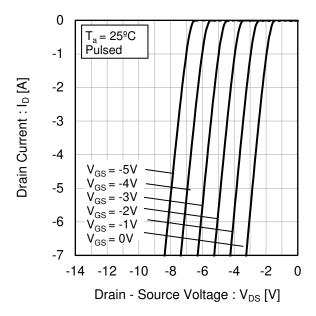


Fig.10 T_i = 125°C 3rd Quadrant Characteristics

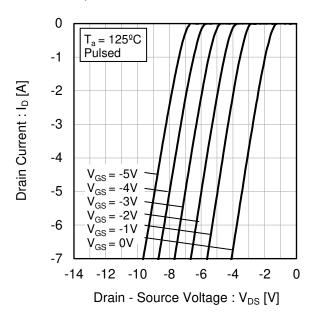


Fig.11 Typical Capacitance

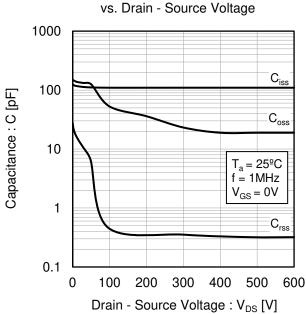


Fig.12 Coss Stored Energy

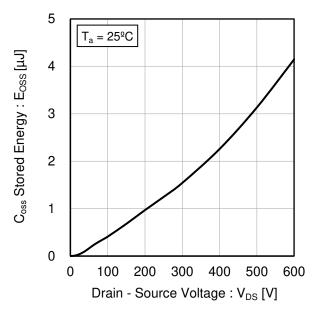
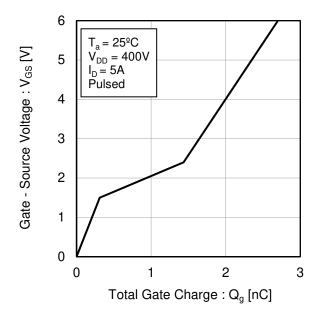
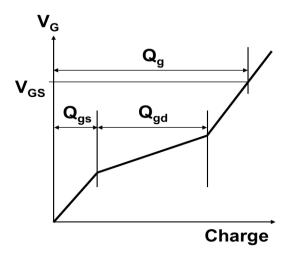


Fig.13 Dynamic Input Characteristics



*Gate Charge Waveform



Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

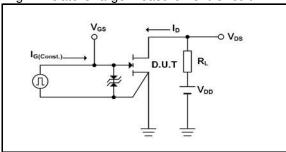


Fig.2-1 Switching Characteristics Measurement Circuit

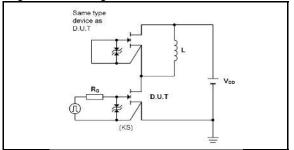


Fig.2-2 Waveforms for Switching Time

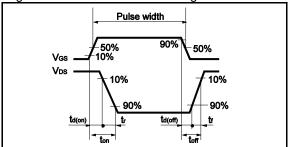
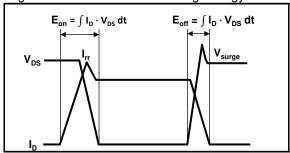


Fig.2-3 Waveforms for Switching Energy Loss



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|---------|-----------|------------|-----------|
| CLASSⅢ | CL ACCIII | CLASS II b | CI VCCIII |
| CLASSIV | CLASSⅢ | CLASSⅢ | CLASSⅢ |

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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 exceeding the recommended storage time period.
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