

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended  
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**P-CHANNEL MOS FIELD EFFECT TRANSISTOR  
FOR SWITCHING**

**DESCRIPTION**

The μPA654TT is a switching device, which can be driven directly by a 1.8 V power source, and it is suitable for applications such as power switch of portable equipment and so on.

**FEATURES**

- 1.8 V drive available
- Low on-state resistance  
 $R_{DS(on)1} = 88 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.5 \text{ V, } I_D = -1.5 \text{ A)}$   
 $R_{DS(on)2} = 133 \text{ m}\Omega \text{ MAX. (} V_{GS} = -2.5 \text{ V, } I_D = -1.5 \text{ A)}$   
 $R_{DS(on)3} = 234 \text{ m}\Omega \text{ MAX. (} V_{GS} = -1.8 \text{ V, } I_D = -1.0 \text{ A)}$

**ORDERING INFORMATION**

PART NUMBER	PACKAGE
μPA654TT-E1-A	6pinWSOF (1620)
μPA654TT-E2-A	

**Marking: WH**

**Remark** "-A" indicates Pb-free (This product does not contain Pb in external electrode and other parts.). "-E1", "-E2" indicates the unit orientation. (8 mm embossed carrier tape, 3000 pcs/reel)

**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C)**

Drain to Source Voltage (V <sub>GS</sub> = 0 V)	V <sub>DSS</sub>	-12	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	V <sub>GSS</sub>	±8.0	V
Drain Current (DC) <sup>Note1</sup>	I <sub>D(DC)</sub>	±2.5	A
Drain Current (pulse) <sup>Note2</sup>	I <sub>D(pulse)</sub>	±10	A
Total Power Dissipation 1	P <sub>T1</sub>	0.2	W
Total Power Dissipation 2 <sup>Note1</sup>	P <sub>T2</sub>	1.3	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

**Notes** 1. Mounted on FR-4 board of 5000 mm<sup>2</sup> x 1.1 mm, t ≤ 5 sec.

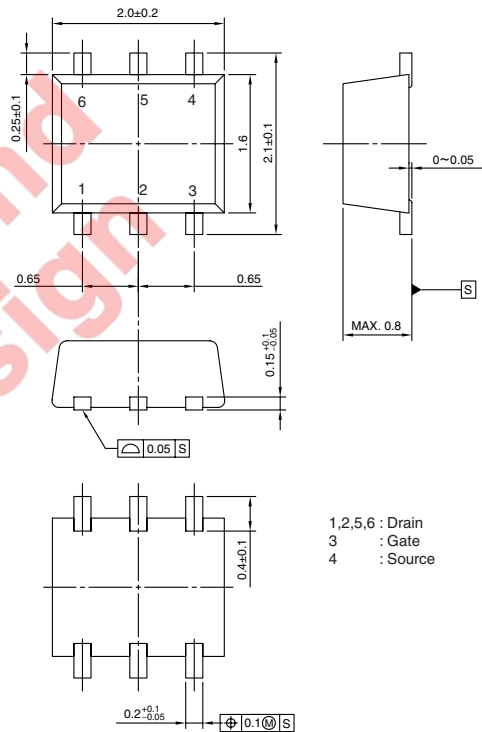
2. PW ≤ 10 μs, Duty Cycle ≤ 1%

**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

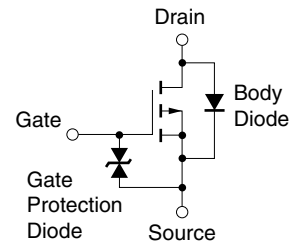
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**PACKAGE DRAWING (Unit: mm)**



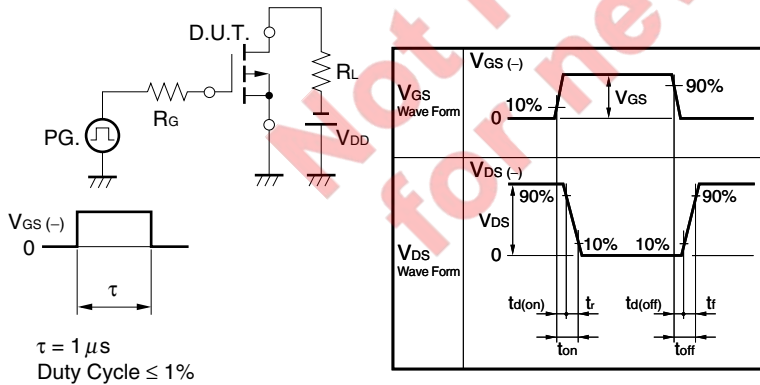
**EQUIVALENT CIRCUIT**



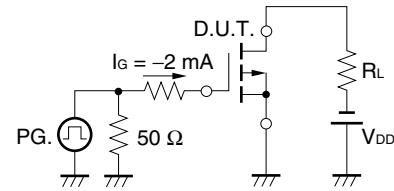
**ELECTRICAL CHARACTERISTICS (TA = 25°C)**

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -12\text{ V}, V_{GS} = 0\text{ V}$			-10	μA
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \mp 8.0\text{ V}, V_{DS} = 0\text{ V}$			±10	μA
Gate Cut-off Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{ V}, I_D = -1.0\text{ mA}$	-0.45	-0.75	-1.5	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10\text{ V}, I_D = -1.5\text{ A}$	1.0	4.7		S
Drain to Source On-state Resistance	$R_{DS(on)1}$	$V_{GS} = -4.5\text{ V}, I_D = -1.5\text{ A}$		70	88	mΩ
	$R_{DS(on)2}$	$V_{GS} = -2.5\text{ V}, I_D = -1.5\text{ A}$		100	133	mΩ
	$R_{DS(on)3}$	$V_{GS} = -1.8\text{ V}, I_D = -1.0\text{ A}$		140	234	mΩ
Input Capacitance	$C_{iss}$	$V_{DS} = -10\text{ V}$		250		pF
Output Capacitance	$C_{oss}$	$V_{GS} = 0\text{ V}$		83		pF
Reverse Transfer Capacitance	$C_{rss}$	$f = 1.0\text{ MHz}$		40		pF
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -6.0\text{ V}, I_D = -1.5\text{ A}$		16		ns
Rise Time	$t_r$	$V_{GS} = -4.0\text{ V}$		90		ns
Turn-off Delay Time	$t_{d(off)}$	$R_G = 10\ \Omega$		173		ns
Fall Time	$t_f$			138		ns
Total Gate Charge	$Q_G$	$V_{DD} = -10\text{ V}$		2.7		nC
Gate to Source Charge	$Q_{GS}$	$V_{GS} = -4.0\text{ V}$		0.5		nC
Gate to Drain Charge	$Q_{GD}$	$I_D = -2.5\text{ A}$		0.8		nC
Body Diode Forward Voltage	$V_{F(S-D)}$	$I_F = 2.5\text{ A}, V_{GS} = 0\text{ V}$		0.87		V

**TEST CIRCUIT 1 SWITCHING TIME**

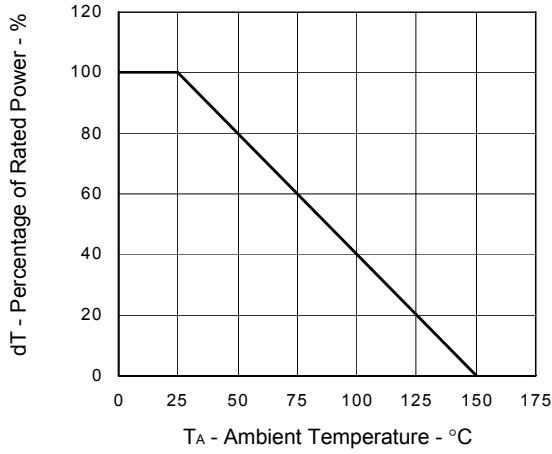


**TEST CIRCUIT 2 GATE CHARGE**

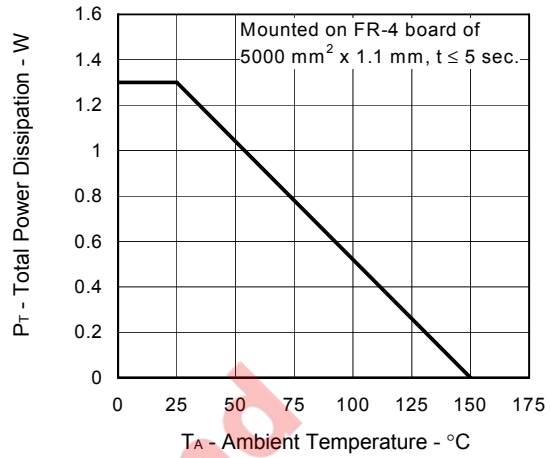


TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )

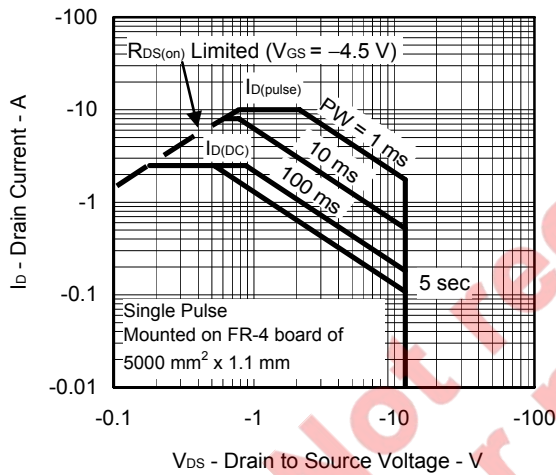
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



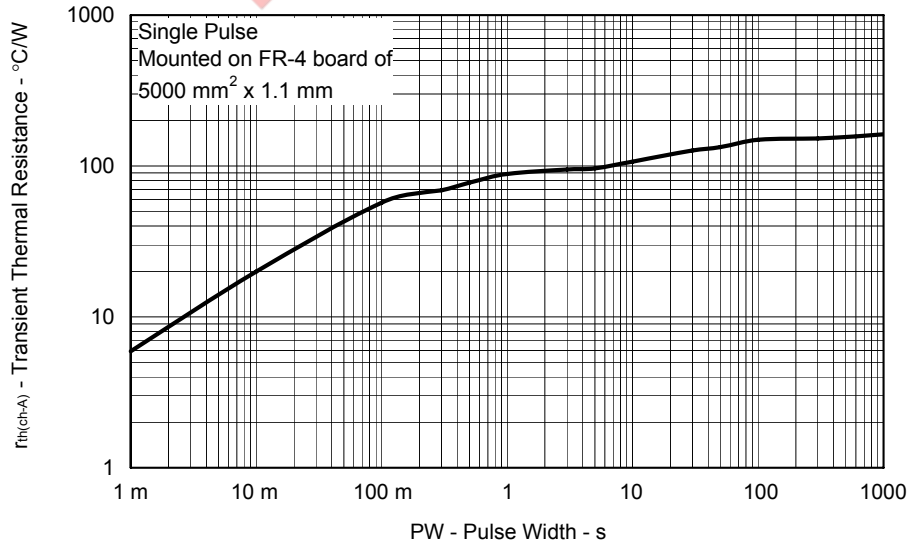
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



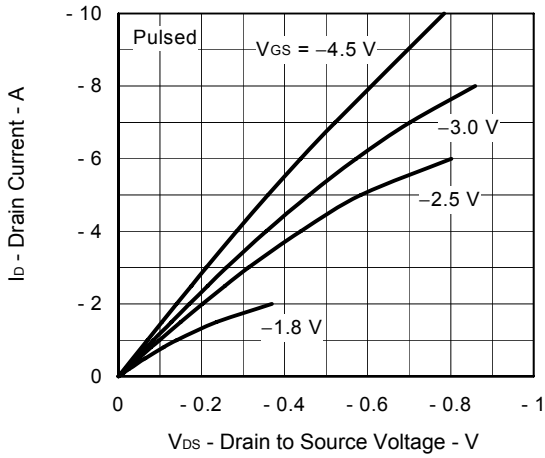
FORWARD BIAS SAFE OPERATING AREA



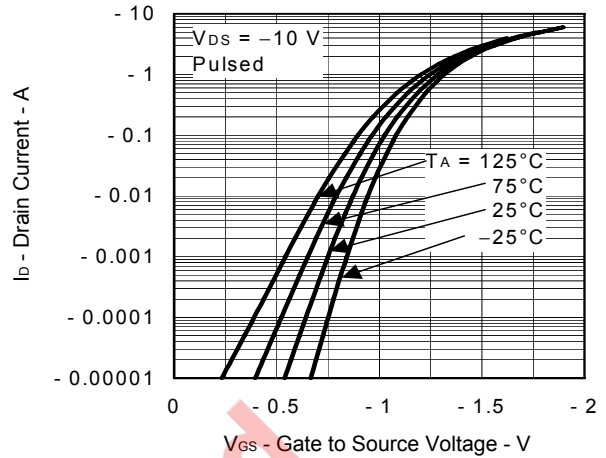
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



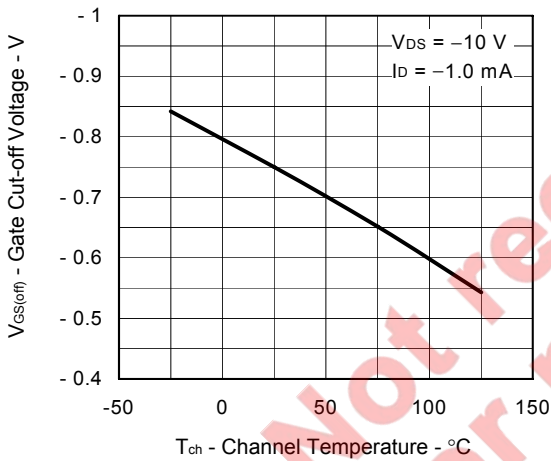
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



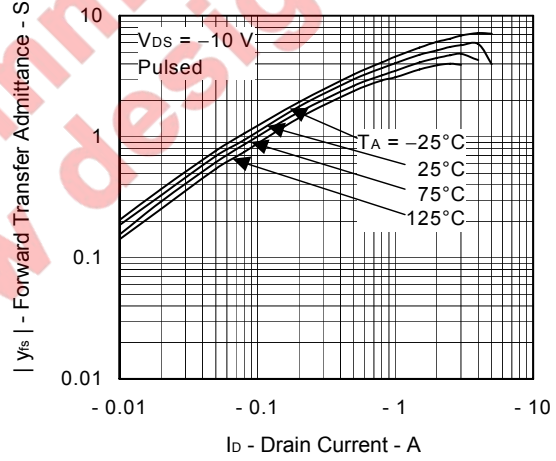
FORWARD TRANSFER CHARACTERISTICS



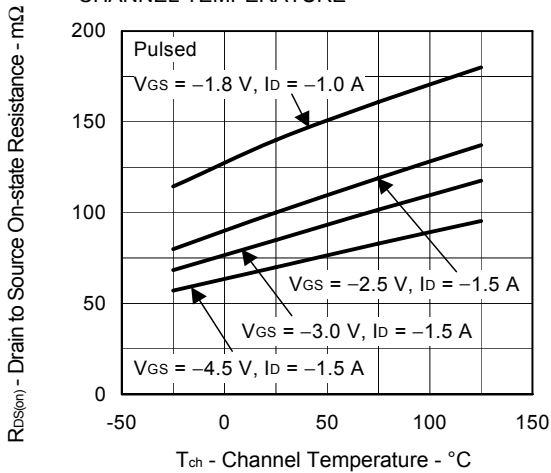
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



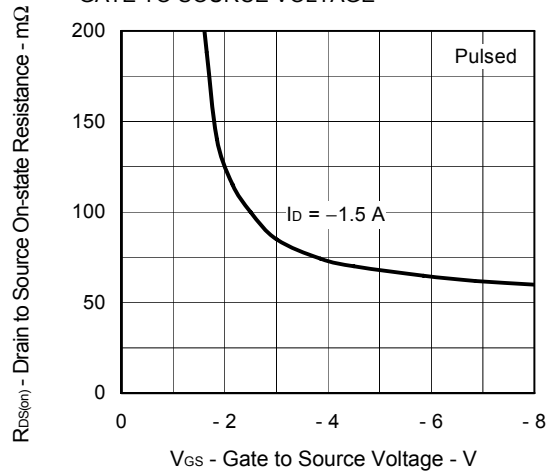
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

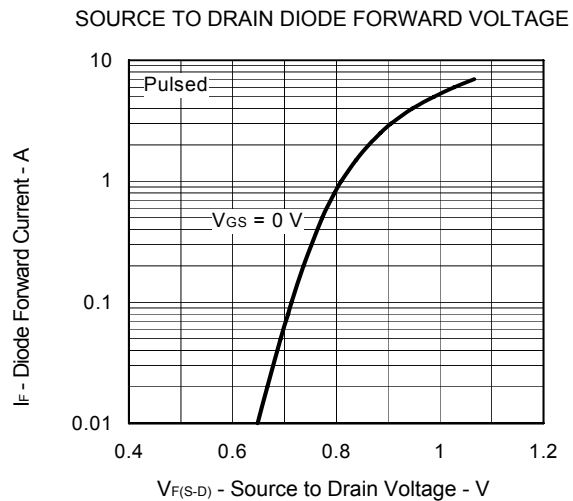
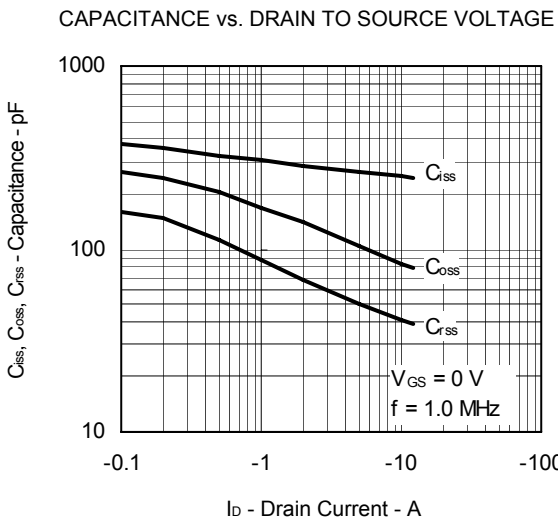
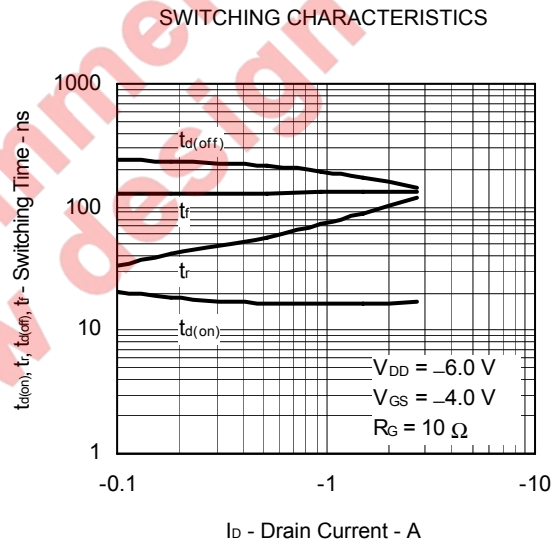
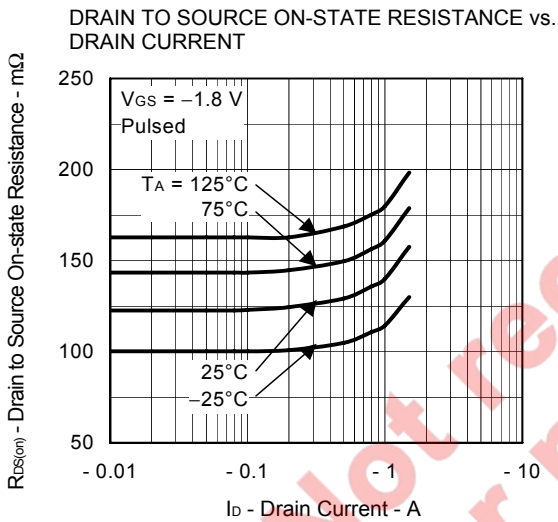
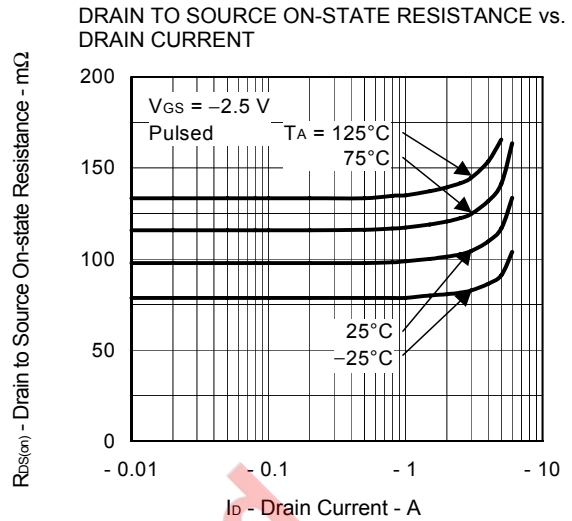
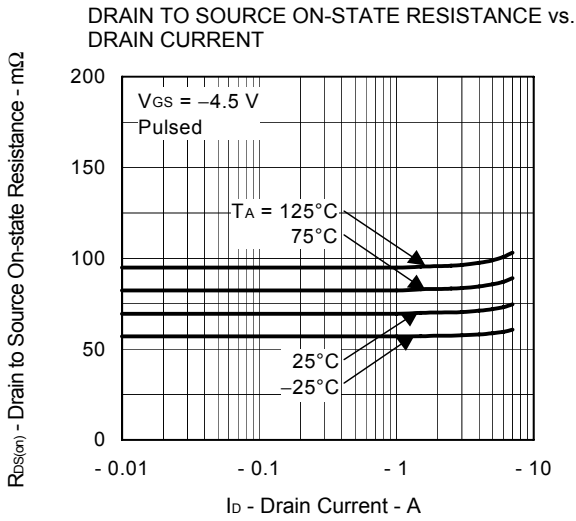


DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

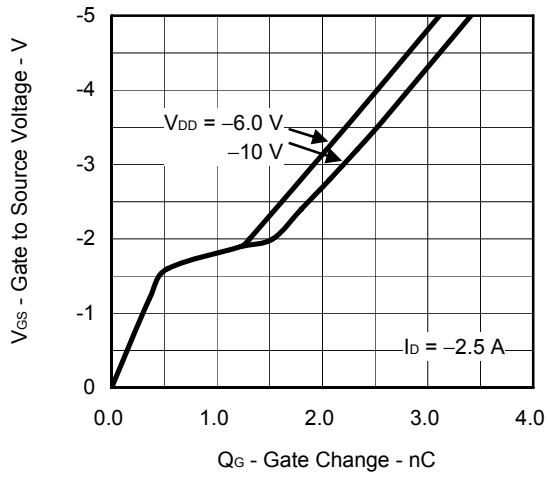


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE





DYNAMIC INPUT CHARACTERISTICS



Not recommend  
for new design



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