

R6030ENZ1

Nch 600V 30A Power MOSFET

V_{DSS}	600V
R _{DS(on)} (Max.)	0.130Ω
I _D	30A
P_D	120W

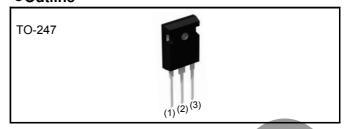
Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V_{GSS}) guaranteed to be $\pm 20V$.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating; RoHS compliant

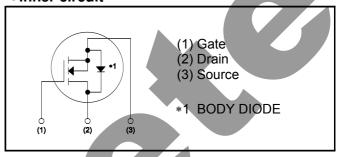
Application

Switching Power Supply

Outline



•Inner circuit



or dekaging specifications					
	Packaging	Tube			
	Reel size (mm)	-			
Typo	Tape width (mm)	-			
Type	Basic ordering unit (pcs)	450			
	Taping code	C9			
	Marking	R6030ENZ1			

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{ m DSS}$	600	V
Continuous drain current T _c = 25°C	I _D ^{*1}	±30	А
$T_c = 100^{\circ}C$	l _D *1	±16.3	А
Pulsed drain current	I _{D,pulse} *2	±80	А
Gate - Source voltage	V_{GSS}	±20	V
Avalanche energy, single pulse	E _{AS} *3	636	mJ
Avalanche energy, repetitive	E _{AR} *3	0.96	mJ
Avalanche current, repetitive	I _{AR}	5.2	Α
Power dissipation $(T_c = 25^{\circ}C)$	P_{D}	120	W
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C
Reverse diode dv/dt	dv/dt *4	15	V/ns

Absolute maximum ratings

Parameter	Symbol	Conditions	Values	Unit
Drain - Source voltage slope	dv/dt	$V_{DS} = 480V$ $T_j = 25^{\circ}C$	50	V/ns

●Thermal resistance

Parameter	Symbol	Values			Unit
r arameter	Gymbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	-	1.04	°C/W
Thermal resistance, junction - ambient	R_{thJA}	-	-	30	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}		1	265	°C

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
- arameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	-	V
		$V_{DS} = 600V, V_{GS} = 0V$				
Zero gate voltage drain current	I _{DSS}	T _j = 25°C	-	0.1	100	μΑ
		T _j = 125°C	-	-	1000	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	1	ı	±100	nA
Gate threshold voltage	V _{GS (th)}	V_{DS} = 10V, I_D = 1mA	2	ı	4	V
		$V_{GS} = 10V, I_D = 14.5A$				
Static drain - source on - state resistance	$R_{DS(on)}^{}^{*5}}$	T _j = 25°C	-	0.115	0.130	Ω
		T _j = 125°C		0.255	-	
Gate input resistance	R_G	f = 1MHz, open drain	-	3.6	-	Ω

●Electrical characteristics (T_a = 25°C)

Parameter	Cumbal	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g _{fs} *5	$V_{DS} = 10V, I_{D} = 15A$	8	16	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	2100	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	1900	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	190	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V	-	82		n H
Effective output capacitance, time related	C _{o(tr)}	V _{DS} = 0V to 480V	-	400		pF
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 300V$, $V_{GS} = 10V$		40	-	
Rise time	t _r *5	I _D = 15A	Y-	5 5	-	20
Turn - off delay time	t _{d(off)} *5	$R_L = 20\Omega$	(F)	190	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	60	-	

•Gate Charge characteristics $(T_a = 25^{\circ}C)$

Parameter	Symbol Conditions		Values			Unit
r ai ai nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Qg *5	V _{DD} ≈ 300V	-	85	-	
Gate - Source charge	Q _{gs} *5	I _D = 30A	-	15	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 10V	-	45	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 300V$, $I_D = 30A$	-	6.5	-	V

^{*1} Limited only by maximum temperature allowed.

^{*2} $P_W \leq 10 \mu s,$ Duty cycle $\leq 1\%$

^{*3} I_D = 5.2A, V_{DD} = 50V

^{*4} Reference measurement circuits Fig.5-1.

^{*5} Pulsed

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai ii etei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _c = 25°C	-	ı	30	А
Inverse diode direct current, pulsed	I _{SM} *2	1 c = 20 G	-	-	80	A
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = 30A$	-	-	1.5	V
Reverse recovery time	t _{rr} *5		-	660	-	ns
Reverse recovery charge	Q _{rr} *5	I _S = 30A di/dt = 100A/μs	-	15	1.	μС
Peak reverse recovery current	I _{rrm} *5			45	-	Α

●Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	0.190		C _{th1}	0.0143	
R _{th2}	0.429	K/W	C _{th2}	0.322	Ws/K
R _{th3}	0.250		C _{th3}	14.7	

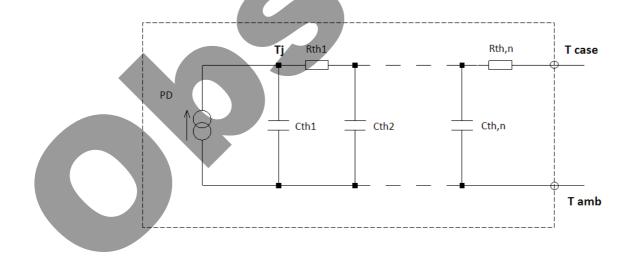
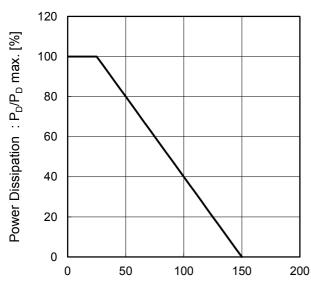


Fig.1 Power Dissipation Derating Curve



Junction Temperature : T_i [°C]

Resistance vs. Pulse Width Normalized Transient Thermal Resistance : r_(t) 1000 $T_a = 25^{\circ}C$ Single Pulse 100 $R_{th(ch-a)(t)} = r_{(t)} \times R_{th(ch-a)}$ $R_{th(ch-a)} = 30^{\circ}C/W$ 10 1 0.1 0.01 D = 0.50.001 D = 0.1 D = 0.05

0.0001

0.00001

0.0001 0.001 0.01

Fig.2 Normalized Transient Thermal

Pulse Width: Pw [s]

0.1

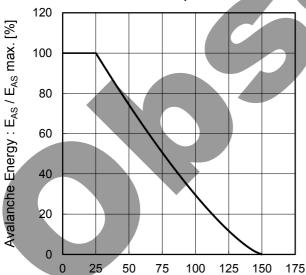
D = 0.01D = Single

100

1000

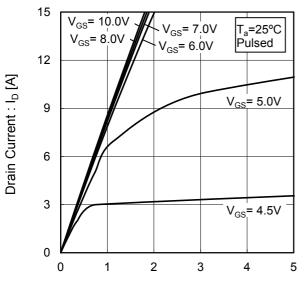
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Fig.3 Avalanche Energy Derating Curve vs Junction Temperature



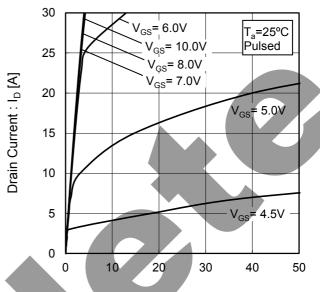
Junction Temperature : T_i [°C]

Fig.4 Typical Output Characteristics(I)



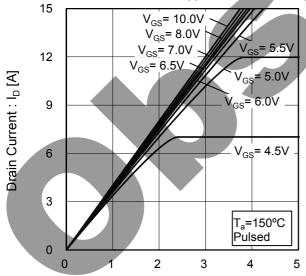
Drain - Source Voltage : V_{DS} [V]

Fig.5 Typical Output Characteristics(II)



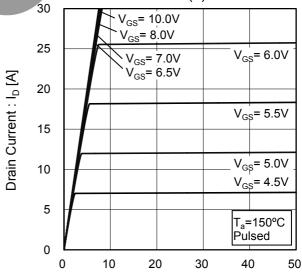
Drain - Source Voltage : V_{DS} [V]

Fig.6 T_j = 150°C Typical Output Characteristics(I)



Drain - Source Voltage: V_{DS} [V]

Fig.7 T_j = 150°C Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

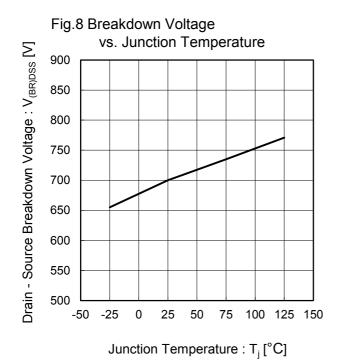


Fig.9 Typical Transfer Characteristics

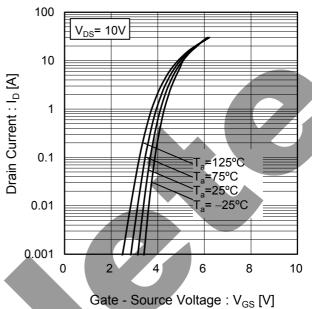


Fig.10 Gate Threshold Voltage vs. Junction Temperature

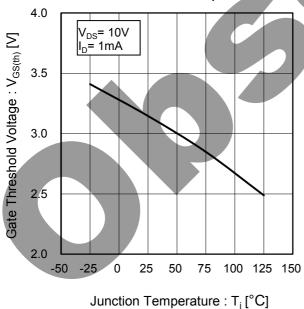


Fig.11 Transconductance vs. Drain Current

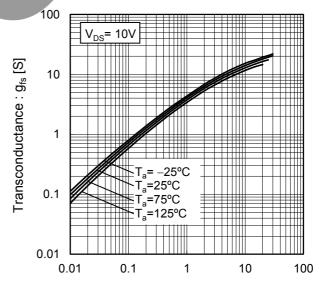
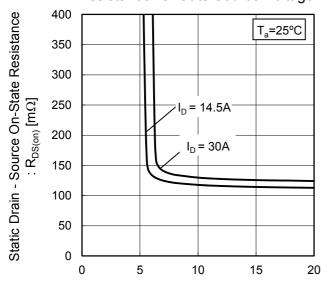


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : V_{GS} [V]

Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature

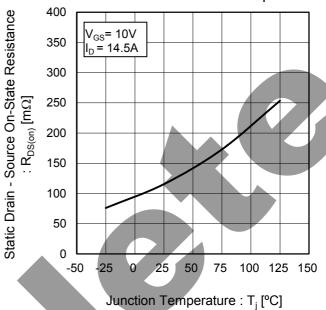


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current

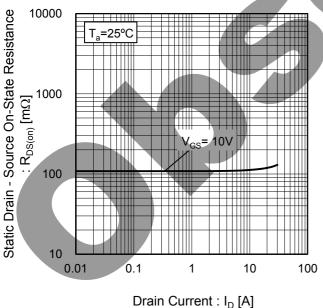


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current

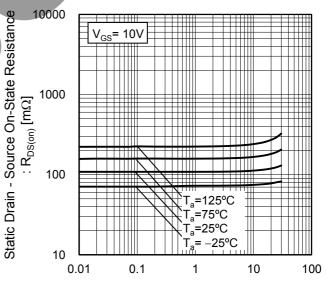
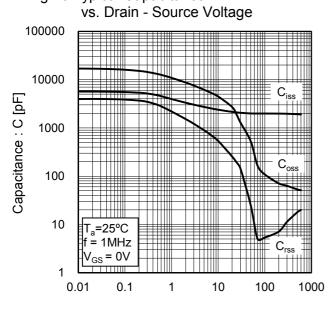
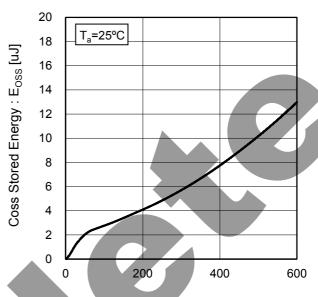


Fig.16 Typical Capacitance



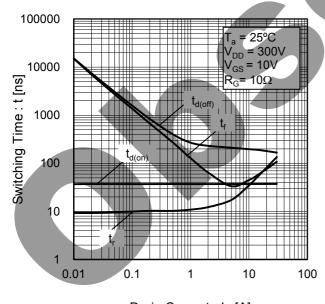
Drain - Source Voltage : V_{DS} [V]

Fig.17 Coss Stored Energy



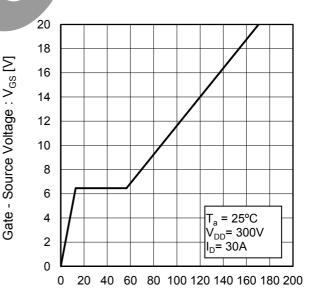
Drain - Source Voltage : V_{DS} [V]

Fig.18 Switching Characteristics

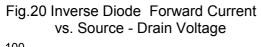


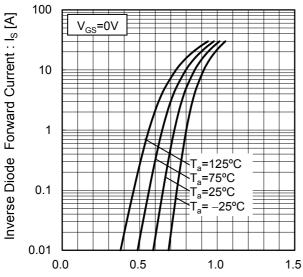
Drain Current : I_D [A]

Fig.19 Dynamic Input Characteristics



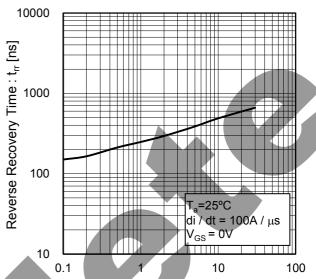
Total Gate Charge : Q_g [nC]



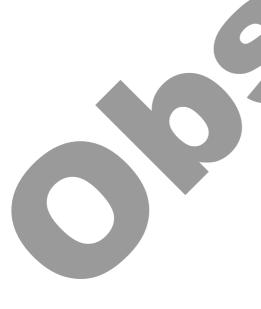


Source - Drain Voltage : V_{SD} [V]

Fig.21 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

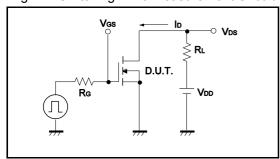


Fig.2-1 Gate Charge Measurement Circuit

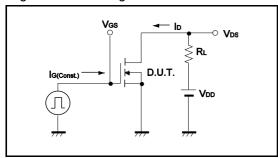


Fig.3-1 Avalanche Measurement Circuit

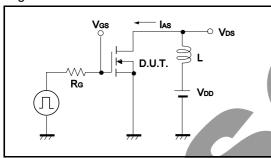


Fig.4-1 dv/dt Measurement Circuit

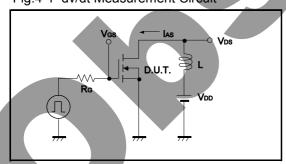


Fig.5-1 di/dt Measurement Circuit

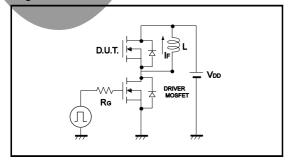


Fig.1-2 Switching Waveforms

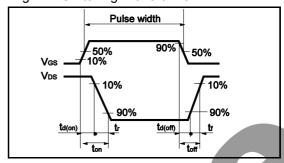


Fig.2-2 Gate Charge Waveform

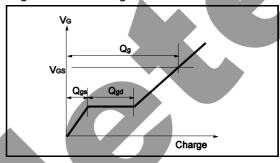


Fig.3-2 Avalanche Waveform

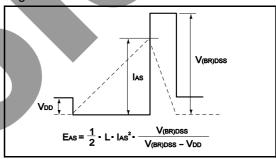


Fig.4-2 dv/dt Waveform

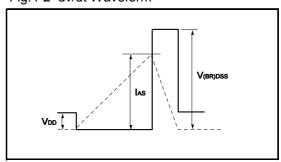
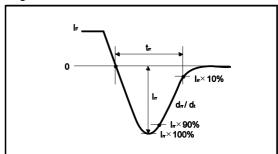
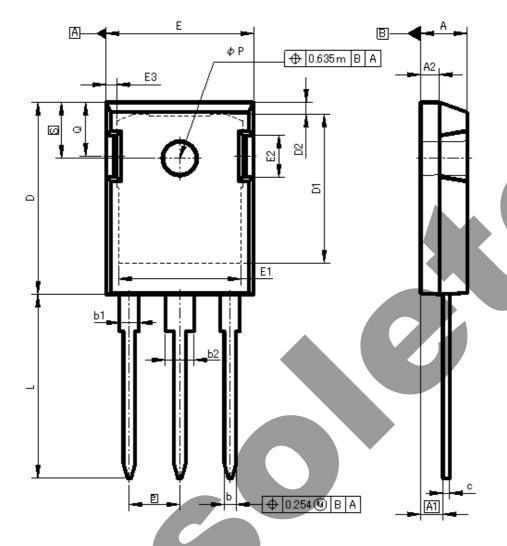


Fig.5-2 di/dt Waveform



●Dimensions (Unit : mm)

TO-247



DIM	MILIMI	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A	4.83	5.21	0.190	0.205
A1	2.29	2.54	0.090	0.100
A2	1.91	2.16	0.075	0.085
b	1.14	1.40	0.045	0.055
b1	1.91	2.20	0.075	0.087
b2	2.92	3.20	0.115	0.126
С	0.61	0.80	0.024	0.031
D	20.80	21.34	0.819	0.840
D1	17.43	17.83	0.686	0.702
E	15.75	16.13	0.620	0.635
е	5.4	45	0.215	
N	3.0	00	3.0	00
L	19.81	20.57	0.780	0.810
L1	3.81	4.32	0.150	0.170
ФР	3.55	3.65	0.140	0.144
Q	5.59	6.20	0.220	0.244
S	6.	15	0.2	40

Dimension in mm / inches

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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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