



20V P-Channel Enhancement Mode MOSFET

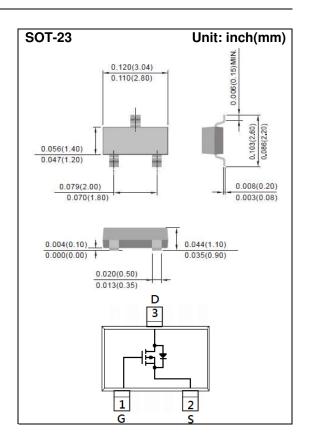
Voltage -20 V Current -3.4A

Features

- RDS(ON), VGS@-4.5V, ID@-3.4A<82m Ω
- RDS(ON) , VGS@-2.5V, ID@-2.2A<110mΩ
- RDS(ON) , VGS@-1.8V, ID@-1.2A<146mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A13



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	-3.4	Α
Pulsed Drain Current		I _{DM}	-13.6	Α
Power Dissipation	T _a =25°C	P_D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal resistance				
- Junction to Ambient (Note 3)		$R_{\theta JA}$	100	°C/W





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-0.4	-0.65	-1.2	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-3.4A	-	65	82	mΩ
		V _{GS} =-2.5V, I _D =-2.2A	-	82	110	
		V _{GS} =-1.8V, I _D =-1.2A	-	103	146	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Q_g	V _{DS} =-10V, I _D =-3.4A, V _{GS} =-4.5V (Note 1,2)	-	7	-	nC
Gate-Source Charge	Q_gs		-	1	-	
Gate-Drain Charge	Q_gd		-	1.8	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	522	-	pF
Output Capacitance	Coss		-	55	-	
Reverse Transfer Capacitance	Crss	I-1.UIVIHZ	-	40	-	
Switching						
Turn-On Delay Time	td _(on)	\/ - 40\/ I - 2.4A	-	10	-	ns
Turn-On Rise Time	tr	V_{DD} =-10V, I_{D} =-3.4A, V_{GS} =-4.5V, R_{G} =6 Ω (Note 1.2)		4	-	
Turn-Off Delay Time	td _(off)		-	34	-	
Turn-Off Fall Time	tf	R _G -012		5		
Drain-Source Diode						
Maximum Continuous Drain-Source					-1.5	Α
Diode Forward Current	I _S		_	_	-1.5	^
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V	-	0.77	-1.2	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





TYPICAL CHARACTERISTIC CURVES

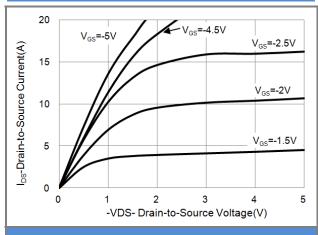


Fig.1 On-Region Characteristics

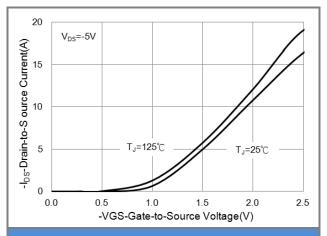


Fig.2 Transfer Characteristics

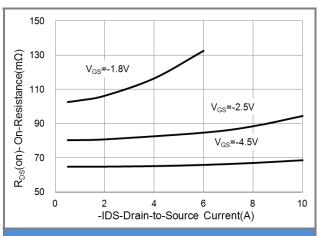


Fig.3 On-Resistance vs. Drain Current

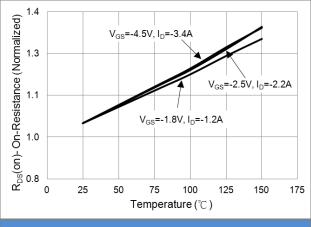
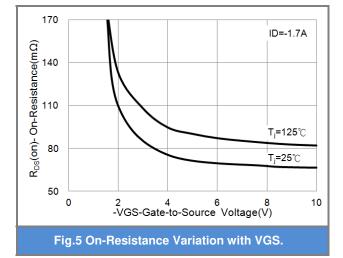
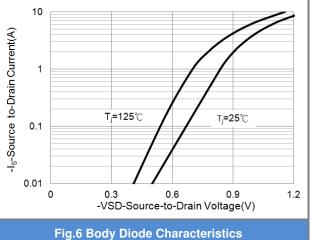


Fig.4 On-Resistance vs. Junction temperature









TYPICAL CHARACTERISTIC CURVES

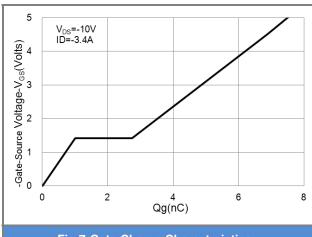
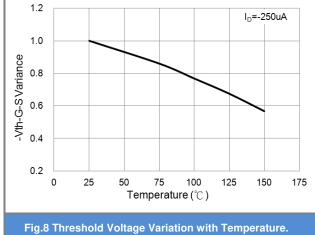


Fig.7 Gate-Charge Characteristics



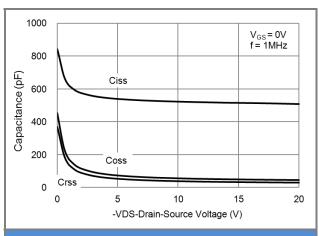


Fig.9 Threshold Voltage Variation with Temperature.

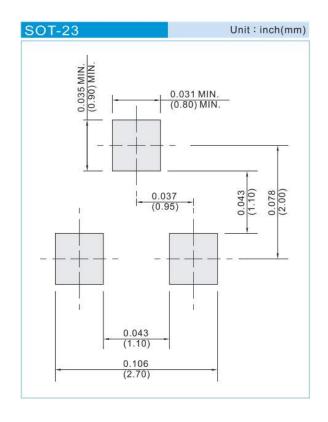




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJA3413_R1_00001	SOT-23	3K pcs / 7" reel	A13	Halogen free
PJA3413_R2_00001	SOT-23	12K pcs / 13" reel	A13	Halogen free

MOUNTING PAD LAYOUT







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