



1 CHANNEL UNIDIRECTIONAL TVS

Product Summary

V _{BR} (Min)	IPP (Max)	Ст (Тур)
6.5V	10A	60pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD and Surge. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral

Features

- Low Profile Package (0.30mm Typical) and Ultra-Small PCB Footprint Area (0.61mm × 0.3mm Max) Suitable for Compact Portable Electronics
- One Channel of ESD and Surge Protection
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- Provides Surge and Lightning Protection per IEC 61000-4-5 Standard: IPP Max 10A
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: X3-DFN0603-2
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin over Copper Leadframe, Solderable per MIL-STD-202, Method 208³
- Weight: 0.0002 grams (Approximate)

X3-DFN0603-2



Top View



Bottom View



Device Schematic

Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity
D6V3M1U2LP3-7	Commercial	M1	7	8	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



M1 = Product Type Marking Code
Bar Denotes Pin1 or Cathode Side



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation (Pin1 to Pin2)	P _{PP}	120	W	8/20μs, per Figure 3
Peak Pulse Current	IPP	10	Α	8/20μs, per Figure 3
ESD Protection—Contact Discharge	VESD_CONTACT	±30	kV	IEC 61000-4-2 Standard
ESD Protection—Air Discharge	Vesd_air	±30	kV	IEC 61000-4-2 Standard

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	RөJA	500	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

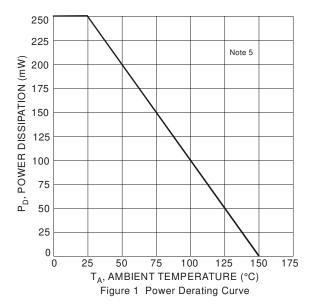
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

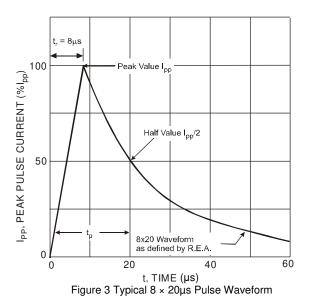
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V _{RWM}	_	_	6.3	V	_
Reverse Current (Note 6)	IR	_	_	1.0	μΑ	V _R = V _{RWM}
Reverse Breakdown Voltage	V _{BR}	6.5	_	8.5	V	I _R = 1mA
Reverse Clamping Voltage (Note 7)	VcL	_	7.2	9.5	V	IPP = 1A, tp = 8/20µs
		_	10.3	12.0		IPP = 10A, tP = 8/20μs
ESD Clamping Voltage (Note 8)	Vc	_	7.7	_	V	I _{PP} = 4A, t _P = 100ns
		_	9.5	_		I _{PP} = 16A, t _P = 100ns
Capacitance	Ст	_	60	_	pF	V _R = 0V, f = 1MHz

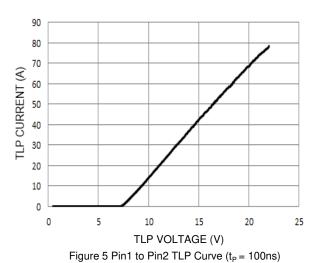
Notes:

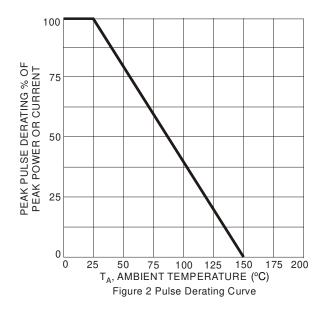
- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an 8x20µs peak pulse current (IPP) waveform.
- 8. Transmission Line Pulse Test (TLP) settings: $t_P = 100$ ns, $t_R = 10$ ns, l_{TLP} and V_{TLP} averaging window is from 70ns to 90ns.

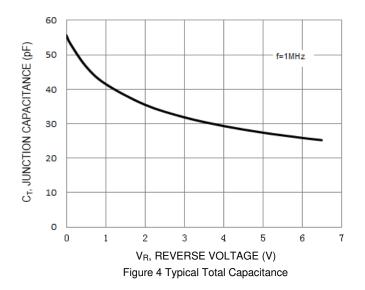












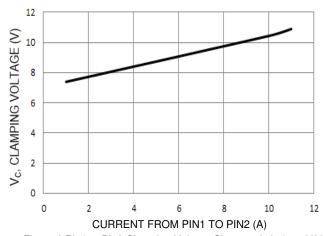


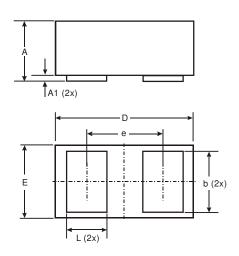
Figure 6 Pin1 to Pin2 Clamping Voltage Characteristic (t_P = 8/20 μ s)



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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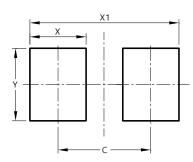


X3-DFN0603-2					
Dim	Min	Max	Тур		
Α	0.27	0.35	0.30		
A1	0.00	0.03	0.02		
b	0.19	0.29	0.24		
D	0.595	0.645	0.62		
Е	0.295	0.345	0.32		
е	-	-	0.355		
Ĺ	0.14	0.24	0.19		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DFN0603-2



Dimensions	Value		
Dillicitatoria	(in mm)		
С	0.380		
Х	0.230		
X1	0.610		
Υ	0.300		



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