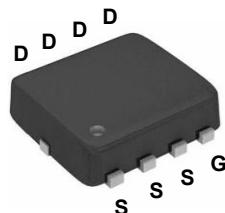
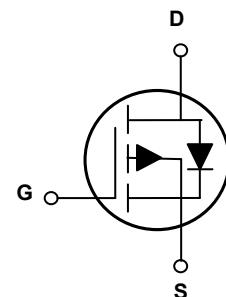


## Main Product Characteristics

$BV_{(BR)DSS}$	-40V
$R_{DS(ON)}$	14mΩ
$I_D$	-38A



PPAK3x3



Schematic Diagram

## Features and Benefits

- Advanced MOSFET process technology
- Ideal for hand-held devices, battery protection and load switch
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



## Description

The SSFN4903 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

## Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	$V_{DS}$	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous ( $T_C=25^\circ\text{C}$ )	$I_D$	-38	A
Drain Current-Continuous ( $T_C=100^\circ\text{C}$ )		-24	
Drain Current-Pulsed <sup>1</sup>	$I_{DM}$	-152	A
Single Pulse Avalanche Energy <sup>2</sup>	$E_{AS}$	130	mJ
Single Pulse Avalanche Current <sup>2</sup>	$I_{AS}$	51	A
Power Dissipation ( $T_C=25^\circ\text{C}$ )	$P_D$	52	W
Power Dissipation-Derate above 25°C		0.42	W/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.4	°C/W
Operating Junction Temperature Range	$T_J$	-55 To +150	°C
Storage Temperature Range	$T_{STG}$	-55 To +150	°C

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>On/Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$	-40	-	-	V
Drain-Source Leakage Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=-40\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=25^\circ\text{C}$	-	-	-1	$\mu\text{A}$
		$\text{V}_{\text{DS}}=-32\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=125^\circ\text{C}$	-	-	-10	$\mu\text{A}$
Gate-Source Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-15\text{A}$	-	11.3	14	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-8\text{A}$	-	15.6	21	$\text{m}\Omega$
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}, \text{I}_D=-250\mu\text{A}$	-1.0	-1.6	-2.5	V
Forward Transconductance	$\text{g}_{\text{fs}}$	$\text{V}_{\text{DS}}=-10\text{V}, \text{I}_D=-4\text{A}$	-	11	-	S
<b>Dynamic and Switching Characteristics</b>						
Total Gate Charge <sup>3,4</sup>	$\text{Q}_g$	$\text{V}_{\text{DS}}=-32\text{V}, \text{I}_D=-10\text{A}, \text{V}_{\text{GS}}=-4.5\text{V}$	-	22.2	40	nC
Gate-Source Charge <sup>3,4</sup>	$\text{Q}_{\text{gs}}$		-	8.2	16	
Gate-Drain Charge <sup>3,4</sup>	$\text{Q}_{\text{gd}}$		-	8.8	16	
Turn-On Delay Time <sup>3,4</sup>	$\text{t}_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=-20\text{V}, \text{R}_g=6\Omega, \text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-1\text{A}$	-	23	40	nS
Rise Time <sup>3,4</sup>	$\text{t}_r$		-	10	20	
Turn-Off Delay Time <sup>3,4</sup>	$\text{t}_{\text{d}(\text{off})}$		-	135	250	
Fall Time <sup>3,4</sup>	$\text{t}_f$		-	46	90	
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=-25\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{F}=1\text{MHz}$	-	2757	4000	pF
Output Capacitance	$\text{C}_{\text{oss}}$		-	240	360	
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		-	137	200	
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Continuous Source Current	$\text{I}_s$	$\text{V}_G=\text{V}_D=0\text{V}, \text{Force Current}$	-	-	-38	A
Pulsed Source Current	$\text{I}_{\text{SM}}$		-	-	-76	A
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=-1\text{A}, \text{T}_J=25^\circ\text{C}$	-	-	-1	V

Note:

1. Repetitive rating: Pulsed width limited by maximum junction temperature.
2.  $\text{V}_{\text{DD}}=25\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{L}=0.1\text{mH}, \text{I}_{\text{AS}}=51\text{A}, \text{R}_g=25\Omega$ , starting  $\text{T}_J=25^\circ\text{C}$ .
3. Pulse test: pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.

## Typical Electrical and Thermal Characteristic Curves

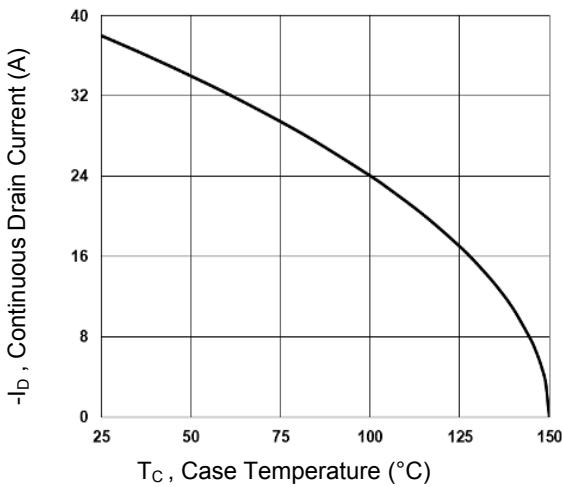


Figure 1. Continuous Drain Current vs. T<sub>c</sub>

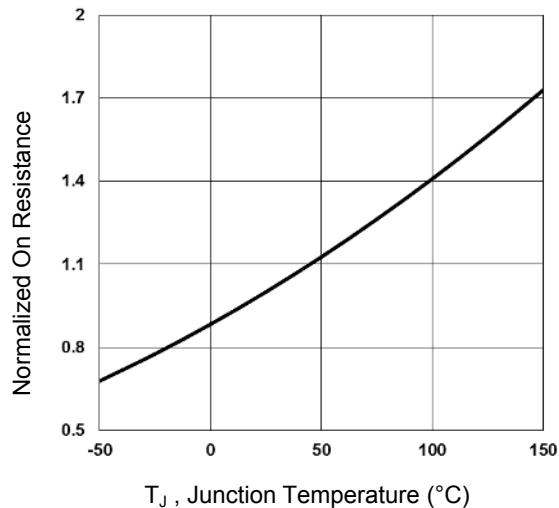


Figure 2. Normalized R<sub>DSON</sub> vs. T<sub>j</sub>

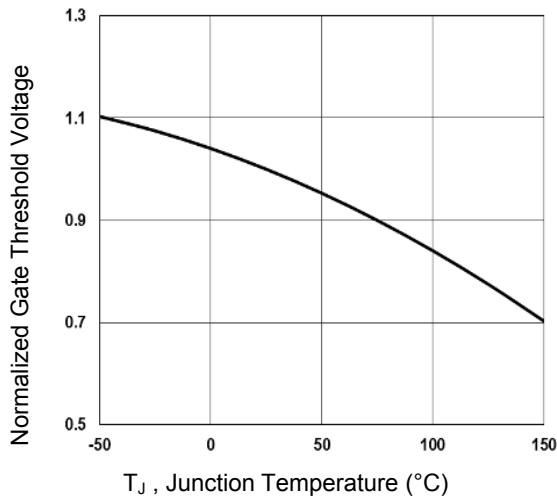


Figure 3. Normalized V<sub>th</sub> vs. T<sub>j</sub>

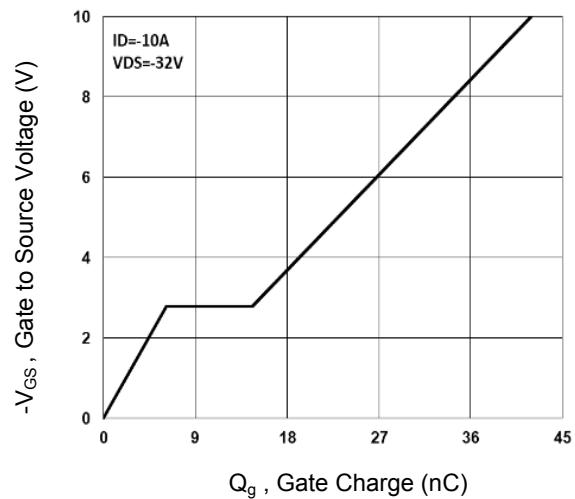


Figure 4. Gate Charge Waveform

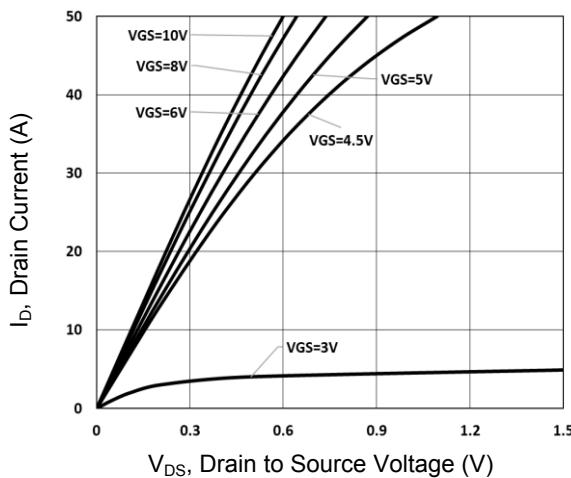


Figure 5. Typical Output Characteristics

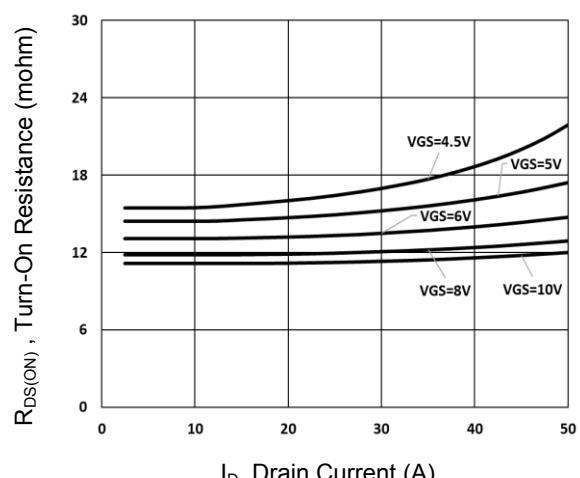
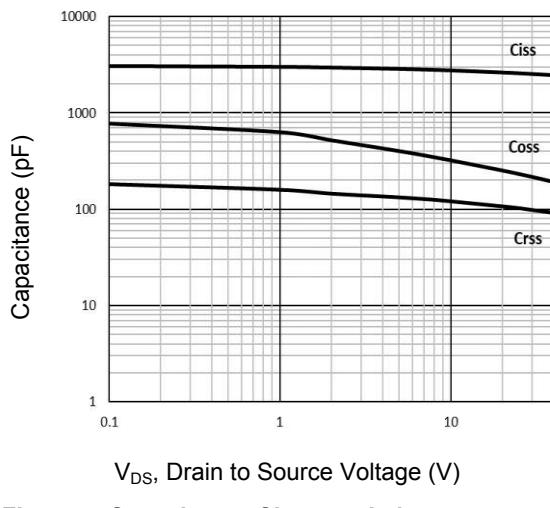


Figure 6. Turn-on Resistance vs. I<sub>D</sub>

## Typical Electrical and Thermal Characteristic Curves



$V_{DS}$ , Drain to Source Voltage (V)

Figure 7. Capacitance Characteristics

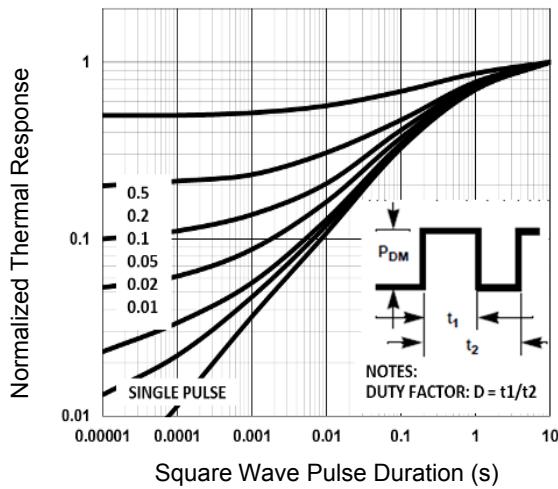


Figure 8. Normalized Transient Impedance

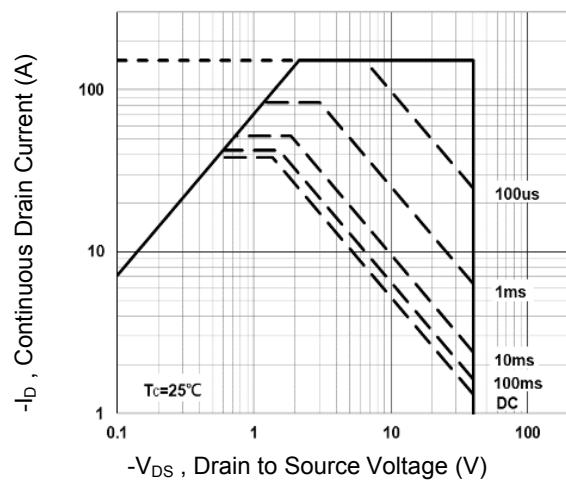


Figure 9. Maximum Safe Operation Area

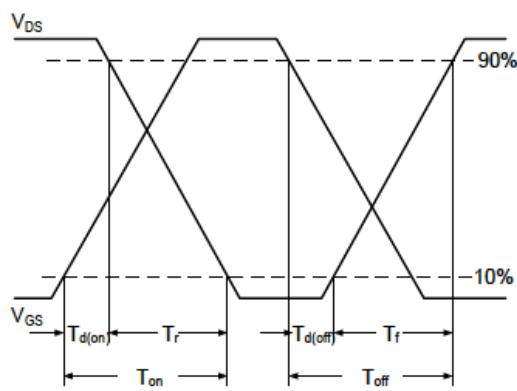


Figure 10. Switching Time Waveform

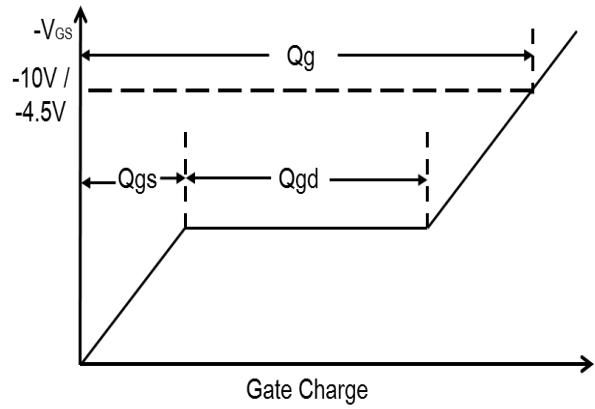
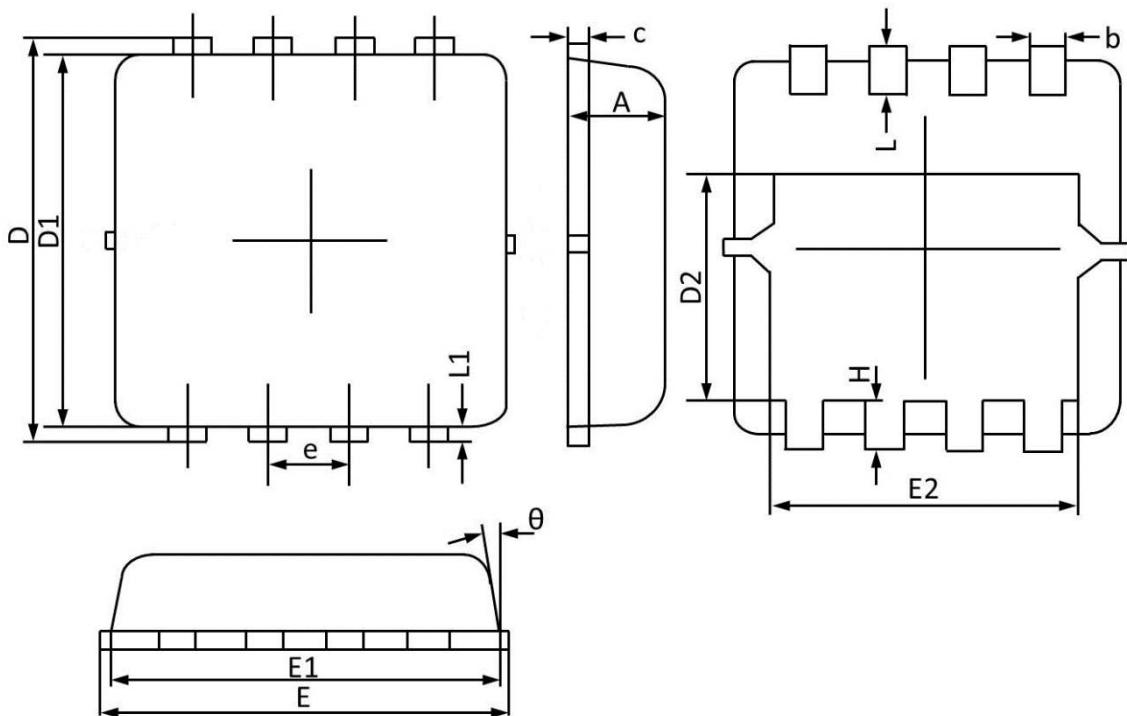


Figure 11. Gate Charge Waveform

### Package Outline Dimensions

**PPAK3x3**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
c	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
e	0.65BSC		0.026BSC	
H	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
θ	14°	6°	14°	6°

### Recommended Pad Layout

