



PBSS4041PZ-Q

60 V, 5.7 A PNP low VCEsat transistor

21 November 2022

Product data sheet

1. General description

PNP low V_{CEsat} transistor in a SOT223 (SC-73) medium power Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4041NZ-Q

2. Features and benefits

- Very low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High energy efficiency due to less heat generation
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

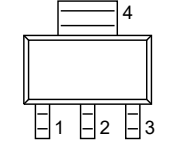
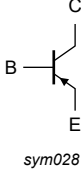
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-60	V
I_C	collector current		-	-	-5.7	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	-15	A
R_{CEsat}	collector-emitter saturation resistance	$I_C = -5$ A; $I_B = -500$ mA; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	29	43.5	m Ω

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 <p>SC-73 (SOT223)</p>	 <p>sym028</p>
2	C	collector		
3	E	emitter		
4	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4041PZ-Q	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4041PZ-Q	PB4041 PZ

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-60	V
V_{CEO}	collector-emitter voltage	open base		-	-60	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-5.7	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	-15	A
I_B	base current			-	-1	A
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	770	mW
			[2]	-	1700	mW
			[3]	-	2600	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

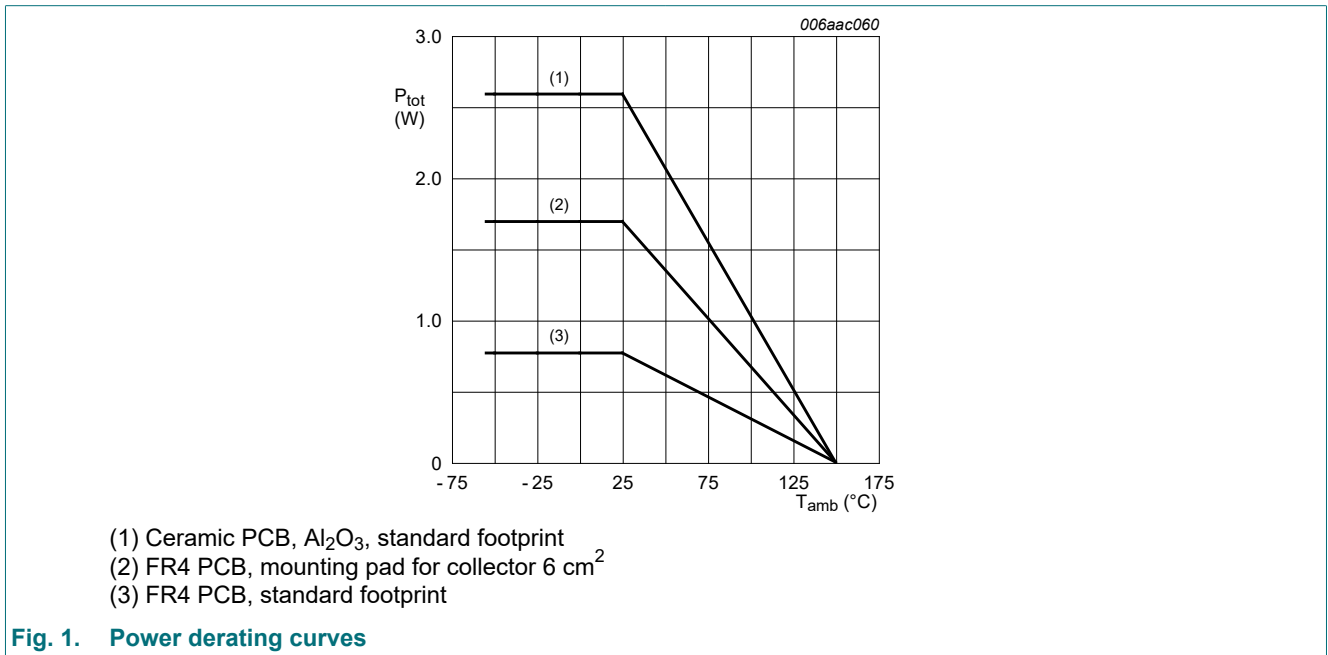


Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	160	K/W
			[2]	-	-	75	K/W
			[3]	-	-	50	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	11	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

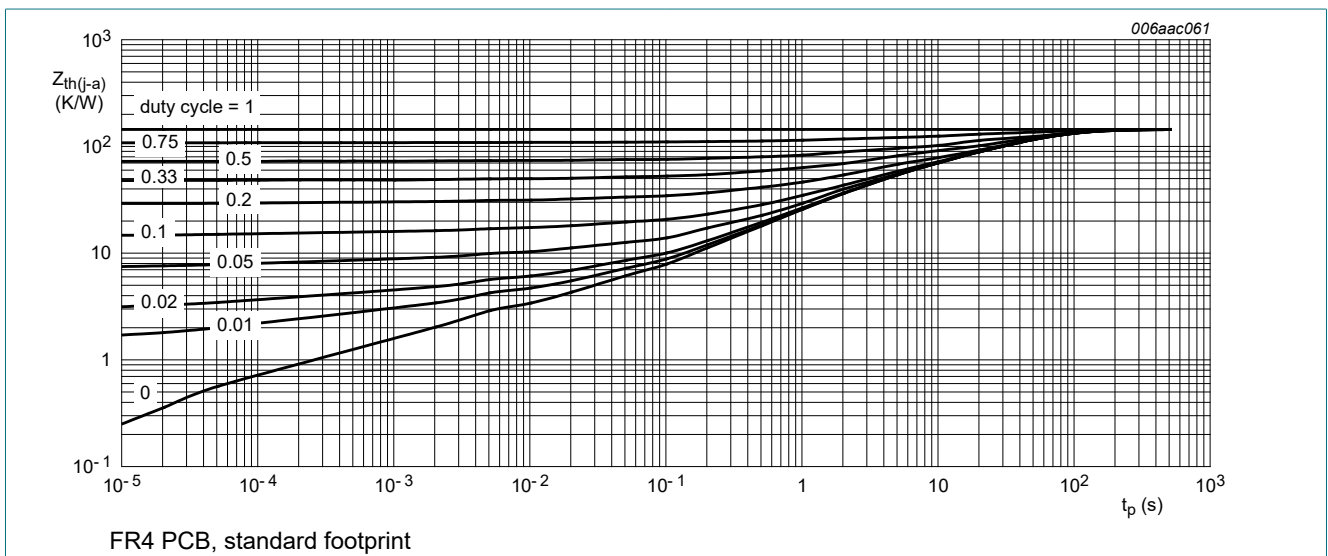


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

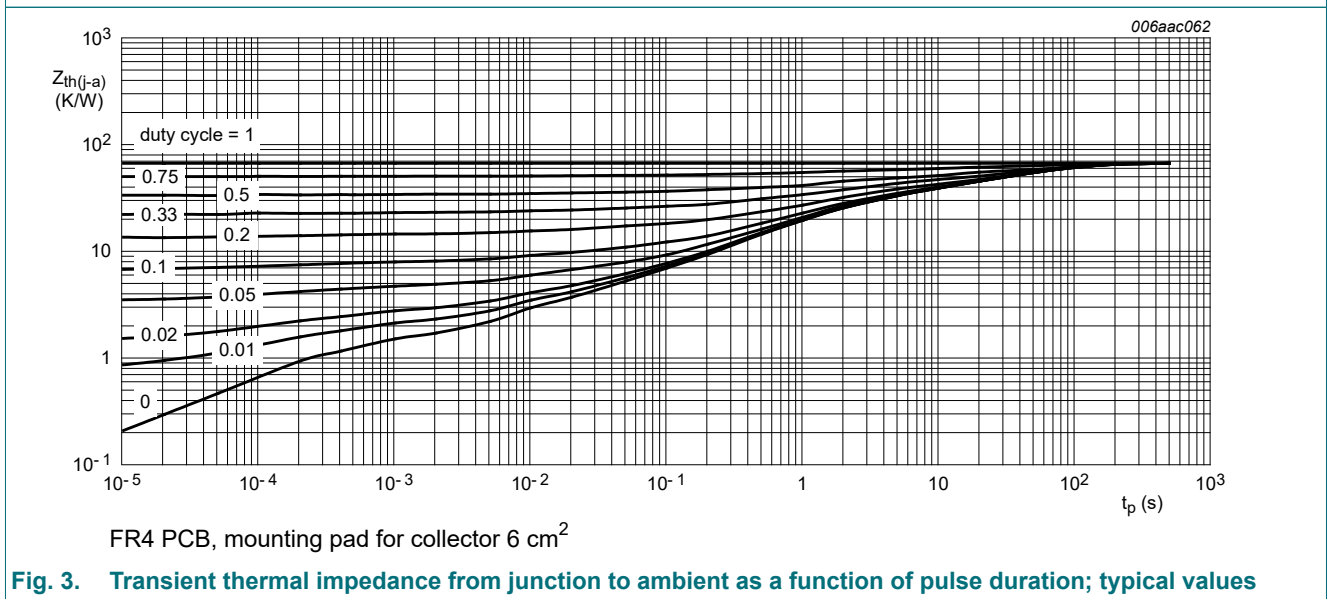
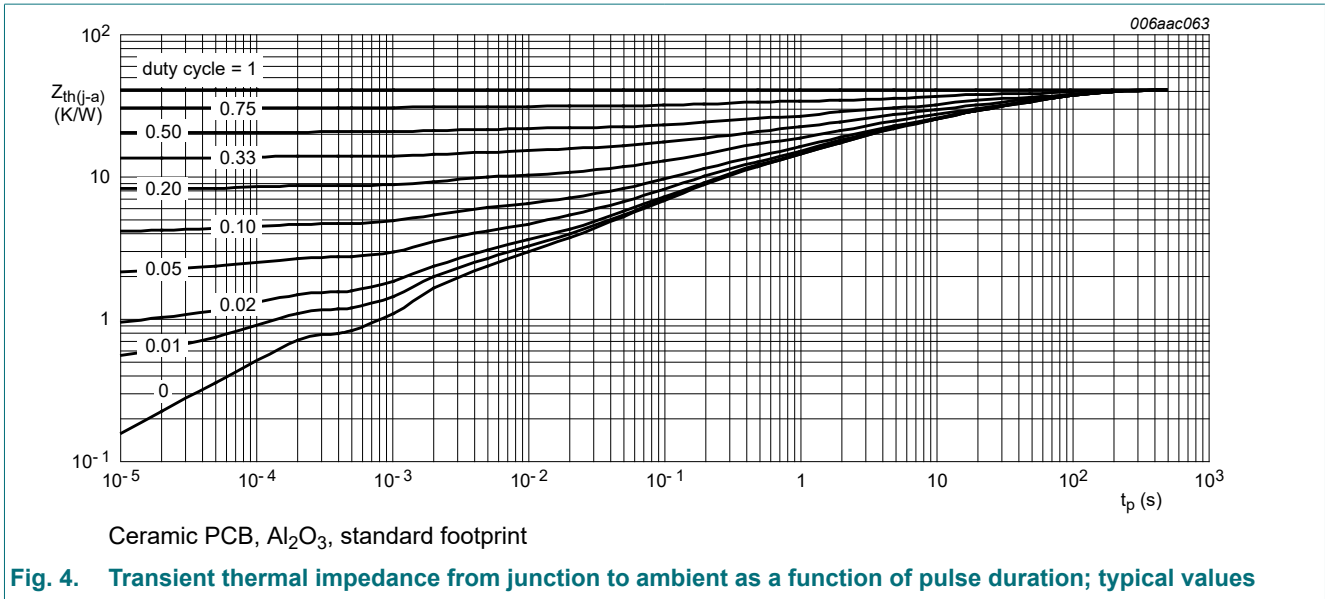


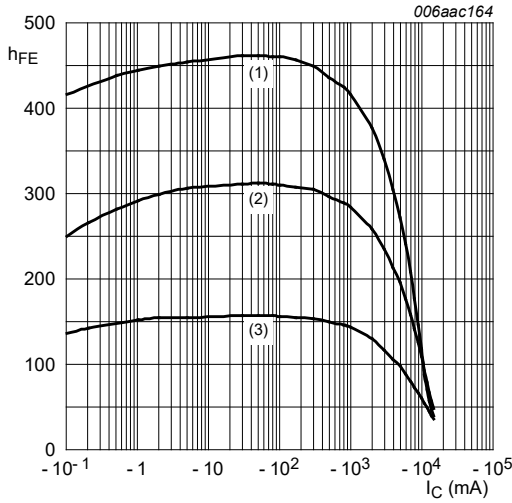
Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



10. Characteristics

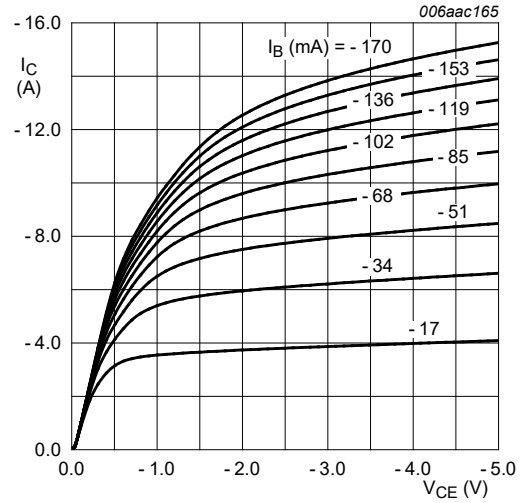
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -60 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
		V _{CB} = -60 V; I _E = 0 A; T _j = 150 °C	-	-	-55	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
I _{CES}	collector-emitter cut-off current	V _{CE} = -48 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -2 V; I _C = -500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	200	300	-	
		V _{CE} = -2 V; I _C = -1 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	200	300	-	
		V _{CE} = -2 V; I _C = -2 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	200	300	-	
		V _{CE} = -2 V; I _C = -4 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	150	250	-	
		V _{CE} = -2 V; I _C = -6 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	120	200	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -1 A; I _B = -50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-42	-63	mV
		I _C = -1 A; I _B = -10 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-82	-125	mV
		I _C = -2 A; I _B = -40 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-98	-150	mV
		I _C = -4 A; I _B = -200 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-130	-195	mV
		I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-115	-175	mV
		I _C = -6 A; I _B = -300 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-190	-285	mV
R _{CEsat}	collector-emitter saturation resistance	I _C = -5 A; I _B = -500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	29	43.5	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = -1 A; I _B = -100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-0.82	-0.9	V
		I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	-0.98	-1.05	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = -2 V; I _C = -2 A; T _{amb} = 25 °C	-	-0.74	-0.85	V
t _d	delay time	V _{CC} = -12.5 V; I _C = -1 A; I _{BoN} = -0.05 A; I _{Boff} = 0.05 A; T _{amb} = 25 °C	-	60	-	ns
t _r	rise time		-	60	-	ns
t _{on}	turn-on time		-	120	-	ns
t _s	storage time		-	530	-	ns
t _f	fall time		-	100	-	ns
t _{off}	turn-off time		-	630	-	ns
f _T	transition frequency		V _{CE} = -10 V; I _C = -100 mA; f = 100 MHz; T _{amb} = 25 °C	-	110	-
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	85	-	pF



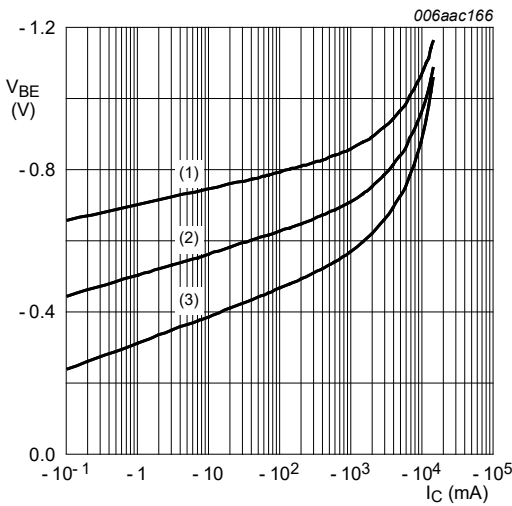
$V_{CE} = -2 V$
 (1) $T_{amb} = 100^\circ C$
 (2) $T_{amb} = 25^\circ C$
 (3) $T_{amb} = -55^\circ C$

Fig. 5. DC current gain as a function of collector current; typical values



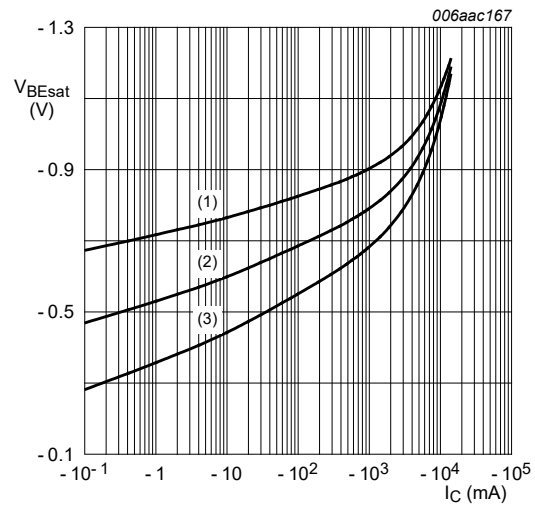
$T_{amb} = 25^\circ C$

Fig. 6. Collector current as a function of collector-emitter voltage; typical values



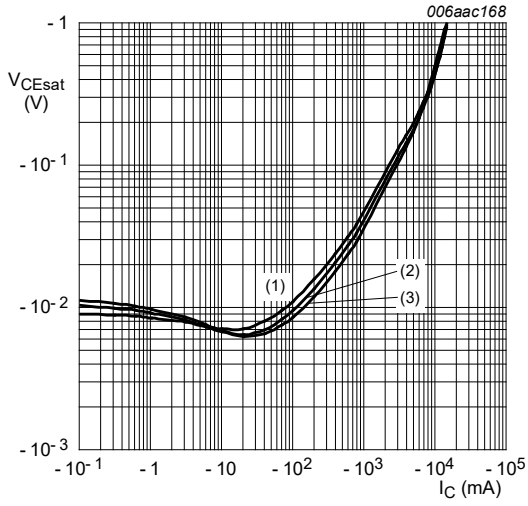
$V_{CE} = -2 V$
 (1) $T_{amb} = -55^\circ C$
 (2) $T_{amb} = 25^\circ C$
 (3) $T_{amb} = 100^\circ C$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



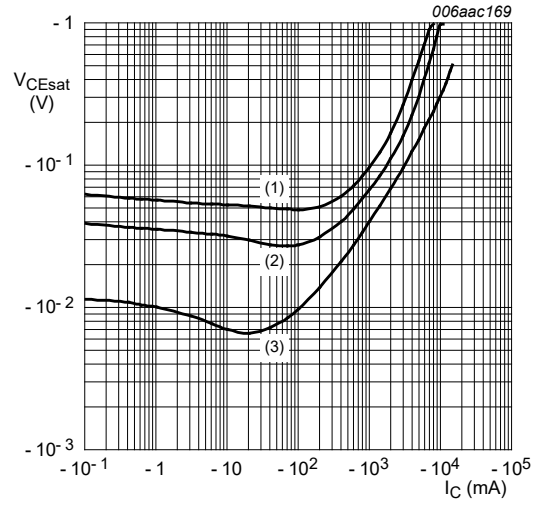
$I_C/I_B = 20$
 (1) $T_{amb} = -55^\circ C$
 (2) $T_{amb} = 25^\circ C$
 (3) $T_{amb} = 100^\circ C$

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values



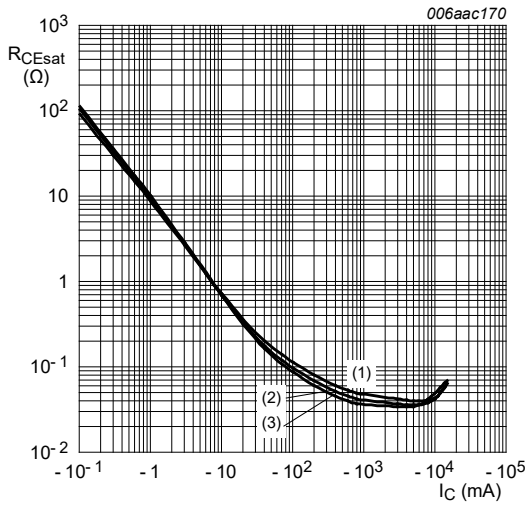
$I_C/I_B = 20$
 (1) $T_{amb} = 100\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = -55\text{ °C}$

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



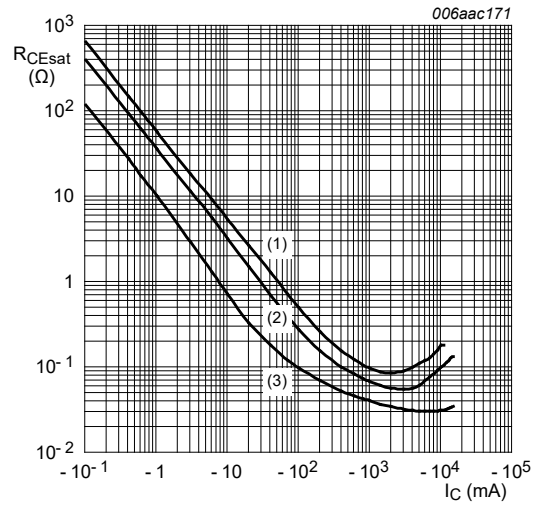
$T_{amb} = 25\text{ °C}$
 (1) $I_C/I_B = 100$
 (2) $I_C/I_B = 50$
 (3) $I_C/I_B = 10$

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values



$I_C/I_B = 20$
 (1) $T_{amb} = 100\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = -55\text{ °C}$

Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values



$T_{amb} = 25\text{ °C}$
 (1) $I_C/I_B = 100$
 (2) $I_C/I_B = 50$
 (3) $I_C/I_B = 10$

Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

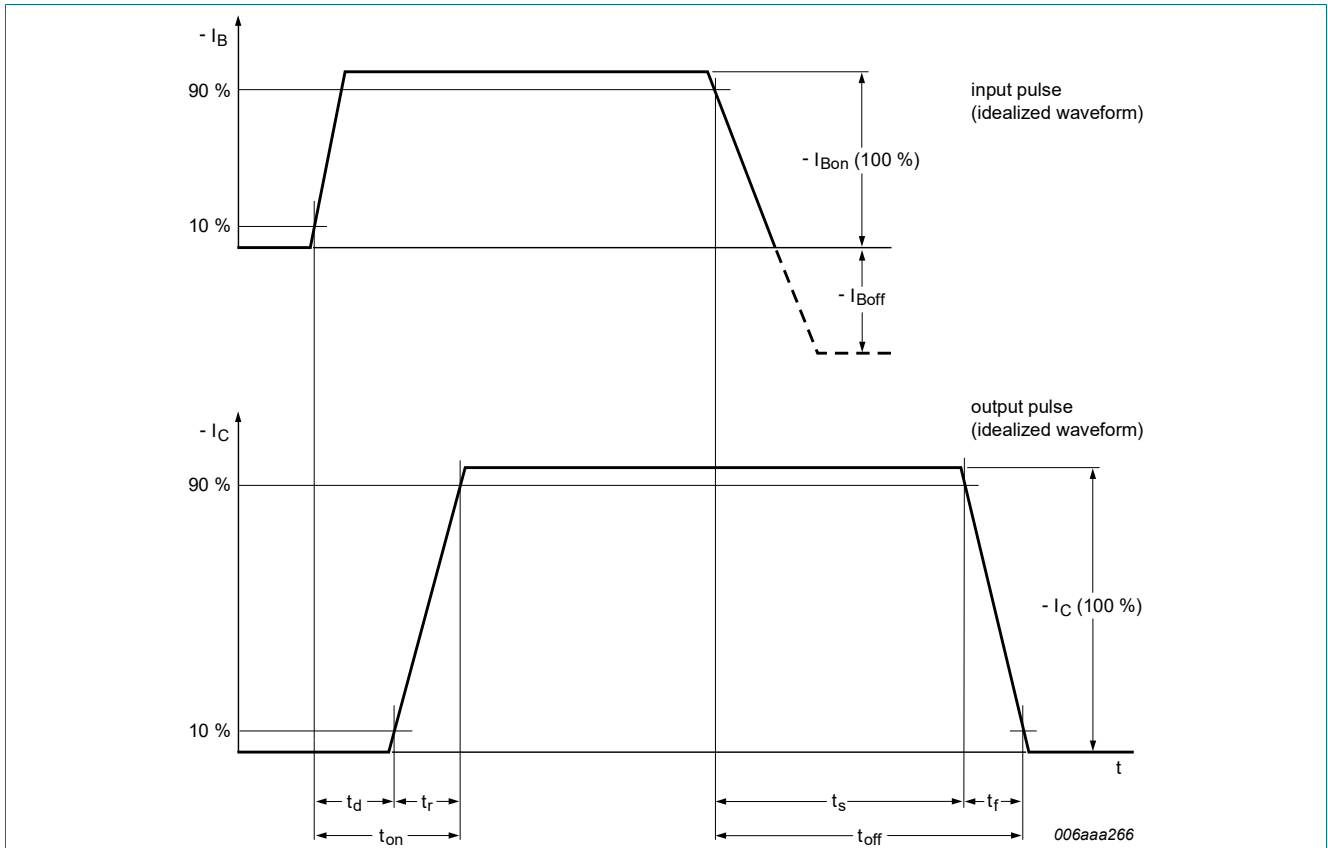


Fig. 13. BISS transistor switching time definition

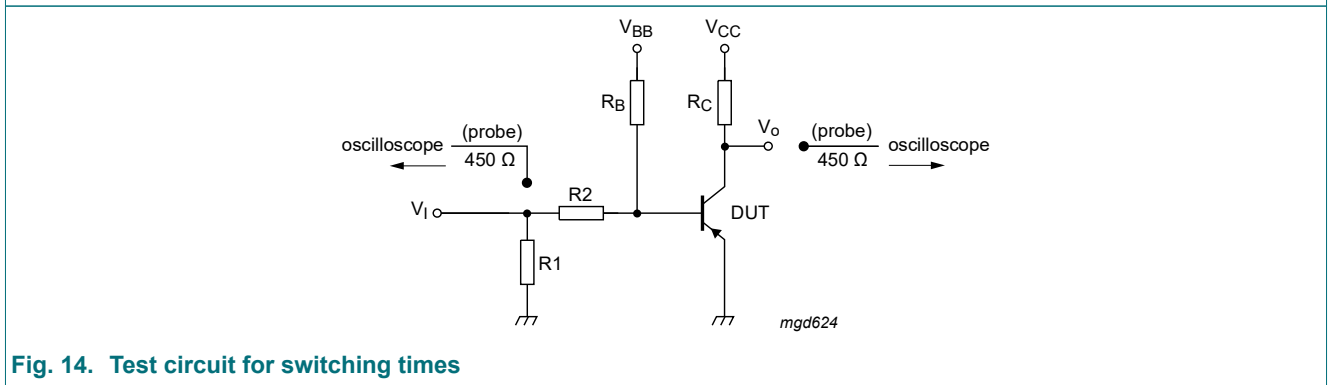


Fig. 14. Test circuit for switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

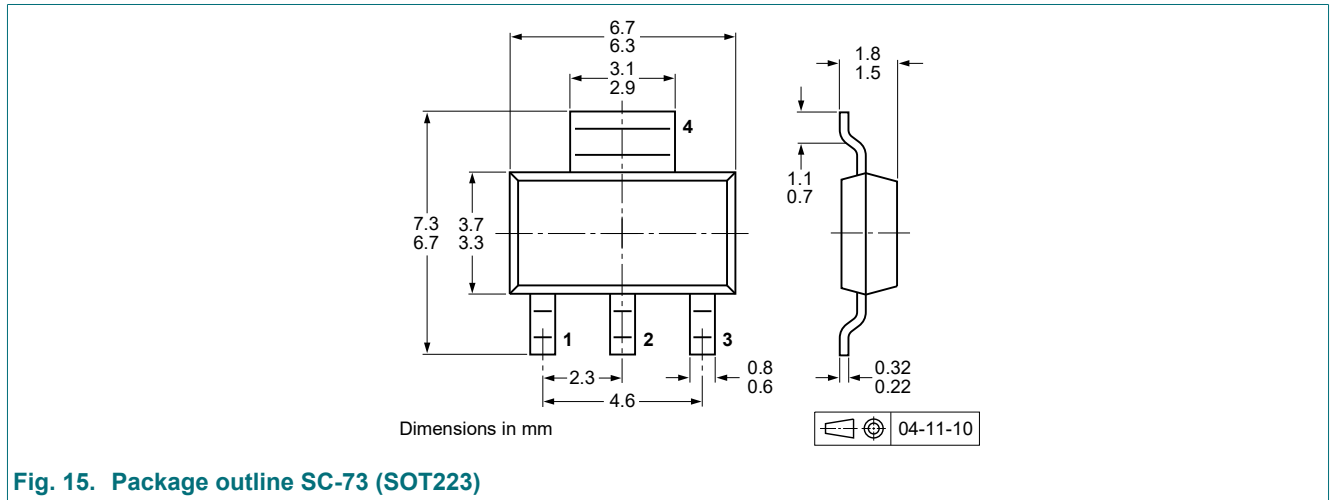


Fig. 15. Package outline SC-73 (SOT223)

13. Soldering

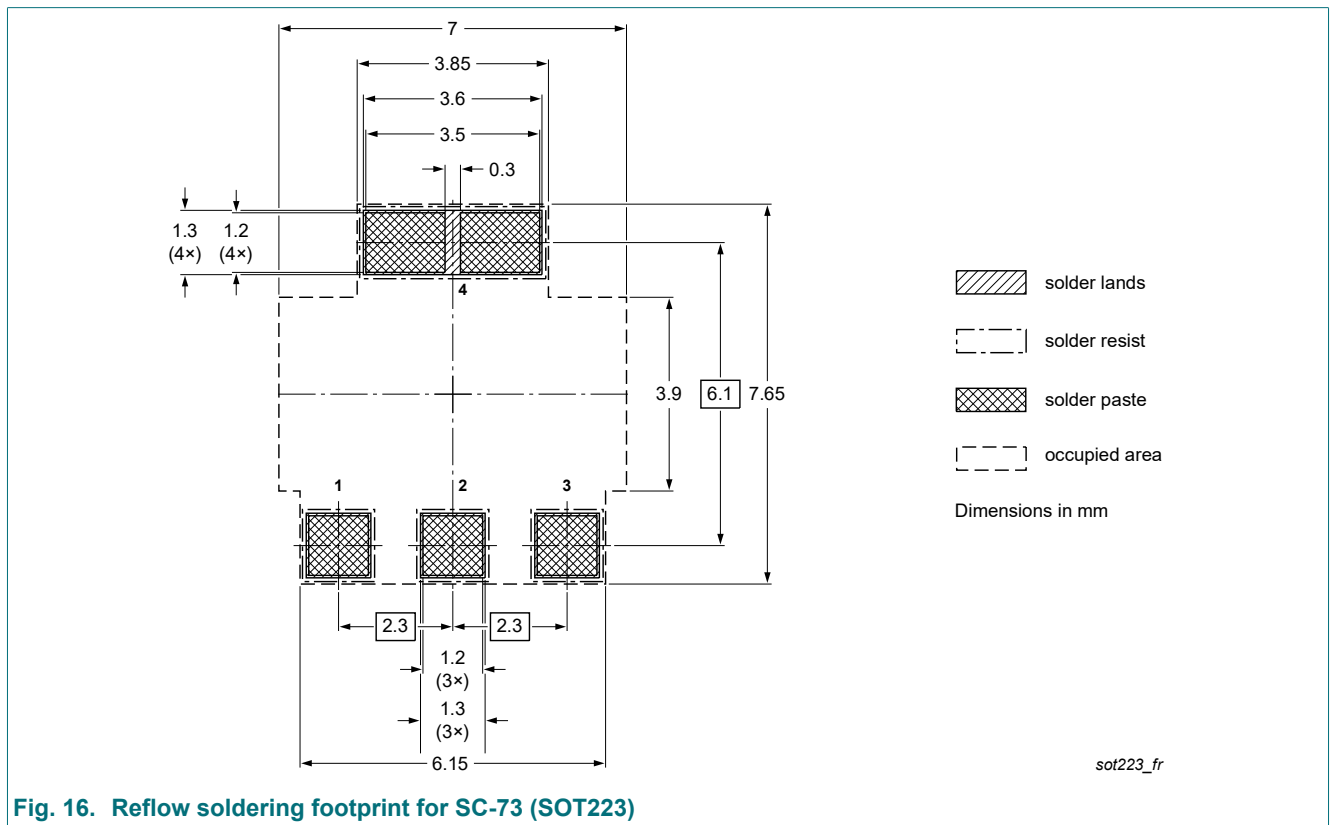


Fig. 16. Reflow soldering footprint for SC-73 (SOT223)

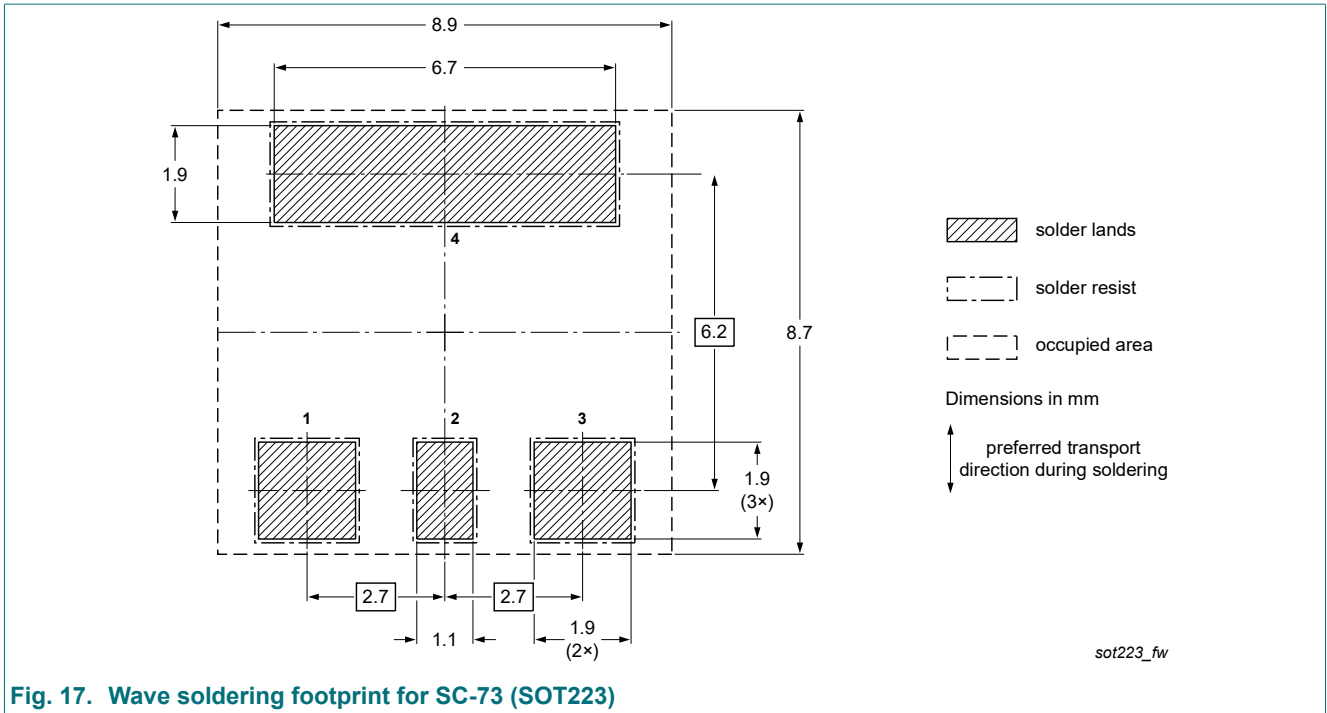


Fig. 17. Wave soldering footprint for SC-73 (SOT223)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4041PZ-Q v.1	20221121	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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