

Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D $T_C = +25^\circ C$
40V	3m Ω @ $V_{GS} = 10V$	192A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

Features

- Rated to +175°C—Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Low $R_{DS(ON)}$ —Minimizes Power Losses
- Low Q_g —Minimizes Switching Losses
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **The DMTH4002SCTBQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

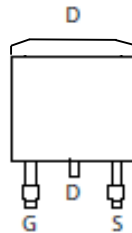
Mechanical Data

- Case: TO263AB
- Case Material: Molded Plastic, “Green” Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 1.7 grams (Approximate)

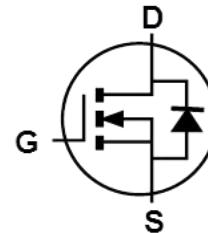
TO263AB (D2PAK)



Top View



Pin Out Top View



Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH4002SCTBQ-13	TO263AB (D2PAK)	800/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2), & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, “Green” and Lead-free.
 3. Halogen- and Antimony-free “Green” products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



TH4002SCTB = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 20 = 2020)
 WW = Week (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	40	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 6)	I _D	T _C = +25°C	192
		T _C = +100°C	136
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	100	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	760	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	760	A
Avalanche Current, L = 3mH	I _{AS}	19.2	A
Avalanche Energy, L = 3mH	E _{AS}	551.8	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	6	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	25	°C/W
Total Power Dissipation (Note 6)	P _D	166.7	W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	0.9	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	µA	V _{DS} = 32V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2	—	4	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	2.22	3	mΩ	V _{GS} = 10V, I _D = 90A
Diode Forward Voltage	V _{SD}	—	0.8	1.2	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	7180	—	pF	V _{DS} = 20V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	1698	—		
Reverse Transfer Capacitance	C _{rss}	—	17	—		
Gate Resistance	R _G	—	1.04	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	77.5	—	nC	V _{DD} = 20V, I _D = 90A, V _{GS} = 10V
Gate-Source Charge	Q _{gs}	—	23.6	—		
Gate-Drain Charge	Q _{gd}	—	13.6	—		
Turn-On Delay Time	t _{D(ON)}	—	16.8	—	ns	V _{DD} = 20V, V _{GS} = 10V, I _D = 90A, R _G = 3.5Ω
Turn-On Rise Time	t _r	—	8.0	—		
Turn-Off Delay Time	t _{D(OFF)}	—	35.8	—		
Turn-Off Fall Time	t _f	—	11.6	—		
Reverse Recovery Time	t _{RR}	—	46.36	—	ns	I _F = 15A, di/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	—	56.11	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

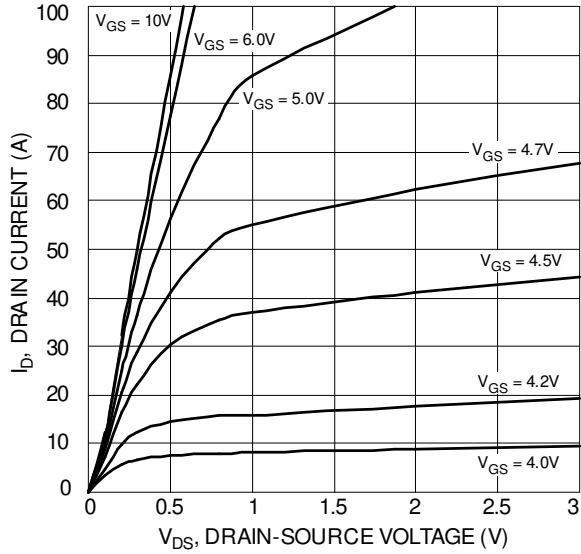


Figure 1 Typical Output Characteristic

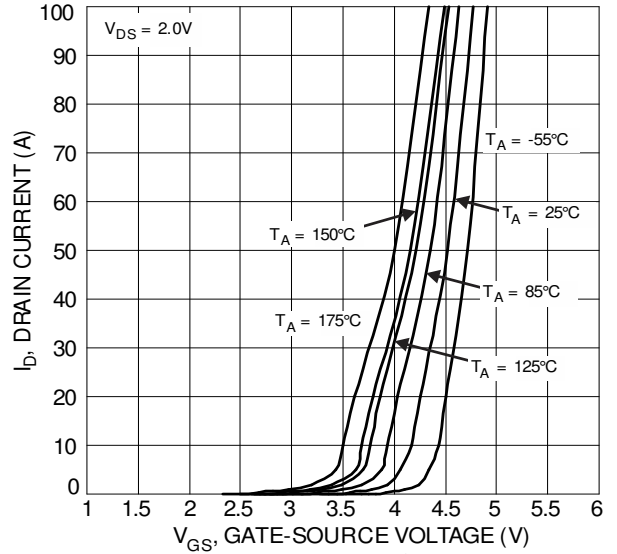


Figure 2 Typical Transfer Characteristics

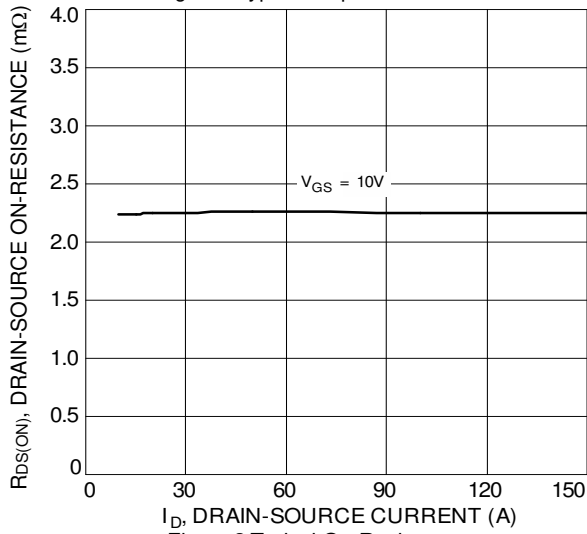


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

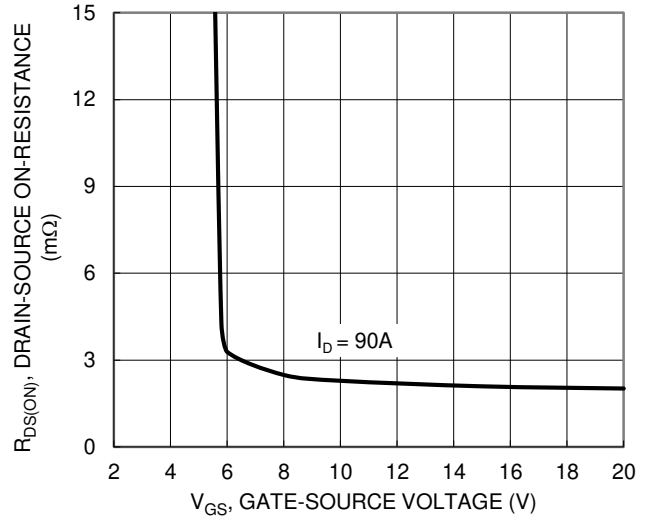


Figure 4. Typical Transfer Characteristic

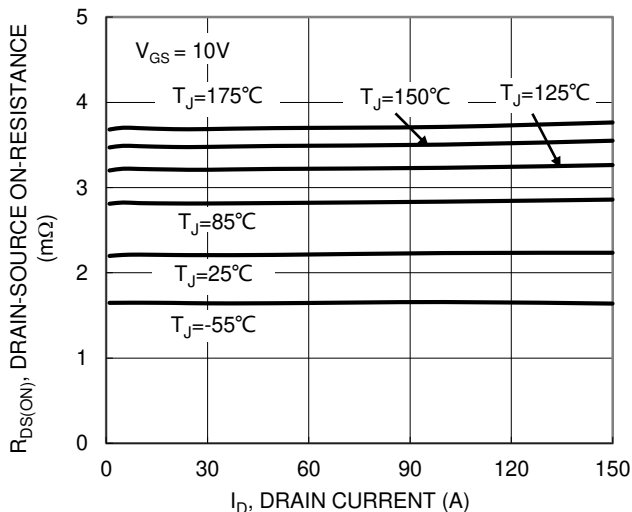


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

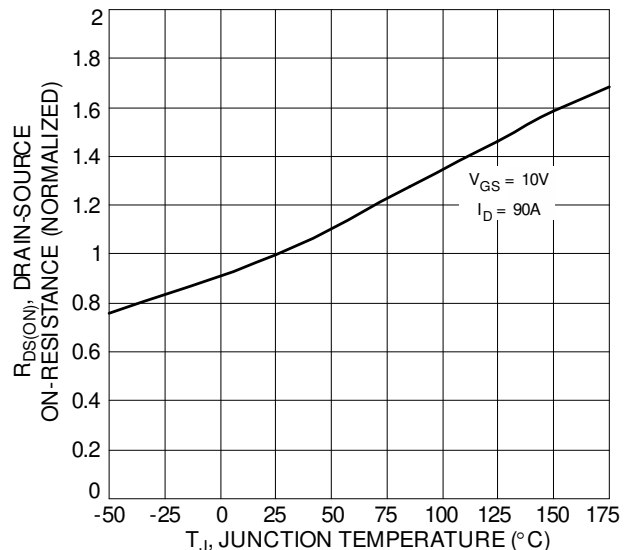


Figure 6 On-Resistance Variation with Temperature

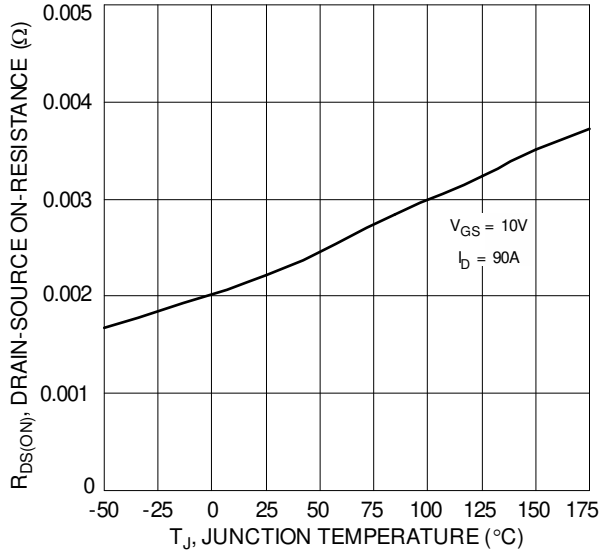


Figure 7 On-Resistance Variation with Temperature

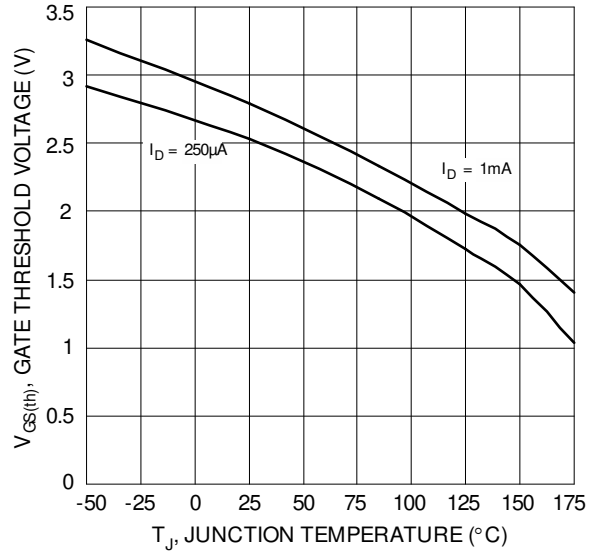


Figure 8 Gate Threshold Variation vs. Junction Temperature

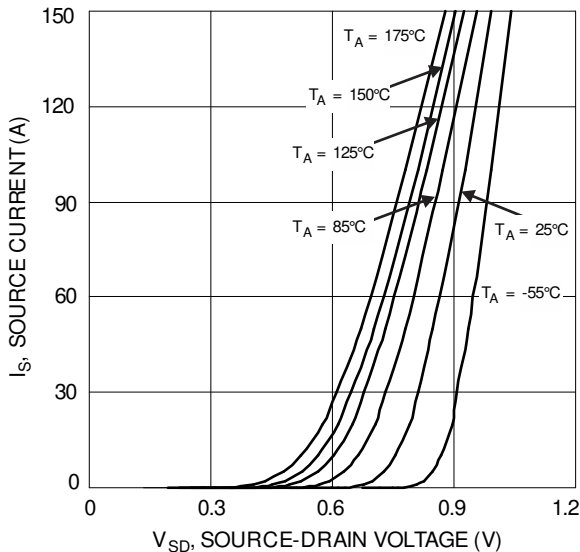


Figure 9 Diode Forward Voltage vs. Current

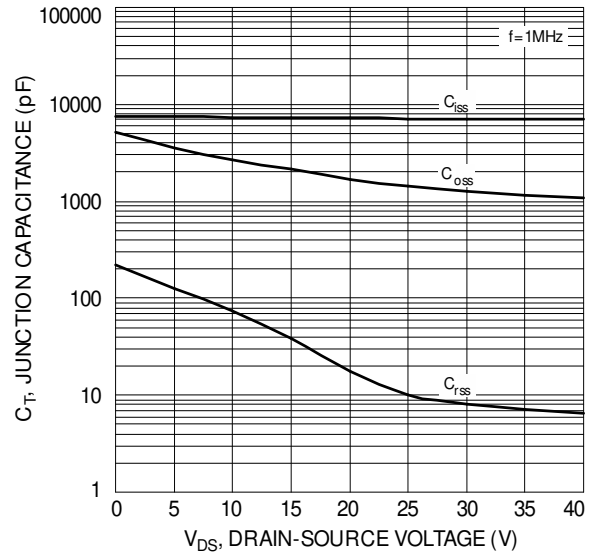


Figure 10 Typical Junction Capacitance

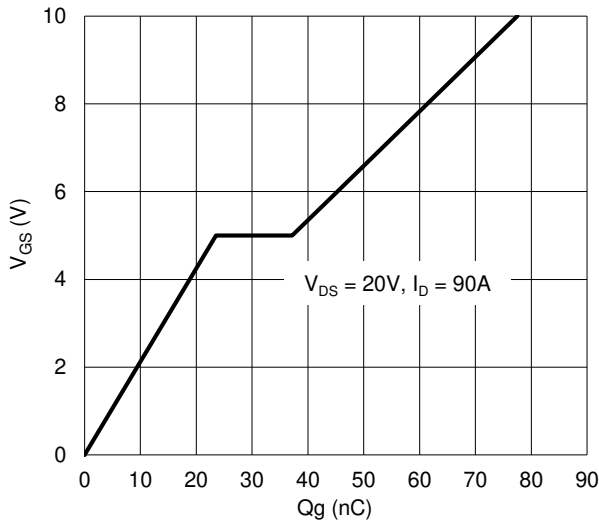


Figure 11. Gate Charge

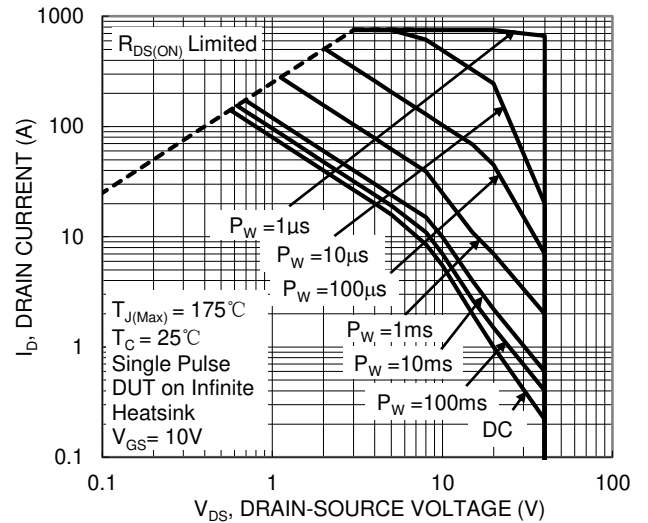
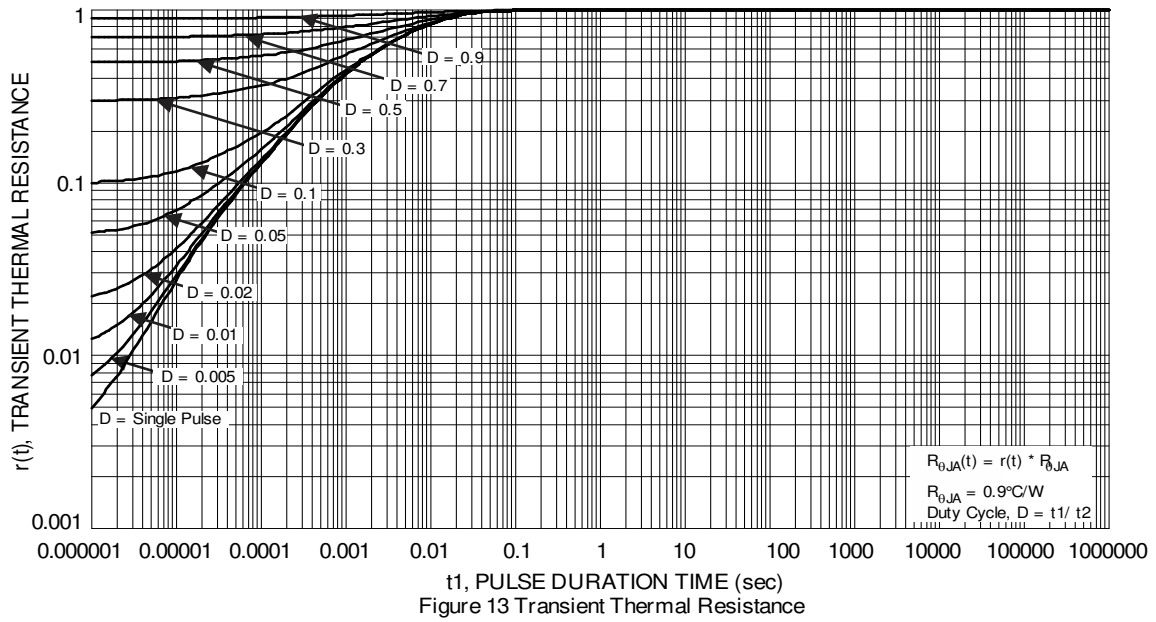


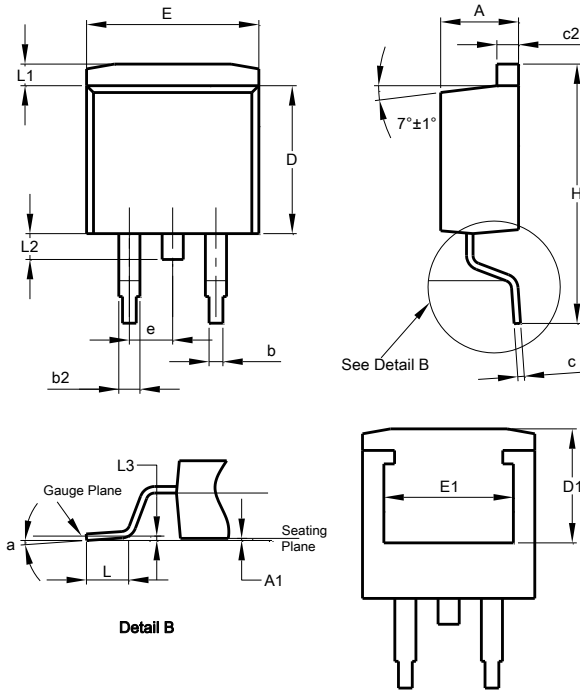
Figure 12. SOA, Safe Operation Area



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TO263AB (D2PAK)

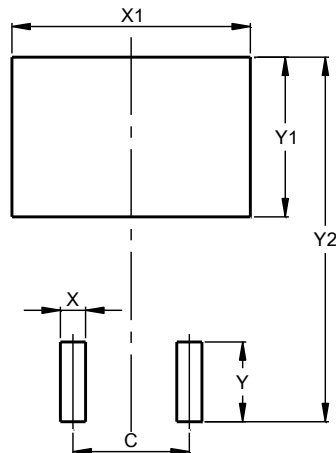


TO263AB (D2PAK)			
Dim	Min	Max	Typ
A	4.07	4.82	—
A1	0.00	0.25	—
b	0.51	0.99	—
b2	1.15	1.77	—
c	0.356	0.73	—
c2	1.143	1.65	—
D	8.39	9.65	—
D1	6.55	6.95	—
e	2.54 TYP		
E	9.66	10.66	—
E1	6.23	8.23	—
H	14.61	15.87	—
L	1.78	2.79	—
L1	—	1.67	—
L2	—	1.77	—
L3	—	—	0.254
a	0°	8°	—
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TO263AB (D2PAK)



Dimensions	Value (in mm)
C	5.08
X	1.10
X1	10.41
Y	3.50
Y1	7.01
Y2	15.99

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