

## Description

**TMDSADP1420 adapter** – used for connecting TI and 3rd party XDS510 and XDS560-class emulators with a 14 pin native connector to the [TMDXEVM6446](#) or customer boards with a compact (CTI) 20-pin header. The adapter improves signal integrity, translates voltages, and can optionally provide adaptive clocking.

**TMDSADP1414** – used for connecting TI and 3rd party XDS510 and XDS560 class 14-pin emulators to custom customer boards with 14-pin JTAG header. The TMDSADP1414 adapter improves signal integrity, translates voltages and can optionally provide adaptive clocking. It is compatible with TI and 3rd party XDS510 and XDS560-class emulators with a 14 pin native connector connecting to a target board with a 14-pin standard JTAG header.

**TMDSADP1460 Emulation Adapter Board** – designed to allow targets containing Texas Instruments' 60-pin Next Generation Emulation Interface to be used with traditional fielded 14-pin emulation hardware (e.g. XDS510 and XDS560). See 60-Pin Emulation Header Technical Reference SPRU655A for more information.

**TMDSADPEMU-20A adapter** – a small printed circuit board adapter that has connections for both 14 pin and 20 pin (ARM) emulators on the top side. The target board side connector is to the standard 20 pin ARM configuration.

**TMDSADPEMU-20T adapter** – a small printed circuit board adapter that has connections for both 14 pin and 20 pin (ARM) emulators on the top side. The target board side connector is to the TI 20 CTI configuration.

**TMDSADP1414-ISO** – for connecting TI and 3rd party XDS510 and XDS560 class 14-pin emulators to custom customer boards with 14-pin JTAG header. The TMDSADP1414-ISO adapter is designed to minimize and protect connected devices (PC/Emulator/Target) from ground loop voltage, harsh environments and new, untested designs. The Isolation Adapter supports 3.3-5.0v targets and is available with TI 14-pin JTAG connections.

## Features

Features by Adapter							
Description	Part #	Emulator Header	Target Header	RTCK Signal Boost	Voltage Translation	Adaptive Clocking	Recommended for
14 pin emulator to 14 pin TI JTAG header	TMDSADP1414	14 TI	14 TI	X	X	X	DM3x, DM2x, OMAP, DM644x target boards
14 pin emulator to 20 pin TI JTAG header	TMDSADP1420	14 TI	20 TI (CTI)	X	X	X	DM6443/6446 target boards
14 pin emulator to 60 pin High density TI JTAG	TMDSADP1460	14 TI	60 TI				C64x/DM64x/64x+ target boards with 60 pin headers
20 pin ARM/14 pin TI emulator to 20 pin ARM	TMDSADPEMU-20A	20 ARM or 14 TI	20 ARM	X			Third Party ARM tools and XDS510/XDS560 Emulators (DM644x target boards)
20 pin ARM/14	TMDSADPEMU-	20 ARM	20 TI	X			Third Party ARM tools

pin TI emulator to 20 pin TI (CTI)	20T	or 14 TI	(CTI)	and XDS510/XDS560 Emulators (DM644x target boards)
14 pin emulator to 14 pin TI JTAG header with Isolation	TMDSADP1414- ISO	14 TI	14 TI	3.3V/5V devices which require Isolation

- Adapters with Voltage translation supported provides between +3.3V emulator interfaces and JTAG targets that require down to +1.8V interfaces.
- Adaptive clocking for TI ARM based processors includes 8 selectable delay clocking options
- Adapters do not require target board modifications
- No power supply required, powered by target board