

RoHS

COMPLIANT HALOGEN

FREE Available

Vishay Siliconix

P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
- 30	0.150 at V _{GS} = - 10 V	- 2.2			
	0.260 at V _{GS} = - 4.5 V	- 1.6			

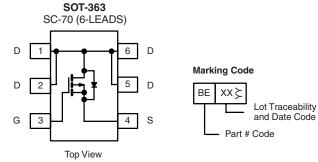
FEATURES

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- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFETs: 1.8 V Rated
 - Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Load Switches
- Notebook PC
 - Servers



Ordering Information: Si1433DH-T1-E3 (Lead (Pb)-free) Si1433DH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unles Parameter		Symbol	5 s	Steady State	Unit	
		,			Unit	
Drain-Source Voltage		V _{DS}	- 30		V	
Gate-Source Voltage		V _{GS}	± 20			
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T _A = 25 °C	– I _D	- 2.2	- 1.9	А	
	T _A = 85 °C		- 1.7	- 1.4		
Pulsed Drain Current		I _{DM}	- 8		A	
Continuous Diode Current (Diode Conduction) ^a		۱ _S	- 1.4	- 0.9		
Maximum Power Dissipation ^a	T _A = 25 °C	Р	1.45	0.95	W	
	T _A = 85 °C	P _D	0.75	0.5		
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \le 5 s$	R _{thJA}	65	85	
	Steady State		105	130	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	38	48	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted								
Parameter	Symbol Test Conditions		Min.	Тур.	Max.	Unit		
Static	-							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -100 \ \mu A$	- 1		- 3	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0$ V, $V_{GS} = \pm 8$ V			± 100	nA		
Zero Gate Voltage Drain Current		$V_{DS} = -16 V, V_{GS} = 0 V$	- 1		- 1			
	IDSS	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, \text{ T}_{J} = 85 ^{\circ}\text{C}$			- 5	- μΑ		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V$, $V_{GS} = -4.5 V$	- 4			А		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -10$ V, $I_{D} = -2.2$ A		0.120	0.150	Ω		
		V _{GS} = - 4.5 V, I _D = - 1.6 A		0.210	0.260			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 2.2 A		4		S		
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.2 A, V _{GS} = 0 V		- 0.85	- 1.2	V		
Dynamic ^b	•							
Total Gate Charge	Qg			3.1	5			
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 4.5 V, I_{D} = - 2.2 A		1.0		nC		
Gate-Drain Charge	Q _{gd}			1.6				
Turn-On Delay Time	t _{d(on)}			11	17			
Rise Time	t _r	V _{DD} = - 15 V, R _I = 15 Ω		17	26	ns		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 1 Å, V_{GEN} = - 10 V, R_g = 6 Ω		18	27			
Fall Time	t _f			13	20			

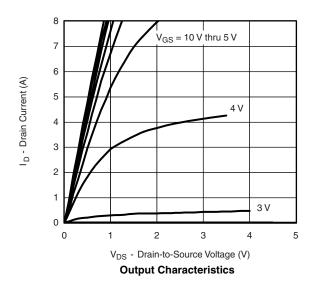
Notes:

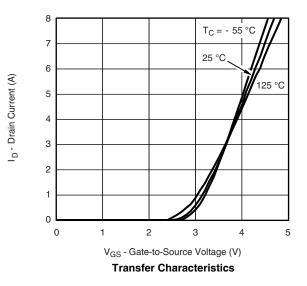
a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.

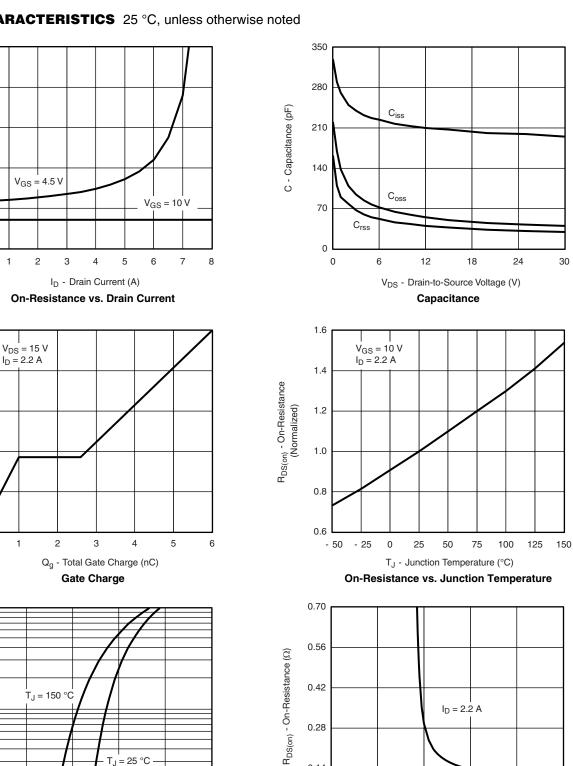
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







0.14

0.00

0

2

4

6

V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

VISHAY

0.75

0.60

0.45

0.30

0.15

0.00

10

8

6

4

2

0

10

1

0.1 0

0.3

0.6

V_{SD} - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

T_{.1} = 25 °C

0.9

1.2

1.5

Is - Source Current (A)

0

V_{GS} - Gate-to-Source Voltage (V)

0

1

 $R_{DS(on)}$ - On-Resistance (Ω)

Document Number: 72323 S10-0935-Rev. C, 19-Apr-10 10

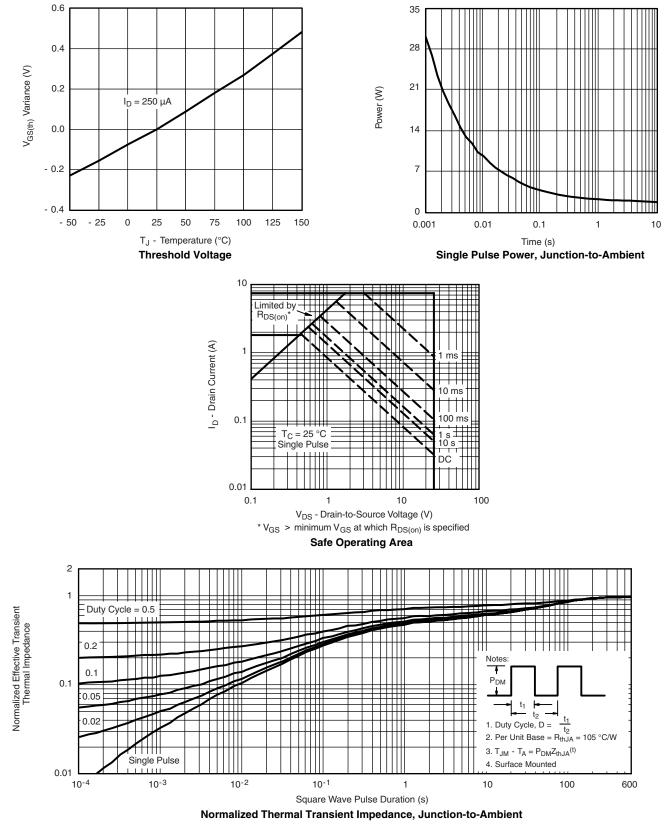
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Si1433DH

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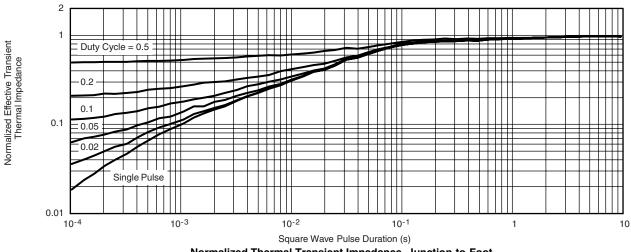




Si1433DH

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?72323</u>.



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