

STC04IE170HP

Monolithic emitter switched bipolar transistor ESBT $^{\mbox{\tiny B}}$ 1700 V - 4 A - 0.17 Ω

Features

V _{CS(ON)}	Ι _C	R _{CS(ON)}
0.7 V	4 A	0.17 Ω

- High voltage / high current cascode configuration
- Low equivalent ON resistance
- Very fast-switch, up to 150 kHz
- Squared RBSOA, up to 1700 V
- Very low C_{ISS} driven by $R_G = 47 \Omega$
- Very low turn-off cross over time

Application

■ Aux SMPS for three-phase mains

Description

The STC04IE170HP is manufactured in Monolithic ESBT technology, aimed at providing the best performance in high frequency / high voltage applications. It is designed for use in gate driven based topologies. TO247-4L HP

Figure 1. Internal schematic diagrams

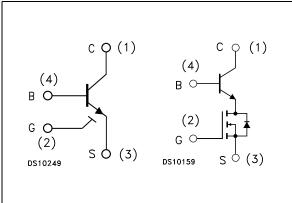


Table 1.Device summary

Order code	Marking	Package	Packing
STC04IE170HP	C04IE170HP	TO247-4L HP	Tube

1 Electrical ratings

Table 2.	Absolute	maximum	ratings
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Symbol	Parameter	Value	Unit
V _{CS(SS)}	Collector-source voltage ($V_{BS} = V_{GS} = 0$)	1700	V
V _{BS(OS)}	Base-source voltage ($I_C = 0$, $V_{GS} = 0$)	30	V
V _{SB(OS)}	Source-base voltage ($I_C = 0$, $V_{GS} = 0$)	17	V
V _{GS}	Gate-source voltage	± 17	V
Ι _C	Collector current	4	А
I _{CM}	Collector peak current (t _P < 5 ms)	8	А
I _B	Base current	4	А
I _{BM}	Base peak current (t _P < 1 ms)	8	А
P _{tot}	Total dissipation at $T_c \le 25^{\circ}C$	50	W
T _{stg}	Storage temperature	-40 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3.Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case	2.5	°C/W



2 Electrical characteristics

(T_{case} = 25 °C unless otherwise specified)

 Table 4.
 Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CS(SS)}	Collector cut-off current $(V_{BS} = V_{GS} = 0)$	V _{CS} = 1700 V			100	μA
I _{BS(OS)}	Base cut-off current $(I_C = 0, V_{GS} = 0)$	V _{BS} = 30 V			10	μA
I _{SB(OS)}	Source cut-off current $(I_C = 0, V_{GS} = 0)$	V _{SB} = 17 V			100	μA
I _{GS(OS)}	Gate-source leakage current (V _{BS} = 0)	$V_{GS} = \pm 17V$			100	nA
V _{CS(ON)}	Collector-source ON voltage	$V_{GS} = 10 V I_C = 4 A I_B = 0.8 A$ $V_{GS} = 10 V I_C = 1.5 A I_B = 0.15 A$		0.7 0.6	1.5 1.4	V V
$h_{FE}^{(1)}$	DC current gain		4 7	5.5 11		
V _{BS(ON)}	Base-source ON voltage			1.3 0.9	1.5 1.1	V V
V _{GS(th)}	Gate threshold voltage	$V_{BS} = V_{GS}$ $I_B = 250 \ \mu A$	2	3	4	V
C _{iss}	Input capacitance $(V_{GS} = V_{CB} = 0)$	V _{CS} = 25 V f = 1 MHz		510		pF
Q _{GS(tot)}	Gate-source charge (V _{CB} = 0)	V _{GS} = 10 V		3.9		nC
t _s t _f	Inductive load Storage time Fall time	$ \begin{array}{ll} V_{GS} = 10 \ V & R_G = 47 \ \Omega \\ V_{Clamp} = 1360 \ V & t_p = 4 \ \mu s \\ I_C = 2 \ A & I_B = 0.4 \ A \end{array} $		770 10		ns ns
t _s t _f	Inductive load Storage time Fall time	$\label{eq:VGS} \begin{array}{ll} V_{GS} = 10 \ V & R_G = 47 \ \Omega \\ V_{Clamp} = 1360 V & t_p = 4 \ \mu s \\ I_C = 2 \ A & I_B = 0.2 \ A \end{array}$		410 10		ns ns
V _{CS(dyn)}	Collector-source dynamic voltage (0.5 µs)	$\begin{split} V_{CC} &= V_{Clamp} = 400 \ V \\ V_{GS} &= 10 \ V I_C = 1.5 \ A \\ I_B &= 0.3 \ A t_{peak} = 500 \ ns \\ R_G &= 47 \ \Omega I_{Bpeak} = 3 \ A \ (2 \ I_C) \end{split}$		5.36		V
V _{CS(dyn)}	Collector-source dynamic voltage (1 µs)	$\begin{split} V_{CC} &= V_{Clamp} = 400 \ V \\ V_{GS} &= 10 \ V & I_C = 1.5 \ A \\ I_B &= 0.3 \ A & t_{peak} = 500 \ ns \\ R_G &= 47 \ \Omega & I_{Bpeak} = 3 \ A \ (2I_C) \end{split}$		4.32		V
V _{CSW}	Maximum collector- source voltage at turn- off without snubber	$R_{G} = 47 \ \Omega$ $h_{FE} = 5$ $I_{C} = 4 \ A$	1700			V

1. Pulsed duration = 300 $\mu s,$ duty cycle $\leq 1.5\%.$



2.1 Electrical characteristics (curves)



Figure 3. Collector-source dynamic voltage

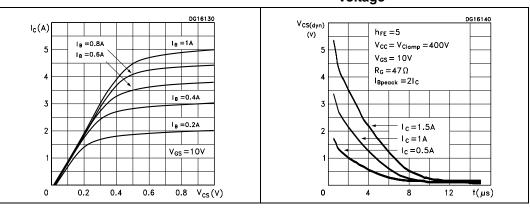


Figure 4. DC current gain

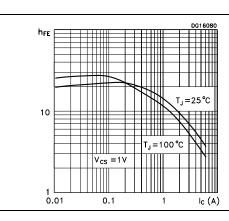


Figure 5. Gate threshold voltage vs temperature

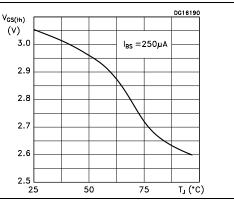
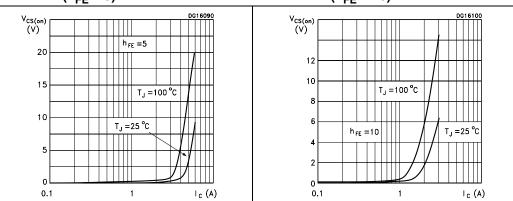


Figure 6. Collector-source ON voltage Figure 7. $(h_{FE} = 5)$

Collector-source ON voltage (h_{FE} = 10)



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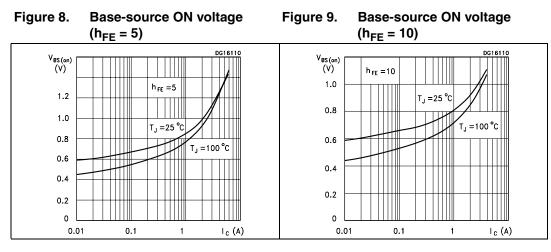


Figure 10. Inductive load switching time Figure 11. Inductive load switching time $(h_{FE} = 5)$ $(h_{FE} = 10)$

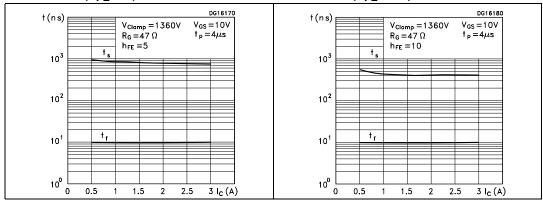
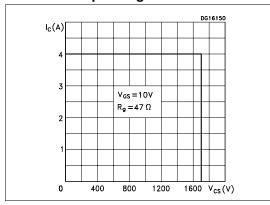


Figure 12. Reverse biased safe operating area





3 Package mechanical data

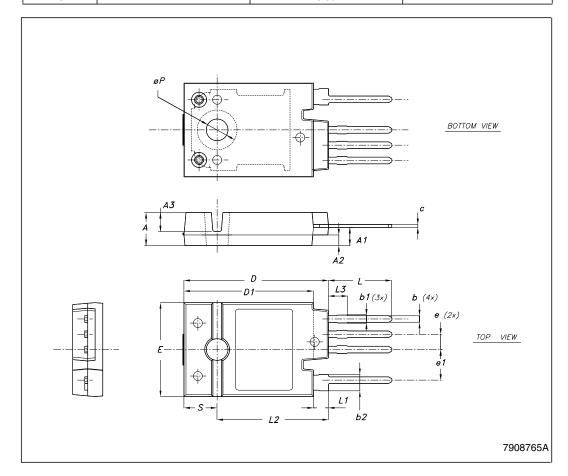
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DIM.		mm.	
	MIN.	ТҮР	MAX.
A	5.50	5.65	5.80
A1	2.85	3.15	3.25
A2		1.92	
A3		3.18	
b	0.95	1.10	1.30
b1	1.10		1.50
b2	2.50		2.90
с	0.40		0.80
D	23.85	24	24.15
D1		21.50	
E	15.45	15.60	15.75
е		2.54	
e1		5.08	
L	10.20		10.80
L1	2.20	2.50	2.80
L2		18.50	
L3		3	
øP	3.55		3.65
S		5.50	





4 Revision history

Table 5.Document revision history

Date	Revision	Changes
27-Sep-2006	1	First release.
21-Nov-2006	2	Improved application target.
17-Jun-2009	3	Updated Figure 2 on page 4 and mechanical data.



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