



DFLS240L

### 2.0A LOW VF SCHOTTKY BARRIER RECTIFIER PowerDI123

#### **Features**

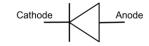
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <a href="https://www.diodes.com/products/automotive/automotive-products/">https://www.diodes.com/products/automotive/automotive-products/</a>.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
   https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (DFLS240LQ)

## **Mechanical Data**

- Package: PowerDI<sup>®</sup>123
- Package Material: Molded Plastic, "Green" Molding
  Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.01 grams (Approximate)

# PowerDI123





**Top View** 

## **Ordering Information** (Note 4)

Part Number	Dookogo	Packing		
Part Number	Package	Qty.	Carrier	
DFLS240L-7	PowerDI123	3000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

# **Marking Information**

PowerDI123



F06A = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 2 = February)

Date Code Key

Year	2004		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	R		J	K	L	М	N	0	Р	R	S	T
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

PowerDI is a registered trademark of Diodes Incorporated.



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>R</sub> WM V <sub>R</sub>	40	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Forward Current	l <sub>F(AV)</sub>	2.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	50	Α

## **Thermal Characteristics**

Characteristic	Symbol	Тур	Max	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	_	1.67	W
Power Dissipation (Note 6)	PD	_	556	mW
Thermal Resistance Junction to Ambient (Note 5)	$R_{\theta JA}$	60	_	°C/W
Thermal Resistance Junction to Ambient (Note 6)	RеJA	180	_	°C/W
Thermal Resistance Junction to Soldering (Note 7)	Reus	_	5	°C/W
Operating Temperature Range (See Figure 4)	TJ	-55 to +	-125	°C
Storage Temperature Range	Tstg	-55 to +	-150	°C

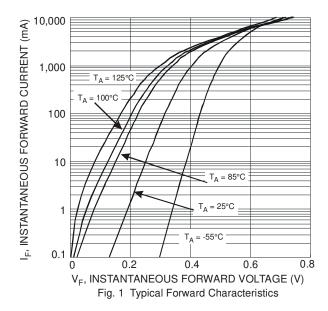
## **Electrical Characteristics** (@TA = +25°C, unless otherwise specified.)

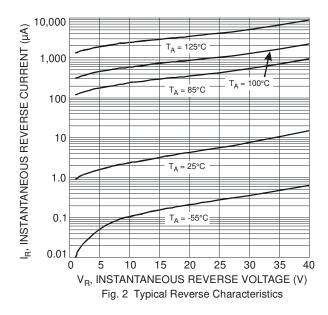
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V <sub>(BR)R</sub>	40	_	_	V	$I_R = 500 \mu A$
		_	0.4	0.45		IF = 1.0A
Forward Voltage	VF	_	0.45	0.50	V	IF = 2.0A
		_	0.50	0.65		$I_F = 3.0A$
		_	_	0.1		V <sub>R</sub> = 40V
Leakage Current (Note 8)	l <sub>a</sub>	_	_	10	mA	$V_R = 40V, T_J = +85^{\circ}C$
Leakage Current (Note o)	IR	_	_	0.05	IIIA	V <sub>R</sub> = 20V
		_	_	5		V <sub>R</sub> = 20V, T <sub>J</sub> = +85°C
Total Capacitance	Ст	_	90	_	pF	V <sub>R</sub> = 10V, f = 1.0MHz

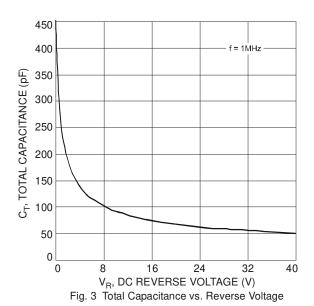
Notes:

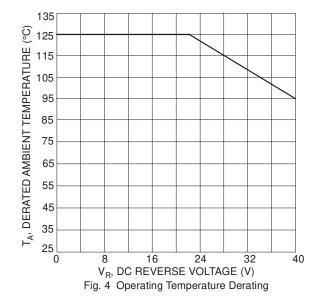
- 5. Part mounted on 50.8mm X 50.8mm GETEK board with 25.4mm X 25.4mm copper pad, 25% anode, 75% cathode.
- 6. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads.
- 7. Theoretical  $R_{ theta JS}$  calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- 8. Short duration pulse test used to minimize self-heating effect.









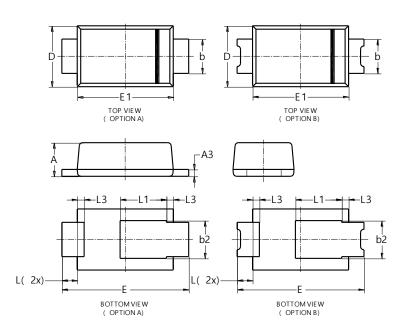




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI123

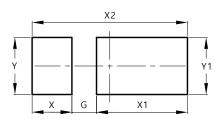


PowerDI123					
Dim	Min	Max	Тур		
Α	0.93	1.00	0.98		
A3	0.15	0.25	0.20		
b	0.85	1.25	1.00		
b2	1.025	1.125	1.10		
D	1.63	1.93	1.78		
Е	3.50	3.90	3.70		
E1	2.60	3.00	2.80		
L	0.40	0.50	0.45		
L1	1.25	1.40	1.35		
L3	0.125	0.275	0.20		
All Dimensions in mm					

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI123



Dimensions	value		
Dillielisions	(in mm)		
G	0.65		
X	1.05		
X1	2.40		
X2	4.10		
Υ	1.50		
Y1	1.50		

Value

March 2022



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