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November 2014

KA324 / KA324A / KA2902 Quad Operational Amplifier

Features

- · Internally Frequency Compensated for Unity Gain
- · Large DC Voltage Gain: 100 dB
- Wide Power Supply Range:
 KA324 / KA324A: 3 V ~ 32 V (or ±1.5 V ~ 16 V)
 KA2902: 3 V ~ 26 V (or ±1.5 V ~ 13 V)
- · Input Common Mode Voltage Range Includes Ground
- Large Output Voltage Swing: 0 V to V_{CC} -1.5 V
- · Power Drain Suitable for Battery Operation

Description

The KA324 series consist of four independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide voltage range. Operation from split power supplies is also possible so long as the difference between the two supplies is 3 V to 32 V. Application areas include transducer amplifier, DC gain blocks and all the conventional OP Amp circuits which now can be easily implemented in single power supply systems.



Ordering Information

Part Number	Operating Temperature Range	Top Mark	Package	Packing Method
KA324		KA324	MDIP 14L	Rail
KA324A	0 to +70°C	KA324A	MDIP 14L	Rail
KA324DTF	0 10 +70 C	KA324D	SOP 14L	Tape and Reel
KA324ADTF		KA324AD	SOP 14L	Tape and Reel
KA2902DTF	-40 to +85°C	KA2902D	SOP 14L	Tape and Reel

Block Diagram

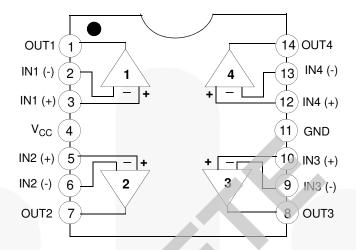


Figure 1. Block Diagram

Schematic Diagram

(One Section Only)

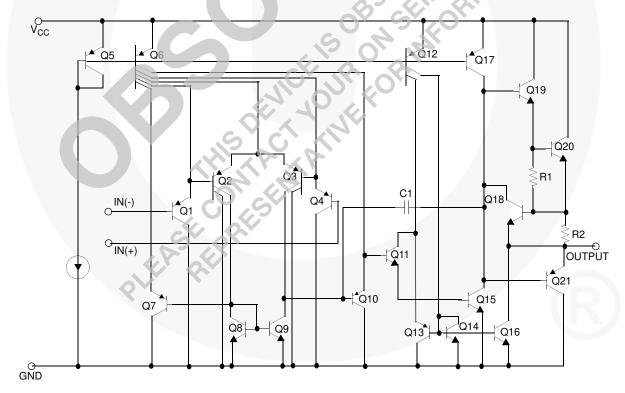


Figure 2. Schematic Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Parameter	Symbol	KA324 / KA324A	KA2902	Unit
Power Supply Voltage	V _{CC}	± 16 or 32	± 13 or 26	V
Differential Input Voltage	V _{I(DIFF)}	32	26	V
Input Voltage	V _I	-0.3 to +32	-0.3 to +26	V
Output Short Circuit to GND $V_{CC} \le 15 \text{ V}, T_A = 25^{\circ}\text{C} \text{ (One Amp)}$	-	Continuous	Continuous	-
Operating Temperature Range	T _{OPR}	0 to +70	-40 to +85	°C
Storage Temperature Range	T _{STG}	-65 to +150	-65 to +150	°C

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
В	Power Dissipation T 25°C	14-DIP 1310	mW
P _D	Power Dissipation, T _A = 25°C	14-SOP 640	IIIVV
Б	They are I Decistors I wasting to Ambient May	14-DIP 95	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	14-SOP 195	°C/VV

Electrical Characteristics

Values are at V_{CC} = 5.0 V, V_{EE} = GND, T_A = 25°C, unless otherwise specified.

Cumahad	Devementer	Conditions	KA324			KA2902			Unit
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
V _{IO}	Input Offset Voltage	$V_{CM} = 0 \text{ V to } V_{CC} - 1.5 \text{ V},$ $V_{O(P)} = 1.4 \text{ V}, R_S = 0 \Omega^{(1)}$	-	1.5	7.0	-	1.5	7.0	mV
I _{IO}	Input Offset Current	$V_{CM} = 0 V$	-	3	50	-	3	50	nA
I _{BIAS}	Input Bias Current	$V_{CM} = 0 V$	-	40	250	-	40	250	nA
V _{I(R)}	Input Common Mode Voltage Range	(1)	0		V _{CC} -1.5	0	-	V _{CC} -1.5	V
I _{CC}	Supply Current	$R_L = \infty$, $V_{CC} = 30 \text{ V}$, (KA2902, $V_{CC} = 26 \text{ V}$)	-	1.0	3.0	1	1.0	3.0	mA
		$R_L = \infty$, $V_{CC} = 5 V$	-	0.7	1.2	-	0.7	1.2	mA
G _V	Large Signal Voltage Gain	$\begin{split} &V_{CC} = 15 \text{ V}, \text{ R}_{L} = 2 \text{ k}\Omega, \\ &V_{O(P)} = 1 \text{ V to } 11 \text{ V} \end{split}$	25	100	-	25	100	-	V/mV
Vann		(1) $R_L = 2 k\Omega$	26	-	-	22)	-	>
V _{O(H)}	Output Voltage Swing	$R_L = 10 \text{ k}\Omega$	27	28		23	24	-	>
$V_{O(L)}$		$V_{CC} = 5 \text{ V}, R_L = 10 \text{ k}\Omega$	1	5	20		5	100	mV
CMRR	Common-Mode Rejection Ratio		65	75	C	50	75	-	dB
PSRR	Power Supply Rejection Ratio		65	100	.08	50	100	-	dB
CS	Channel Separation	f = 1 kHz to 20 kHz ⁽²⁾	3	120	-	-	120	-	dB
I _{SC}	Short Circuit to GND	V _{CC} = 15 V		40	60	1	40	60	mA
I _{SOURCE}		$V_{I(+)} = 1 \text{ V}, V_{I(-)} = 0 \text{ V}, V_{CC} = 15 \text{ V}, V_{O(P)} = 2 \text{ V}$	20	40	-	20	40	-	mA
	Output Current	$V_{I(+)} = 0 \text{ V}, V_{I(-)} = 1 \text{ V}, V_{CC} = 15 \text{ V}, V_{O(P)} = 2 \text{ V}$	10	13	-	10	13	-	mA
I _{SINK}		$V_{I(+)} = 0 \text{ V}, V_{I(-)} = 1 \text{ V}, V_{CC} = 15 \text{ V}, V_{O(R)} = 200 \text{ mV}$	12	45	-	-	-	-	μΑ
V _{I(DIFF)}	Differential Input Voltage	0) (()	-	-	V _{CC}	-	-	V _{CC}	٧

- 1. V_{CC} = 30 V for KA324, V_{CC} = 26 V for KA2902.
- 2. This parameter, although guaranteed is not 100% tested in production.

Electrical Characteristics (Continued)

Values are at V_{CC} = 5.0 V, V_{EE} = GND, unless otherwise specified.

The following specification apply over the range of $0^{\circ}C \le T_{A} \le +70^{\circ}C$ for the KA324, and the -40°C $\le T_{A} \le +85^{\circ}C$ for the KA2902.

Cymbol	Darameter	Conditions	KA324			KA2902			Unit
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Min.	Тур.	Max.	Ullit
V _{IO}	Input Offset Voltage	$V_{ICM} = 0 \text{ V to } V_{CC} -1.5 \text{ V}, V_{O(P)} = 1.4 \text{ V}, R_S = 0 \Omega^{(3)}$	-	-	9.0	-	-	10.0	mV
$\Delta V_{IO}/\Delta T$	Input Offset Voltage Drift	$R_S = 0 \Omega^{(4)}$	-	7.0	-	-	7.0	-	μV/°C
I _{IO}	Input Offset Current	V _{CM} = 0 V	-	-	150	-	-	200	nA
$\Delta I_{IO}/\Delta T$	Input Offset Current Drift	$R_S = 0 \Omega^{(4)}$	-	10	-	-	10	-	pA/°C
I _{BIAS}	Input Bias Current	V _{CM} = 0 V	→ \		500	-	-	500	nA
V _{I(R)}	Input Common Mode Voltage Range	(3)	0	-	V _{CC} -2.0	0	1	V _{CC} -2.0	V
G _V	Large Signal Voltage Gain	$V_{CC} = 15 \text{ V}, R_L = 2.0 \text{ k}\Omega,$ $V_{O(P)} = 1 \text{ V to } 11 \text{ V}$	15	-	-	15	C ¹	-	V/mV
V		$R_L = 2 k\Omega$	26	-	-	22	-3	-	V
$V_{O(H)}$	Output Voltage Swing	$R_L = 10 \text{ k}\Omega$	27	28	V -	23	24	-	V
V _{O(L)}		$V_{CC} = 5 \text{ V}, R_L = 10 \text{ k}\Omega$	-	5	20	- <	5	100	mV
I _{SOURCE}	Output Current	$V_{I(+)} = 1 \text{ V}, V_{I(-)} = 0 \text{ V}, \\ V_{CC} = 15 \text{ V}, V_{O(P)} = 2 \text{ V}$	10	20		10	20	-	mA
I _{SINK}	Output Ourient	$V_{I(+)} = 0 \text{ V}, V_{I(-)} = 1 \text{ V},$ $V_{CC} = 15 \text{ V}, V_{O(P)} = 2 \text{ V}$	5	8	O	5	8	-	mA
$V_{I(DIFF)}$	Differential Input Voltage	- / (3)	0,	1-17	V_{CC}	-	-	V _{CC}	V

- 3. V_{CC} = 30 V for KA324, V_{CC} = 26 V for KA2902.
- 4. These parameters, although guaranteed are not 100% tested in production.

Electrical Characteristics (Continued)

Values are at V_{CC} = 5.0 V, V_{EE} = GND, T_A = 25°C, unless otherwise specified.

Symbol	Parameter	Conditions		Unit		
Syllibol		Conditions	Min.	Тур.	Max.	Ullit
V _{IO}	Input Offset Voltage	$V_{CM} = 0 \text{ V to } V_{CC} -1.5 \text{ V},$ $V_{O(P)} = 1.4 \text{ V}, R_S = 0 \Omega^{(5)}$	-	1.5	3.0	mV
I _{IO}	Input Offset Current	V _{CM} = 0 V	-	3	30	nA
I _{BIAS}	Input Bias Current	$V_{CM} = 0 V$	ı	40	100	nA
$V_{I(R)}$	Input Common-Mode Voltage Range	(5)	0	-	V _{CC} -1.5	٧
	Supply Current	V _{CC} = 30 V, R _L = ∞	-	1.5	3.0	mA
Icc	Supply Current	V _{CC} = 5 V, R _L = ∞	-	0.7	1.2	mA
G _V	Large Signal Voltage Gain	$V_{CC} = 15 \text{ V}, R_L = 2 \text{ k}\Omega,$ $V_{O(P)} = 1 \text{ V to } 11 \text{ V}$	25	100	Q.	V/mV
V		(5) $R_L = 2 k\Omega$	26	- ^	-	V
V _{O(H)}	Output Voltage Swing	$R_L = 10 \text{ k}\Omega$	27	28	-	٧
$V_{O(L)}$		$V_{CC} = 5 \text{ V}, R_L = 10 \text{ k}\Omega$	();	5	20	mV
CMRR	Common-Mode Rejection Ratio		65	85	-	dB
PSRR	Power Supply Rejection Ratio		65	100	ı	dB
CS	Channel Separation	$f = 1 \text{ kHz to } 20 \text{ kHz}^{(6)}$	-	120	ı	dB
I _{SC}	Short Circuit to GND	V _{CC} = 15 V		40	60	mA
I _{SOURCE}		$V_{I(+)} = 1 \text{ V}, V_{I(-)} = 0 \text{ V},$ $V_{CC} = 15 \text{ V}, V_{O(P)} = 2 \text{ V}$	20	40	1	mA
	Output Current	$V_{I(+)} = 0 \text{ V}, V_{I(-)} = 1 \text{ V}, V_{CC} = 15 \text{ V}, V_{O(P)} = 2 \text{ V}$	10	20	-	mA
I _{SINK}		$V_{I(+)} = 0$ V, $V_{I(-)} = 1$ V, $V_{CC} = 15$ V, $V_{O(P)} = 200$ mV	12	50	-	μА
$V_{I(DIFF)}$	Differential Input Voltage		1	-	V _{CC}	V
Motoc:						

^{5.} V_{CC}=30V for KA324A.
6. This parameter, although guaranteed is not 100% tested in production.

Electrical Characteristics (Continued)

Values are at V_{CC} = 5.0 V, V_{EE} = GND, unless otherwise specified. The following specification apply over the range of $0^{\circ}C \le T_A \le +70^{\circ}C$ for the KA324A.

Comphal	Parameter	Conditions		I I m i t		
Symbol		Conditions	Min.	Тур.	Max.	Unit
V _{IO}	Input Offset Voltage	$V_{CM} = 0 \text{ V to } V_{CC} - 1.5 \text{ V},$ $V_{O(P)} = 1.4 \text{V}, R_S = 0\Omega^{(7)}$	-	-	5.0	mV
$\Delta V_{IO}/\Delta T$	Input Offset Voltage Drift	$R_S = 0 \Omega^{(8)}$	-	7	30	μV/°C
I _{IO}	Input Offset Current	V _{CM} = 0 V	-	-	75	nA
$\Delta I_{IO}/\Delta T$	Input Offset Current Drift	$R_S = 0 \Omega^{(8)}$	-	10	300	pA/°C
I _{BIAS}	Input Bias Current	V _{CM} = 0 V	-	40	200	nA
V _{I(R)}	Input Common-Mode Voltage Range	(7)	0	-	V _{CC} -2.0	V
G _V	Large Signal Voltage Gain	$V_{CC} = 15 \text{ V}, R_L = 2.0 \text{ k}\Omega$	15	-	0.	V/mV
V		(7) $R_L = 2 k\Omega$	26	- 6	-	V
V _{O(H)}	Output Voltage Swing	$R_L = 10 \text{ k}\Omega$	27	28	-	V
V _{O(L)}		$V_{CC} = 5 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega$	(5	20	mV
I _{SOURCE}	Output Current	$V_{I(+)} = 1 \text{ V}, V_{I(-)} = 0 \text{ V},$ $V_{CC} = 15 \text{ V}, V_{O(P)} = 2 \text{ V}$	10	20	-	mV
I _{SINK}	Output Current	$V_{I(+)} = 0 \text{ V}, V_{I(-)} = 1 \text{ V}, V_{CC} = 15 \text{ V}, V_{C(P)} = 2 \text{ V}$	5	8	-	mA
V _{I(DIFF)}	Differential Input Voltage	0, 2,	\O),	-	V _{CC}	V

- 7. V_{CC}=30V for KA324A
- 8. This parameter, although guaranteed is not 100% tested in production.

Typical Performance Characteristics

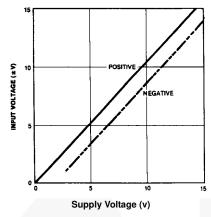


Figure 3. Input Voltage Range vs. Supply Voltage

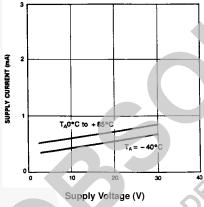


Figure 5. Supply Current vs. Supply Voltage

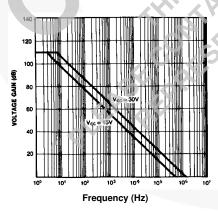


Figure 7. Open Loop Frequency Response

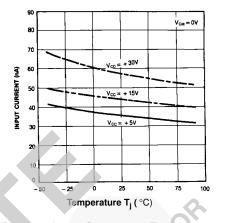


Figure 4. Input Current vs. Temperature

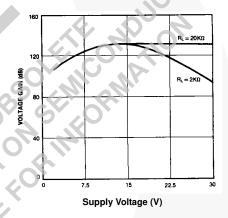


Figure 6. Voltage Gain vs. Supply Voltage

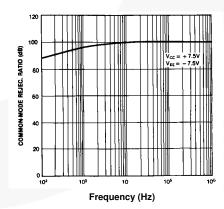


Figure 8. Common Mode Rejection Ratio

Typical Performance Characteristics (Continued)

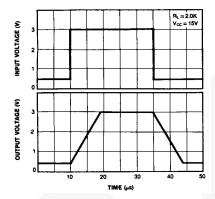


Figure 9. Voltage Follower Pulse Response

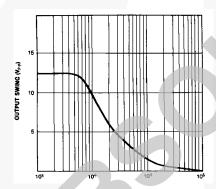


Figure 11. Large Signal Frequency Response

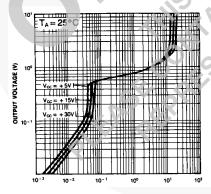


Figure 13. Output Characteristics vs. Current Sinking

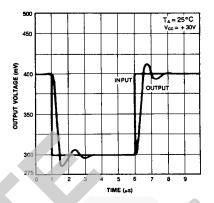


Figure 10. Voltage Follower Pulse Response (Small Signal)

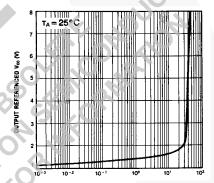


Figure 12. Output Characteristics vs.
Current Sourcing

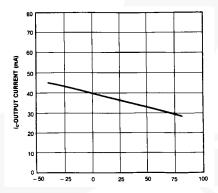
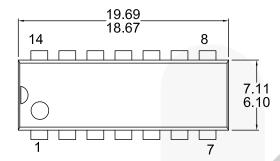
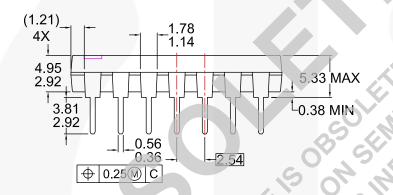
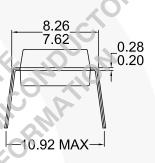


Figure 14. Current Limiting vs. Temperature

Physical Dimensions







NOTES: UNLESS OTHERWISE SPECIFIED

THIS PACKAGE CONFORMS TO

- A) JEDEC MS-001 VARIATION AA
- B) ALL DIMENSIONS ARE IN MILLIMETERS.

DIMENSIONS ARE EXCLUSIVE OF BURRS,

- C) MOLD FLASH, AND TIE BAR EXTRUSIONS.
- D) DIMENSIONS AND TOLERANCES PER ASME Y14.5-1994
- E) DRAWING FILE NAME: MKT-N14AREV8

Figure 15. 14-LEAD, MDIP, JEDEC MS-001, .300 INCH WIDE

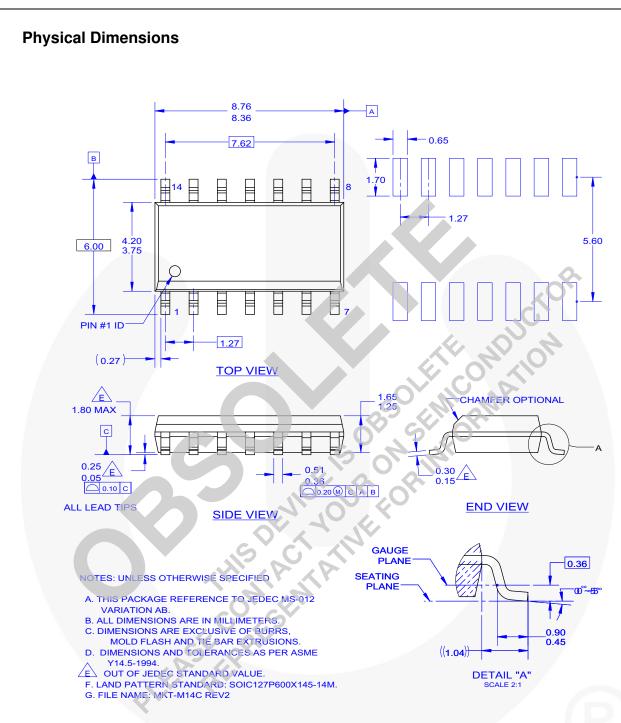


Figure 16. 14-Lead, SOIC, NON-JEDEC, .150 INCH NARROW BODY, 225SOP





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Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit narts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild surongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition			
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.			
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.			
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.			
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.			

Rev. 172



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