Product data sheet

1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1010B-6 (SOT1216) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Trench MOSFET technology
- Leadless ultra small and ultra thin SMD plastic package: 1.1 × 1.0 × 0.37 mm
- Exposed drain pad for excellent thermal conduction
- ElectroStatic Discharge (ESD) protection > 1 kV HBM
- Drain-source on-state resistance R_{DSon} = 470 mΩ

3. Applications

- Relay driver
- · High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	20	V
V _{GS}	gate-source voltage			-8	-	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	-	600	mA
Static characteristics (per transistor)							
R _{DSon}	drain-source on-state resistance	V_{GS} = 4.5 V; I_D = 600 mA; T_j = 25 °C		-	470	620	mΩ

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	500	D1 D2
2	G1	gate TR1	$\begin{bmatrix} 1 \\ 7 \end{bmatrix} \begin{bmatrix} 6 \\ \end{bmatrix}$	
3	D2	drain TR2	2 5	G1 $G2$ $G2$
4	S2	source TR2		
5	G2	gate TR2	3 4	
6	D1	drain TR1	Transparent top view	S1 S2 017aaa256
7	D1	drain TR1	DFN1010B-6 (SOT1216)	
8	D2	drain TR2		

6. Ordering information

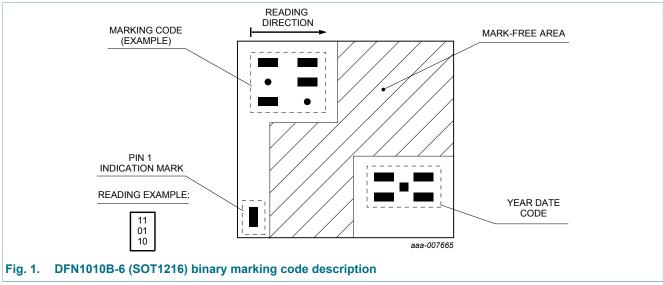
Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMDXB600UNE	DFN1010B-6	DFN1010B-6: plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals	SOT1216		

7. Marking

Table 4. Marking codes

Type number	Marking code
PMDXB600UNE	00 10 00



PMDXB600UNE

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transis	tor					
V _{DS}	drain-source voltage	T _j = 25 °C		-	20	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C		-	600	mA
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	400	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \mu s$		-	2.5	Α
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	265	mW
			[1]	-	380	mW
		T _{sp} = 25 °C		-	4025	mW
Source-dra	in diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	0.4	Α
Per device						
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

^[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

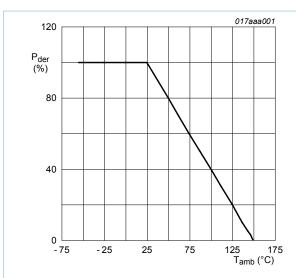


Fig. 2. Normalized total power dissipation as a function of ambient temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25\%)}} \times 100\%$$

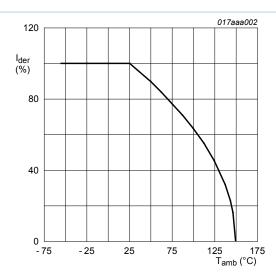


Fig. 3. Normalized continuous drain current as a function of ambient temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

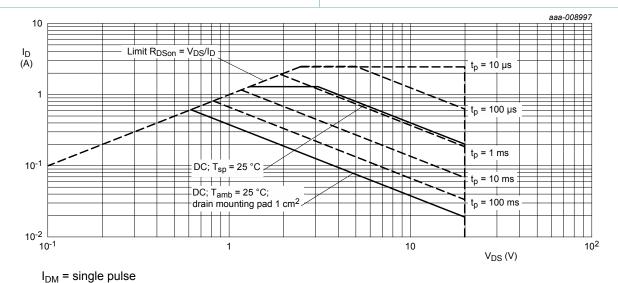


Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drainsource voltage

9. Thermal characteristics

Table 6. Thermal characteristics

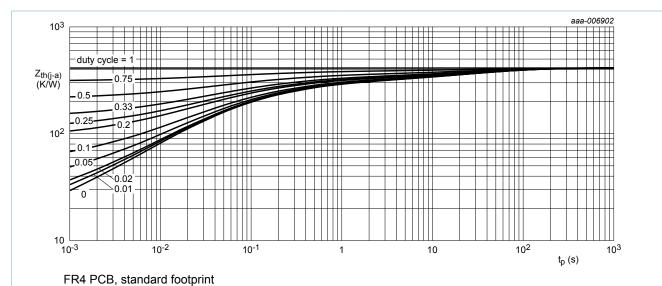
Table of Thomas of a factorious							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
R _{th(j-a)}	thermal resistance		[1]	-	410	475	K/W
	from junction to ambient		[2]	-	285	330	K/W

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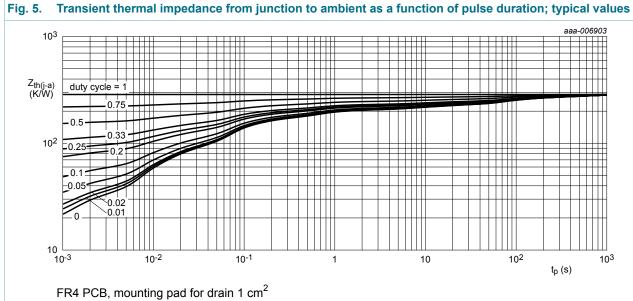
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		-	27	31	K/W

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².



Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



Transient thermal impedance from junction to ambient as a function of pulse duration; typical values Fig. 6.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics (per transistor)					
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	20	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	0.45	0.7	0.95	V
I _{DSS}	drain leakage current	V _{DS} = 20 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μA
I _{GSS}	gate leakage current	V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	10	μA
		V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-10	μA
		V _{GS} = 4.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	1	μA
		$V_{GS} = -4.5 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-	-1	μA
R _{DSon}	drain-source on-state	V _{GS} = 4.5 V; I _D = 600 mA; T _j = 25 °C	-	470	620	mΩ
resistance	resistance	V _{GS} = 4.5 V; I _D = 600 mA; T _j = 150 °C	-	760	1000	mΩ
		V_{GS} = 2.5 V; I_D = 500 mA; T_j = 25 °C	-	620	850	mΩ
		V _{GS} = 1.8 V; I _D = 100 mA; T _j = 25 °C	-	845	1300	mΩ
		V _{GS} = 1.5 V; I _D = 10 mA; T _j = 25 °C	-	1125	3000	mΩ
	V _{GS} = 1.2 V; I _D = 1 mA; T _j = 25 °C	-	2210	-	mΩ	
9 _{fs}	forward transconductance	$V_{DS} = 5 \text{ V}; I_D = 0.6 \text{ A}; T_j = 25 \text{ °C}$	-	1	-	S
Dynamic ch	naracteristics (per transist	or)				
Q _{G(tot)}	total gate charge	V _{DS} = 10 V; I _D = 600 mA; V _{GS} = 4.5 V;	-	0.4	0.7	nC
Q_{GS}	gate-source charge	T _j = 25 °C	-	0.1	-	nC
Q_{GD}	gate-drain charge		-	0.1	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V;	-	21.3	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	5.4	-	pF
C _{rss}	reverse transfer capacitance		-	4.2	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 10 V; I_{D} = 600 mA; V_{GS} = 4.5 V;	-	5.6	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	9.2	-	ns
t _{d(off)}	turn-off delay time		-	19	-	ns
t _f	fall time		-	51	-	ns
Source-dra	in diode (per transistor)		l	1	-	
V_{SD}	source-drain voltage	I _S = 0.36 A; V _{GS} = 0 V; T _i = 25 °C	-	0.8	1.2	V

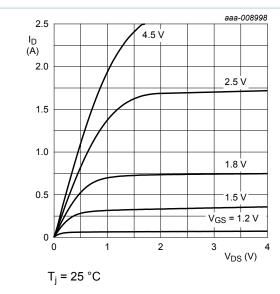


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

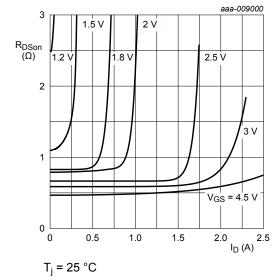


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

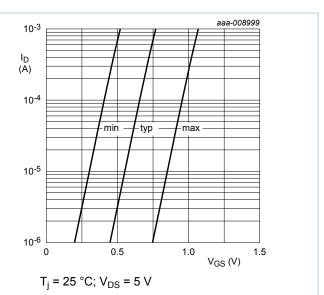


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

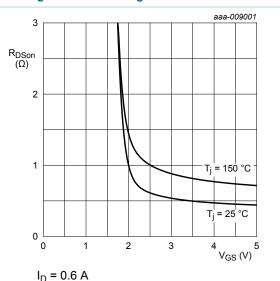


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

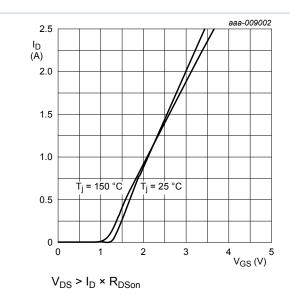


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

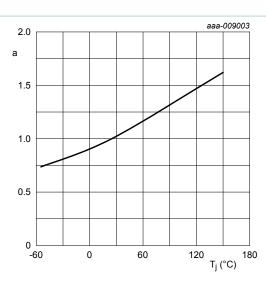


Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

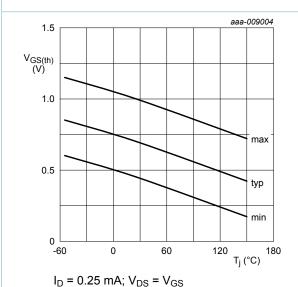
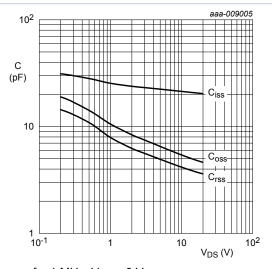


Fig. 13. Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$

Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

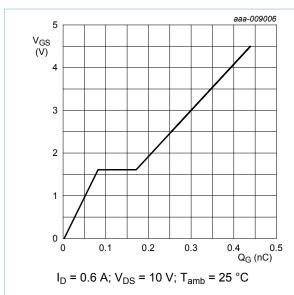


Fig. 15. Gate-source voltage as a function of gate charge; typical values

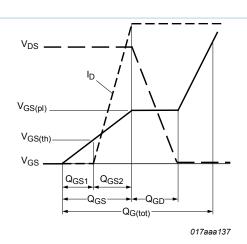


Fig. 16. MOSFET transistor: Gate charge waveform definitions

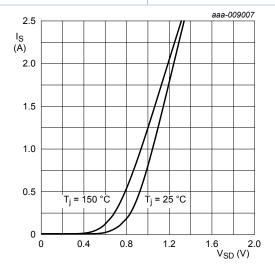
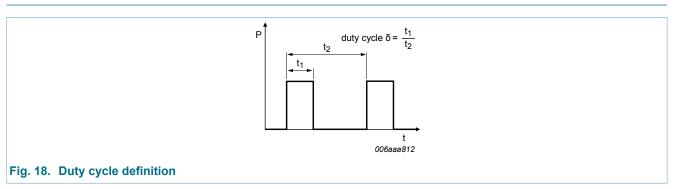


Fig. 17. Source current as a function of source-drain voltage; typical values

11. Test information

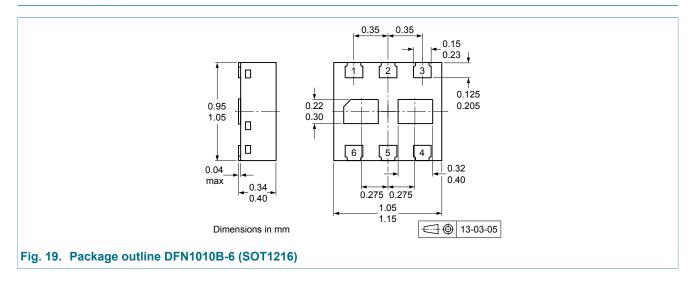
 $V_{GS} = 0 V$



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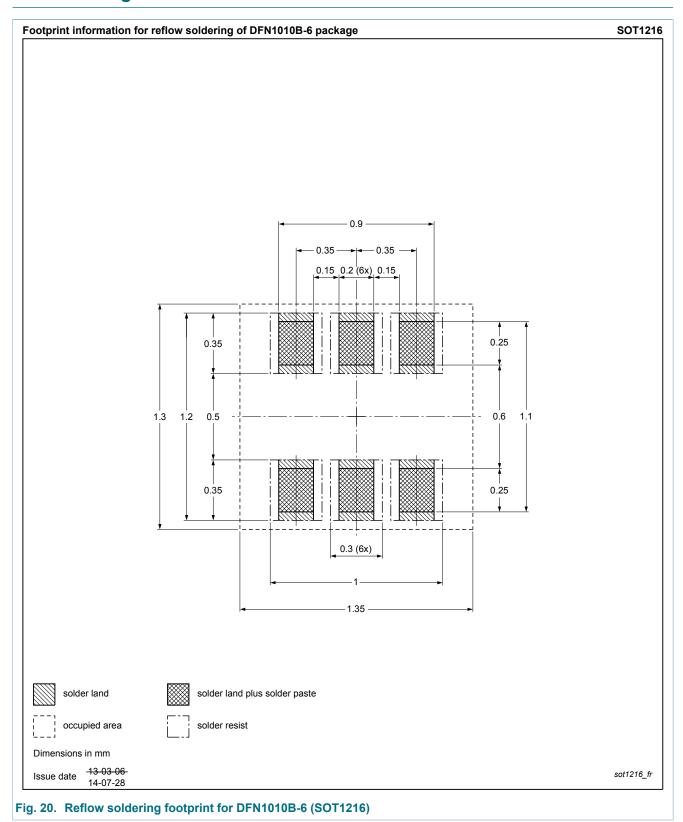
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12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMDXB600UNE v.2	20150701	Product data sheet	-	PMDXB600UNE v.1		
Modifications:	Change of	Change of binary marking code position.				
PMDXB600UNE v.1	20130916	Product data sheet	-	-		

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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