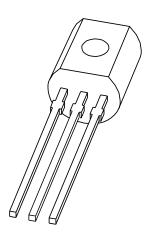
### DISCRETE SEMICONDUCTORS

# DATA SHEET



2N5550; 2N5551 NPN high-voltage transistors

Product specification Supersedes data of 1999 Apr 23 2004 Oct 28





Philips Semiconductors

### **NPN** high-voltage transistors

2N5550; 2N5551

### **FEATURES**

- Low current (max. 300 mA)
- High voltage (max. 160 V).

### **APPLICATIONS**

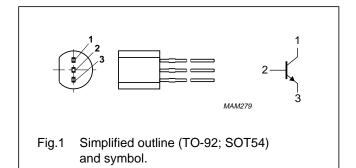
• Switching and amplification in high voltage applications such as telephony.

### **DESCRIPTION**

NPN high-voltage transistor in a TO-92; SOT54 plastic package. PNP complements: 2N5400 and 2N5401.

### **PINNING**

PIN	DESCRIPTION
1	collector
2	base
3	emitter



### **ORDERING INFORMATION**

TYPE NUMBER		PACKAGE				
		DESCRIPTION	VERSION			
2N5550	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54			
2N5551						

### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	2N5550		_	160	V
	2N5551		_	180	V
V <sub>CEO</sub>	collector-emitter voltage	open base			
	2N5550		_	140	V
	2N5551		_	160	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	6	V
Ic	collector current (DC)		_	300	mA
I <sub>CM</sub>	peak collector current		_	600	mA
I <sub>BM</sub>	peak base current		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	_	630	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C

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### NPN high-voltage transistors

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### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	200	K/W

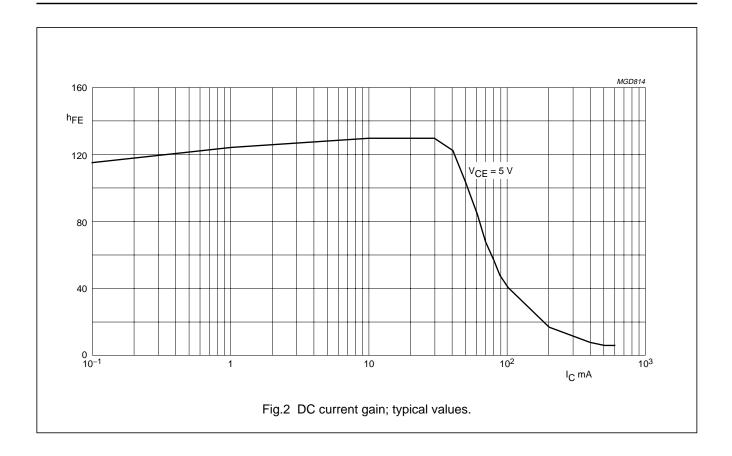
### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current				
	2N5550	$V_{CB} = 100 \text{ V}; I_E = 0 \text{ A}$	_	100	nA
		V <sub>CB</sub> = 100 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 100 °C	_	100	μΑ
	collector-base cut-off current				
	2N5551	V <sub>CB</sub> = 120 V; I <sub>E</sub> = 0 A	-	50	nA
		V <sub>CB</sub> = 120 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 100 °C	_	50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0 A	_	50	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 \text{ V}$ ; $I_{C} = 1 \text{ mA}$ ; see Fig.2			
	2N5550		60	_	
	2N5551		80	_	
	DC current gain	$V_{CE} = 5 \text{ V; } I_{C} = 10 \text{ mA; see Fig.2}$			
	2N5550		60	250	
	2N5551		80	250	
	DC current gain	$V_{CE} = 5 \text{ V}$ ; $I_{C} = 50 \text{ mA}$ ; see Fig.2			
	2N5550		20	_	
	2N5551		30	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA			
	2N5550		_	150	mV
	2N5551		_	150	mV
	collector-emitter saturation voltage	I <sub>C</sub> = 50 mA; I <sub>B</sub> = 5 mA			
	2N5550		_	250	mV
	2N5551		_	200	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA	_	1	V
		I <sub>C</sub> = 50 mA; I <sub>B</sub> = 5 mA	_	1	V
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	_	6	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_C = I_C = 0 \text{ A}; f = 1 \text{ MHz}$	_	30	pF
f <sub>T</sub>	transition frequency	$V_{CE} = 10 \text{ V}; I_{C} = 10 \text{ mA}; f = 100 \text{ MHz}$	100	300	MHz
F	noise figure	$V_{CE} = 5 \text{ V}; I_C = 200 \mu\text{A}; R_S = 2 \text{ k}\Omega;$			
	2N5550	f = 10 Hz to 15.7 kHz	_	10	dB
	2N5551		-	8	dB

### NPN high-voltage transistors

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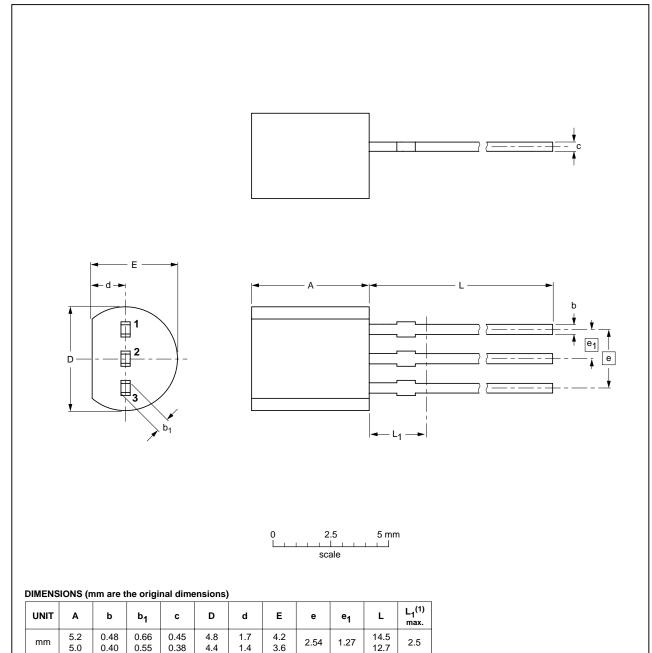
### NPN high-voltage transistors

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### PACKAGE OUTLINE

### Plastic single-ended leaded (through hole) package; 3 leads

SOT54



1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE	REFERENCES EUROF			REFERENCES		EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT54		TO-92	SC-43A			<del>97-02-28</del> 04-06-28	

### NPN high-voltage transistors

2N5550; 2N5551

#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)(3)</sup>	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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