

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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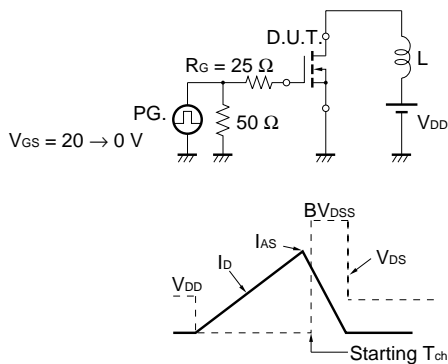
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ELECTRICAL CHARACTERISTICS (TA = 25°C, All terminals are connected.)

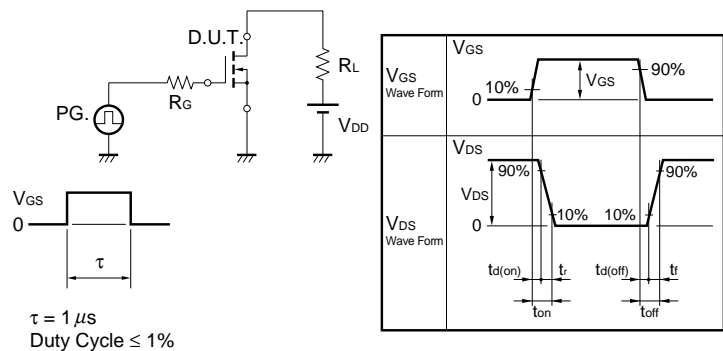
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$			1	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 12\text{ V}, V_{DS} = 0\text{ V}$			±10	μA
Gate Cut-off Voltage ^{Note}	$V_{GS(off)}$	$V_{DS} = 10\text{ V}, I_D = 1\text{ mA}$	0.5		1.5	V
Forward Transfer Admittance ^{Note}	$ y_{fs} $	$V_{DS} = 10\text{ V}, I_D = 5.5\text{ A}$	8	16		S
Drain to Source On-state Resistance ^{Note}	$R_{DS(on)1}$	$V_{GS} = 4.5\text{ V}, I_D = 5.5\text{ A}$		11.5	14.5	mΩ
	$R_{DS(on)2}$	$V_{GS} = 4.0\text{ V}, I_D = 5.5\text{ A}$		11.8	15.0	mΩ
	$R_{DS(on)3}$	$V_{GS} = 3.1\text{ V}, I_D = 5.5\text{ A}$		12.7	16.9	mΩ
	$R_{DS(on)4}$	$V_{GS} = 2.5\text{ V}, I_D = 5.5\text{ A}$		13.9	18.6	mΩ
Input Capacitance	C_{iss}	$V_{DS} = 10\text{ V}$		1940		pF
Output Capacitance	C_{oss}	$V_{GS} = 0\text{ V}$		385		pF
Reverse Transfer Capacitance	C_{rss}	$f = 1\text{ MHz}$		270		pF
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}, I_D = 5.5\text{ A}$		21		ns
Rise Time	t_r	$V_{GS} = 4.5\text{ V}$		45		ns
Turn-off Delay Time	$t_{d(off)}$	$R_G = 10\ \Omega$		75		ns
Fall Time	t_f			30		ns
Total Gate Charge	Q_G	$V_{DD} = 24\text{ V}$		25		nC
Gate to Source Charge	Q_{GS}	$V_{GS} = 4.5\text{ V}$		3		nC
Gate to Drain Charge	Q_{GD}	$I_D = 11\text{ A}$		10		nC
Body Diode Forward Voltage	$V_{F(S-D)}$	$I_F = 11\text{ A}, V_{GS} = 0\text{ V}$		0.81	1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 11\text{ A}, V_{GS} = 0\text{ V}$		47		ns
Reverse Recovery Charge	Q_{rr}	$di/dt = 100\text{ A}/\mu\text{s}$		41		nC

Note Pulsed: $PW \leq 350\ \mu\text{s}$, Duty Cycle $\leq 2\%$

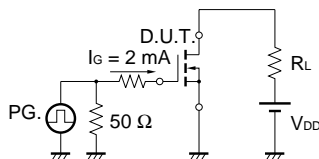
TEST CIRCUIT 1 AVALANCHE CAPABILITY



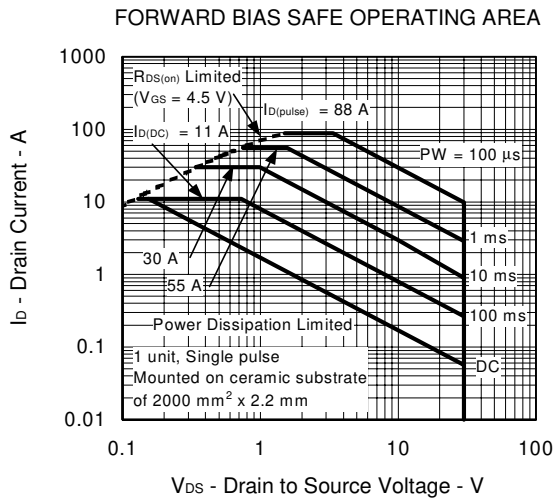
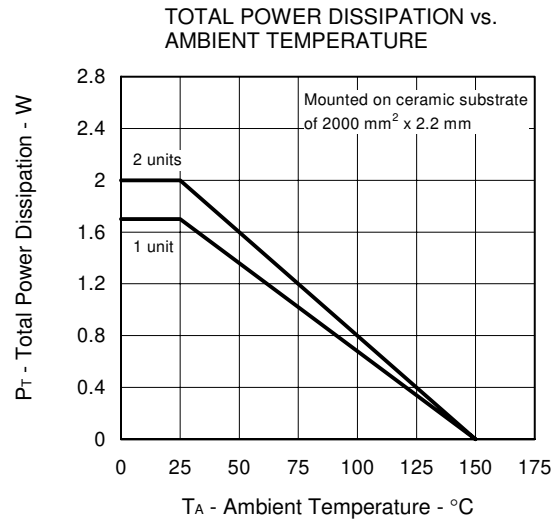
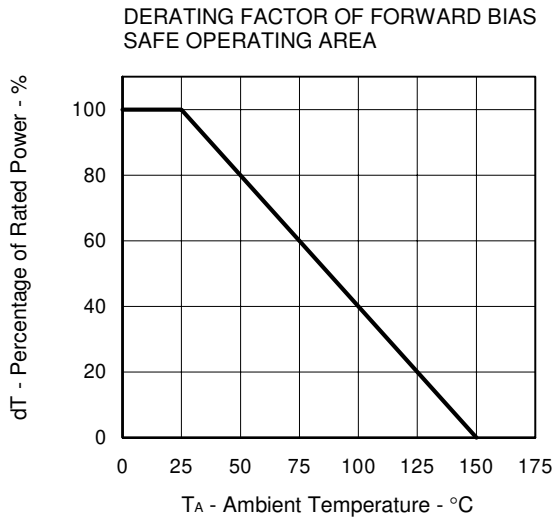
TEST CIRCUIT 2 SWITCHING TIME



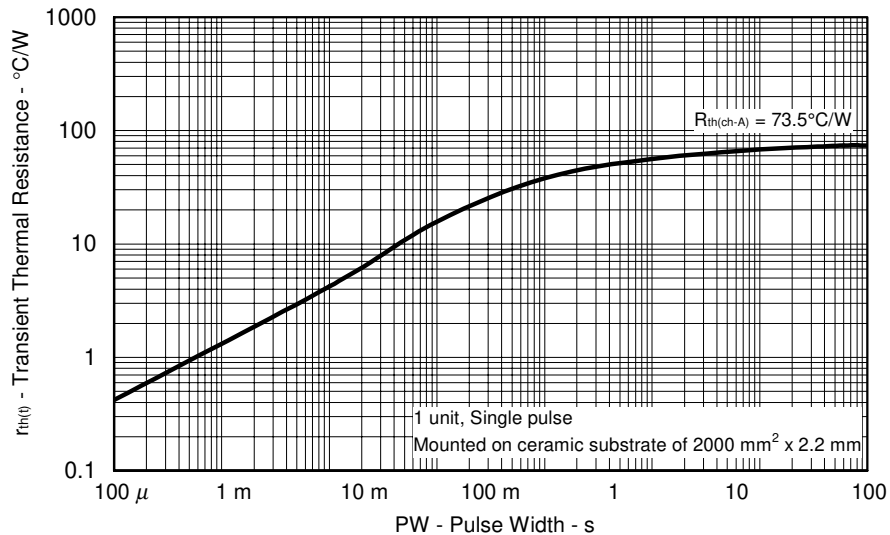
TEST CIRCUIT 3 GATE CHARGE



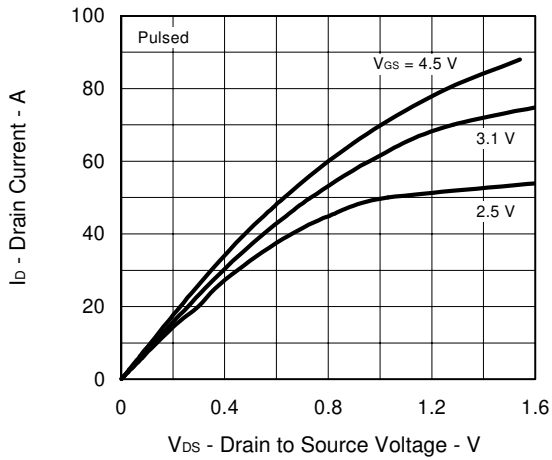
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, All terminals are connected.)



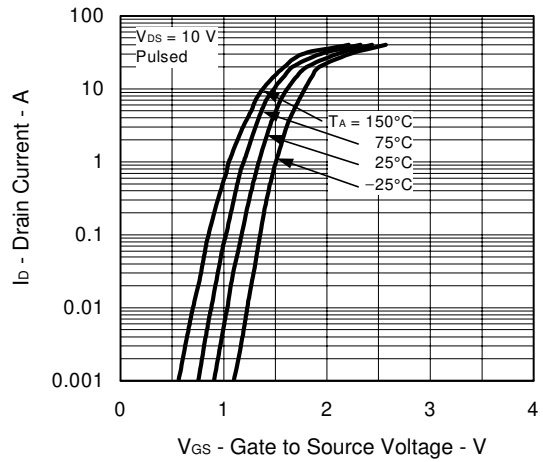
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



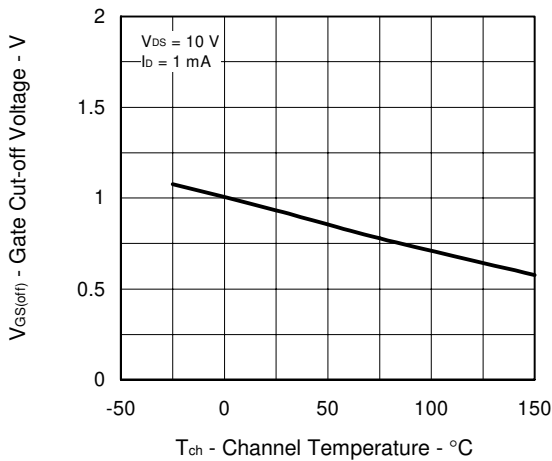
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



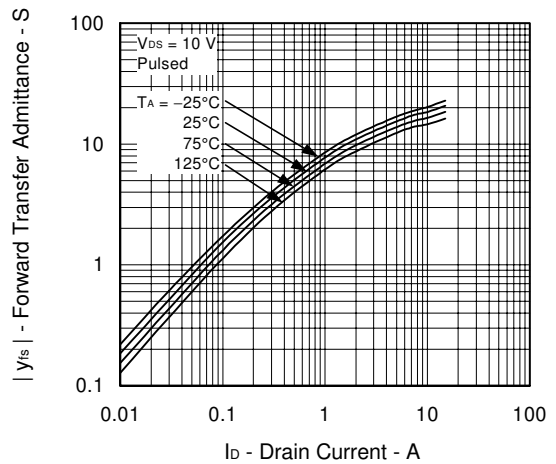
FORWARD TRANSFER CHARACTERISTICS



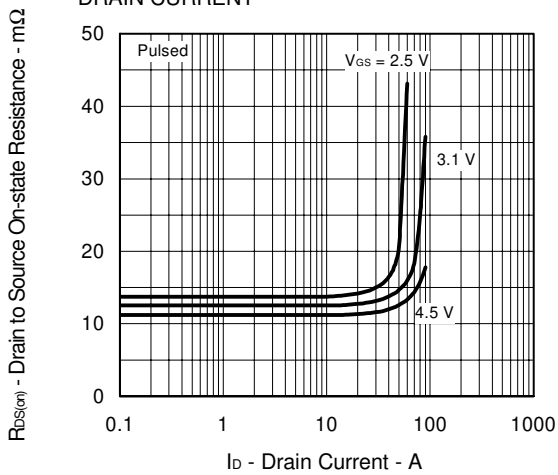
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



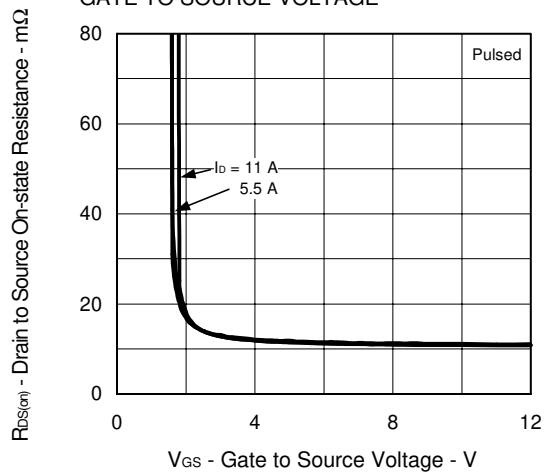
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



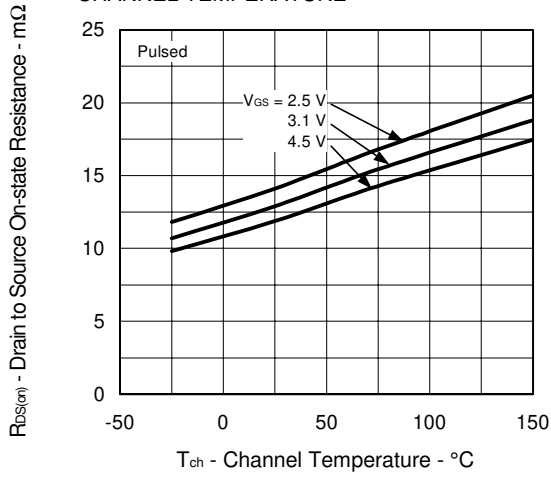
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



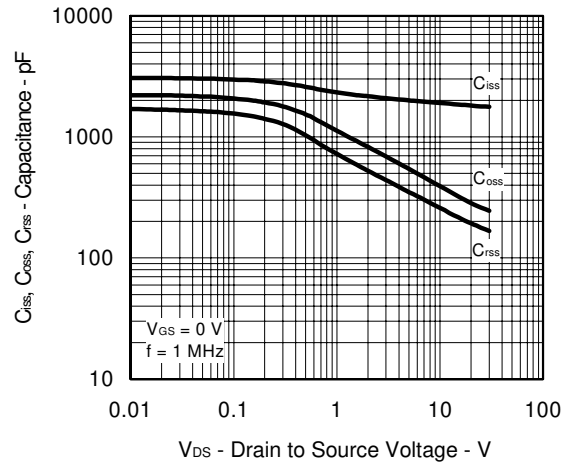
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



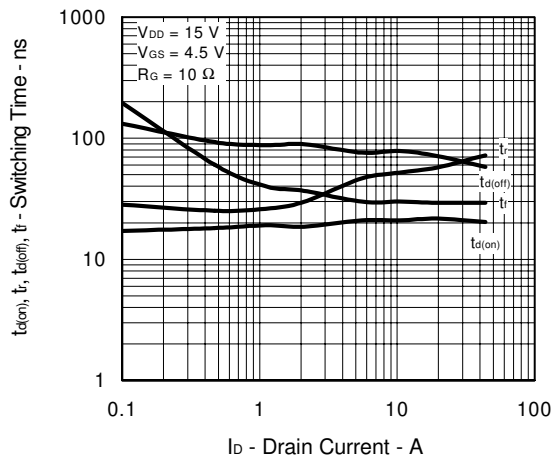
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



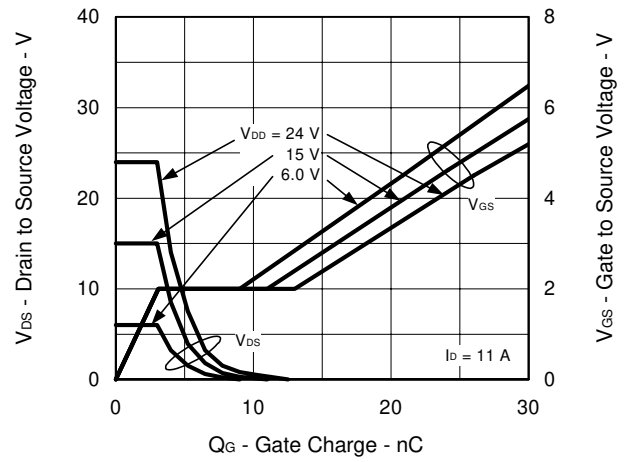
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



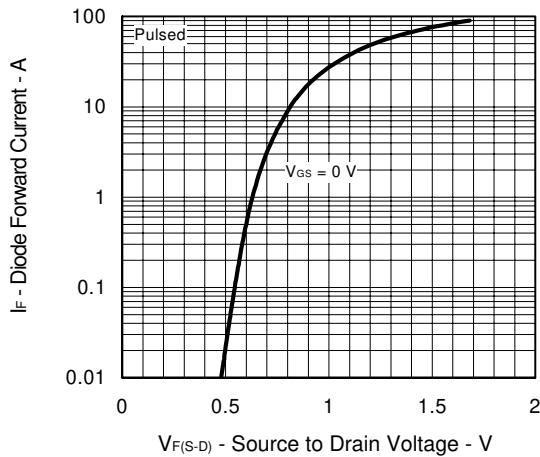
SWITCHING CHARACTERISTICS



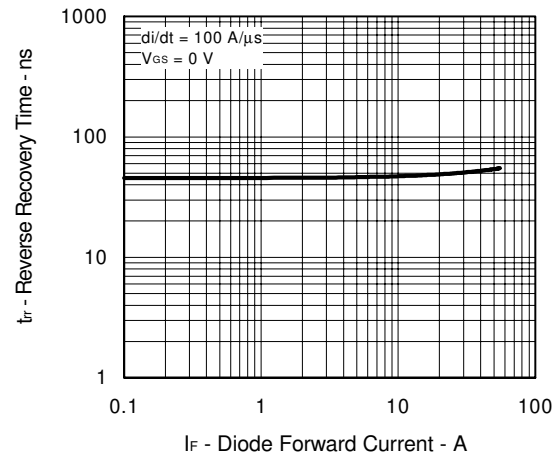
DYNAMIC INPUT/OUTPUT CHARACTERISTICS

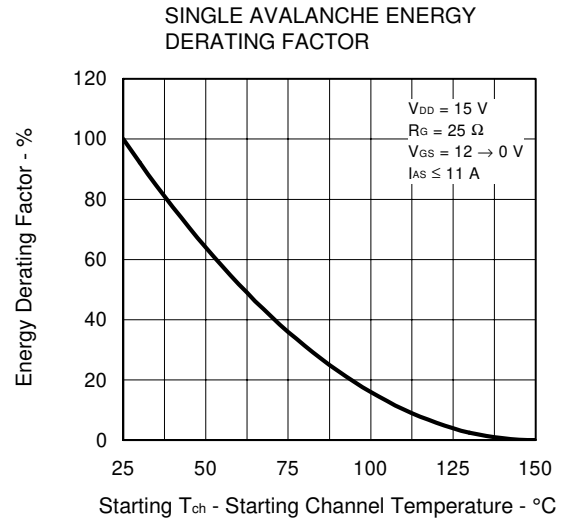
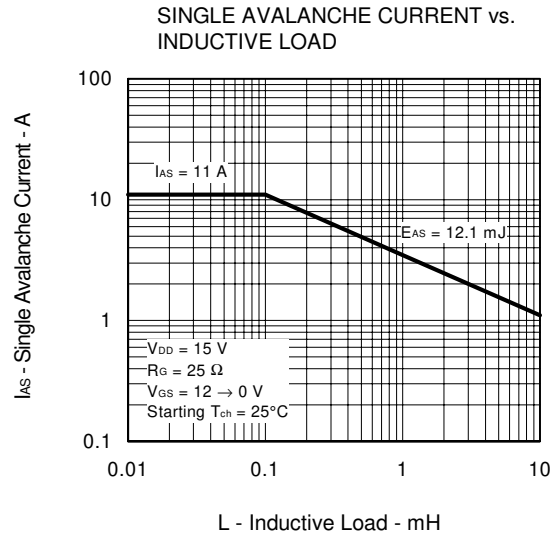


SOURCE TO DRAIN DIODE FORWARD VOLTAGE



REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT





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