



### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	Ι <sub>D</sub> T <sub>C</sub> = +25°C
40V	4.7mΩ @ V <sub>GS</sub> = 10V	100A

# **Description and Applications**

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

TO220-3

- Engine Management Systems
- **Body Control Electronics**
- **DC-DC Converters**

#### 40V N-CHANNEL ENHANCEMENT MODE MOSFET

#### Features

- 100% Unclamped Inductive Switching ensures more reliable and robust end application
- Low Input Capacitance
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### Mechanical Data

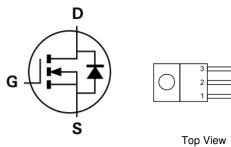
- Case: TO220-3 •
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram Below
- Weight: 1.85 grams (Approximate)



Top View



Bottom View





Pin Out Configuration

### Ordering Information (Note 4)

Part Number		Case	Packaging			
DMT4005SCT		TO220-3	50 pieces/tube			
Notes:	Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.					

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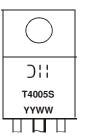
2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.

Equivalent Circuit

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# Marking Information



DII = Manufacturer's Marking T4005S = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 16 = 2016) WW = Week Code (01 to 53)



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V <sub>DSS</sub>	40	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
	$T_{\rm C} = +25^{\circ}{\rm C}$		100	•
Continuous Drain Current (Note 6)	$T_{C} = +70^{\circ}C$	ID	85	А
Maximum Continuous Body Diode Forward Current (Note 6)	T <sub>C</sub> = +25°C	Is	85	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	160	A	
Avalanche Current, L=0.1mH	I <sub>AS</sub>	32.5	A	
Avalanche Energy, L=0.1mH	E <sub>AS</sub>	52.8	mJ	

# **Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	2.3	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{ extsf{ heta}JA}$	52.8	°C/W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	PD	104	W
Thermal Resistance, Junction to Case (Note 6)		$R_{ extsf{ heta}JC}$	1.2	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

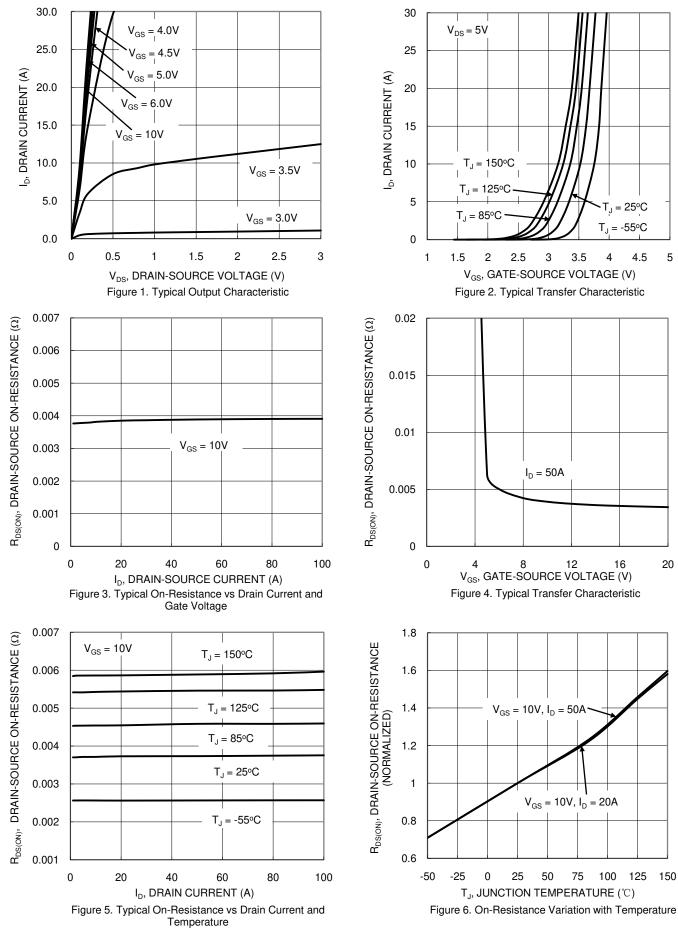
<b>a</b>			-			<b>T</b> 10 IV	
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)						-	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40	—	—	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS		—	1	μA	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	—	4	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		3.8	4.7	mΩ	$V_{GS} = 10V, I_D = 50A$	
Diode Forward Voltage	V <sub>SD</sub>		—	1.2	V	$V_{GS} = 0V, I_{S} = 50A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		3062	-		$V_{DS} = 20V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss		902	_	pF		
Reverse Transfer Capacitance	Crss		179	-			
Gate Resistance	R <sub>G</sub>	_	0.67	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg		49.1	_		$\label{eq:VDD} \begin{array}{l} V_{DD} = 20V, \ I_D = 50A, \\ V_{GS} = 10V \end{array}$	
Gate-Source Charge	Qgs	_	10.3	_	nC		
Gate-Drain Charge	Q <sub>gd</sub>	_	13	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	8.7	_		$V_{DD} = 20V, V_{GS} = 10V,$ $I_D = 50A, R_G = 3\Omega$	
Turn-On Rise Time	t <sub>R</sub>	_	6.8	_			
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	18.6	_	ns		
Turn-Off Fall Time	tF	_	7.3	—			
Reverse Recovery Time	t <sub>RR</sub>	_	31.8	_	ns		
Reverse Recovery Charge	Q <sub>RR</sub>	—	26.5	—	nC	I <sub>F</sub> = 50A, di/dt = 100A/μs	

 Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
Device mounted on infinite heat sink. Notes:

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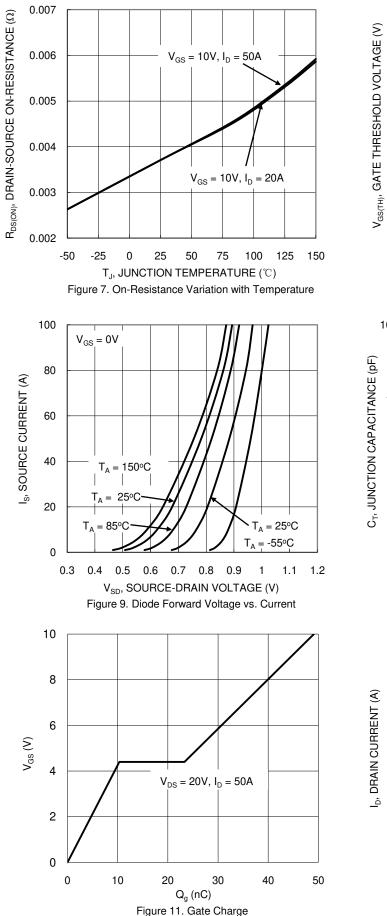


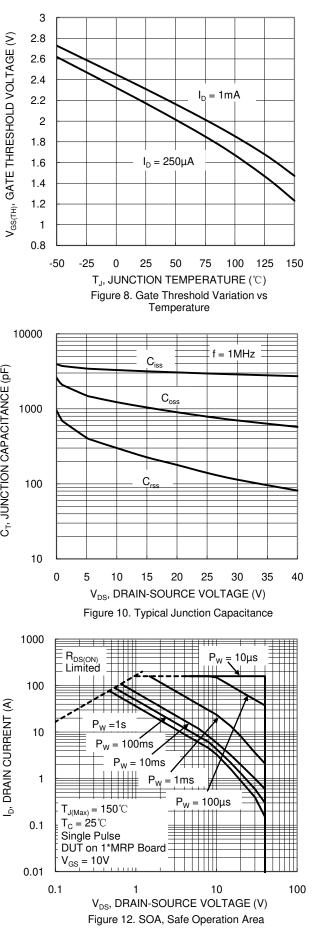
### DMT4005SCT



DMT4005SCT Document number: DS38889 Rev. 1 - 2

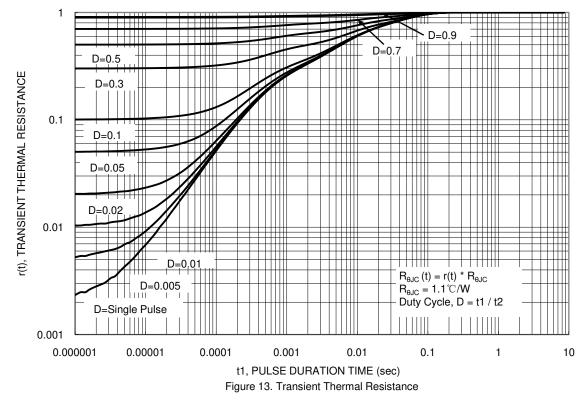






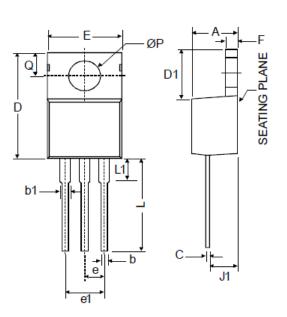
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# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



	TO220-3				
Dim	Min	Max			
Α	3.55	4.85			
b	0.51	1.14			
b1	1.14	1.78			
С	0.31	1.14			
D	14.20	16.50			
D1	5.84	6.86			
Е	9.70	10.70			
е	2.79	2.99			
e1	4.83	5.33			
F	0.51	1.40			
J1	2.03	2.92			
L	12.72	14.72			
L1	3.66	6.35			
Р	3.53	4.09			
Q	2.54	3.43			
All Dimensions in mm					

#### TO220-3



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