

RD3L07BBG

Nch 60V 115A Power MOSFET

V _{DSS}	60V
R _{DS(on)} (Max.)	3.9mΩ
I _D	115A
P_D	102W

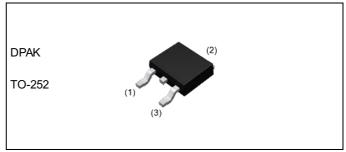
● Features

- 1) Low on resistance
- 2) Pb-free plating; RoHS compliant
- 3) Halogen free

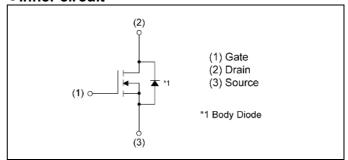
Application

Switching

Outline



Inner circuit



Packaging specifications

	jing opodinoanono	
	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	16
	Quantity (pcs)	2500
	Taping code	TL1
	Marking	RD3L07BBG

● **Absolute maximum** ratings (T_a = 25°C ,unless otherwise specified)

Parar	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	60	V	
Continuous drain current	Silicon limit (V _{GS} =10V)	I _D *1	115	Α
Continuous drain current	$T_a = 25^{\circ}C (V_{GS} = 10V)$	I _D *2	±70	Α
Pulsed drain current	I _{DP} *3	±460	Α	
Gate - Source voltage	V_{GSS}	±20	V	
Avalanche current, single pu	I _{AS} *4	33	Α	
Avalanche energy, single pu	E _{AS} *4	85	mJ	
Power dissipation	P _D *2	102	W	
Junction temperature	T _j	150	°C	
Operating junction and stora	T _{stg}	-55 to +150	°C	

●Thermal resistance

Parameter	Symbol	Values			Lleit
		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *2	-	-	1.22	°C/W

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymah ol	Conditions	Values			l limit	
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	38.9	-	mV/°C	
Zero gate voltage drain current	I_{DSS} $V_{DS} = 60V, V_{GS} = 0V$		-	-	2	μA	
Gate - Source leakage current	I _{GSS}	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		-	±200	nA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 1mA$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}} I_{D} = 1 \text{mA}$ referenced to 25°C		-	-4.7	-	mV/°C	
Static drain - source	R _{DS(on)} *5	V _{GS} = 10V, I _D = 70A	-	3.0	3.9	mO.	
on - state resistance		V _{GS} = 4.5V, I _D = 35A	-	4.1	5.7	mΩ	
Gate resistance	R _G -		-	0.9	-	Ω	
Forward Transfer Admittance	Y _{fs} *5	V _{DS} = 5V, I _D = 70A	72	-	-	S	

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^{*1} Limited by silicon chip capability. Package limit is 70A.

^{*2} T_c =25°C, Limited only by maximum temperature allowed.

^{*3} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*4} L \simeq 0.1mH, V_{DD} = 30V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C)

Daramatar	Symbol .	Conditions	Values			Linit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	2950	-		
Output capacitance	C _{oss}	V _{DS} = 30V	-	750	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	55	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 30V, V_{GS} = 10V$	1	21	1		
Rise time	t r*5	I _D = 35A	1	12	ı	no	
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 0.85\Omega$		72	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	27	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	nbol Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total materials and	O *5	VGS	V _{GS} = 10V	-	47.0	-	
Total gate charge	Q _g *5	$V_{DD} \simeq 30V$		-	23.0	-	~ C
Gate - Source charge	Q _{gs} *5	I _D = 50A	V _{GS} = 4.5V	-	8.8	-	nC
Gate - Drain charge	Q _{gd} *5			-	7.9	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Cumbal	Conditions	Values			Unit		
Parameter	Symbol	I Conditions		Symbol Conditions		Тур.	Max.	Offic
Continuous forward current	I _S	T = 25°C	1	-	70	Α		
Pulse forward current	I _{SP} *3	T _a = 25°C	-	-	460	Α		
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 70A	-	-	1.2	V		
Reverse recovery time	t _{rr} *5	I _S = 50A, V _{GS} =0V	-	49	-	ns		
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	64	-	nC		

Fig.1 Power Dissipation Derating Curve

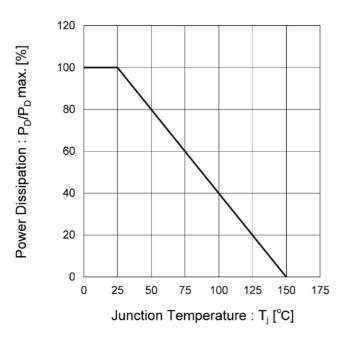
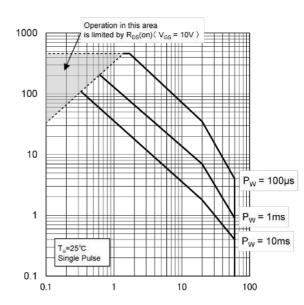


Fig.2 Maximum Safe Operating Area



Drain Current: Ip [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

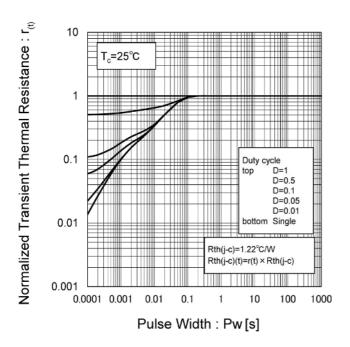


Fig.4 Single Pulse Maximum Power Dissipation

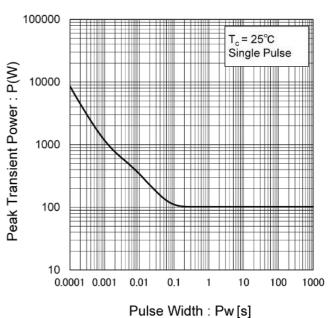
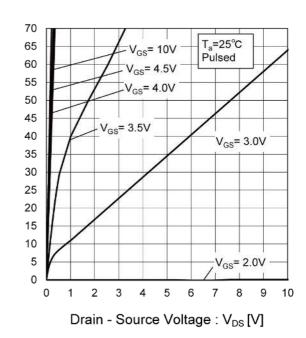


Fig.5 Typical Output Characteristics(I)

70 65 T_a=25°C 60 Pulsed 55 V_{GS}= 4.0V Drain Current : I_D [A] 50 45 V_{GS}= 3.5V 40 35 30 25 20 V_{GS}= 3.0V 15 10 5 $V_{GS} = 2.0V$ 0 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9

Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain Current: Ip [A]

Fig.7 Breakdown Voltage vs.

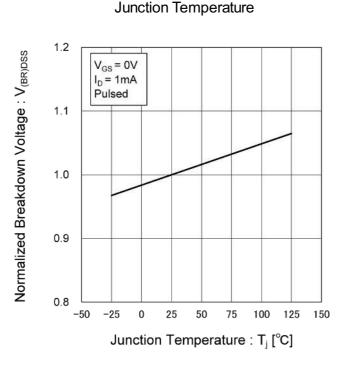
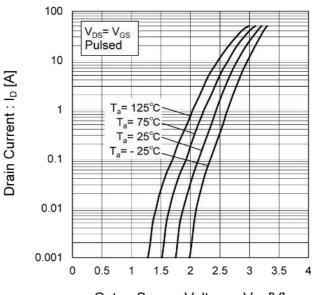


Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : $V_{GS}[V]$

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

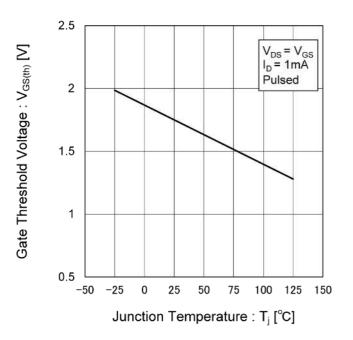


Fig.10 Forward Transfer Admittance vs.
Drain Current

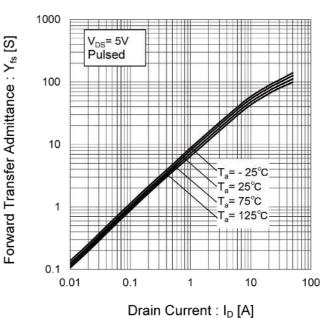


Fig.11 Drain Current Derating Curve

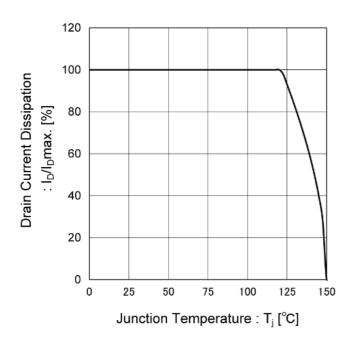


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

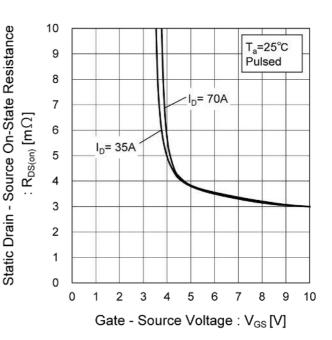


Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature

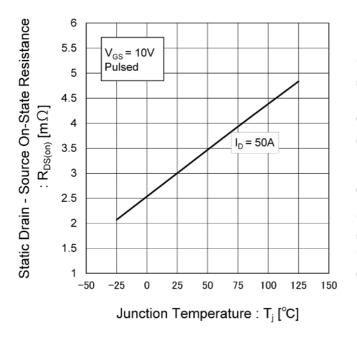


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

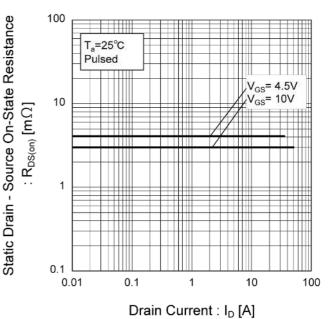


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

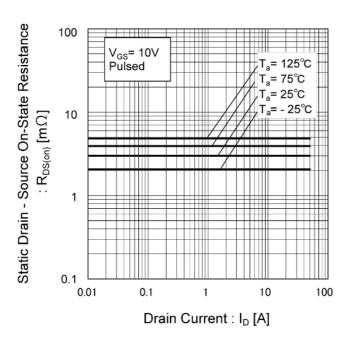


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

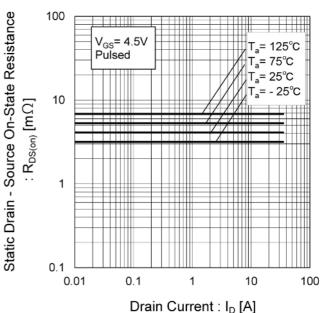


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

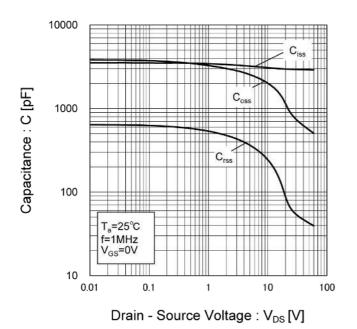


Fig.18 Switching Characteristics

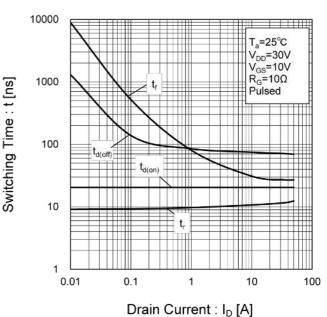


Fig.19 Typical Gate Charge

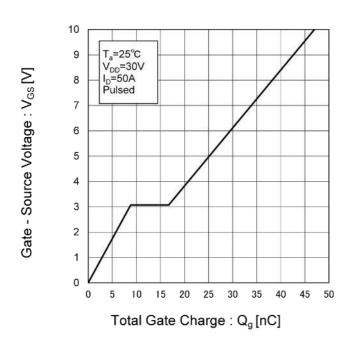
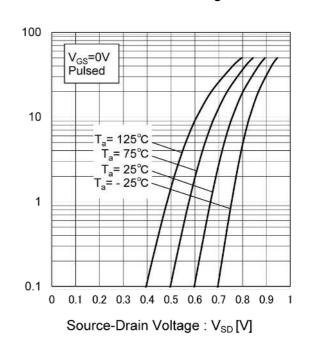


Fig.20 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

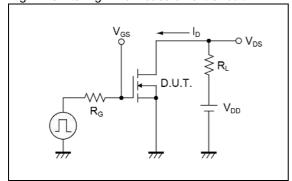


Fig.1-2 Switching Waveforms

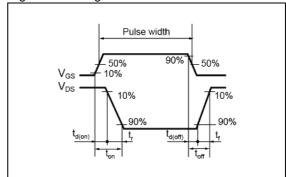


Fig.2-1 Gate Charge Measurement Circuit

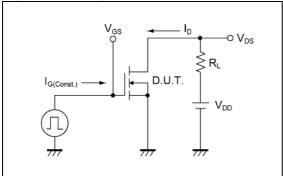


Fig.2-2 Gate Charge Waveform

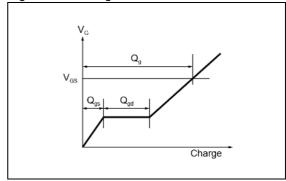


Fig.3-1 Avalanche Measurement Circuit

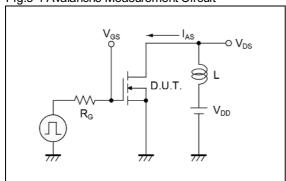
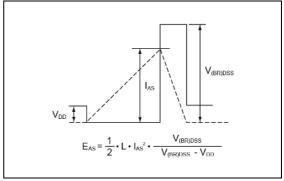


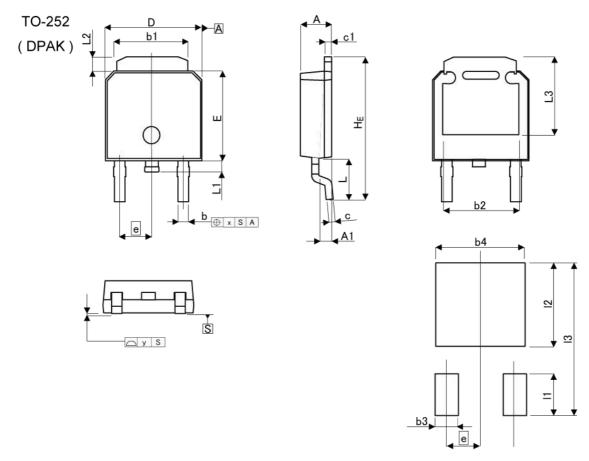
Fig.3-2 Avalanche Waveform



Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIME	ETERS	INCHES		
DIIVI	MIN	MAX	MIN	MAX	
Α	2.20	2.40	0.087	0.094	
A1	0.70	1.10	0.028	0.043	
b	0.60	0.90	0.024	0.035	
b1	5.20	5.50	0.205	0.217	
b2	4.	80	0.1	89	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.	30	0.0	91	
E	6.00	6.40	0.236	0.252	
HE	9.40	10.40	0.370	0.409	
L	2.	90	0.114		
L1	0.60	1.00	0.024	0.039	
L2	0.70	1.30	0.028	0.051	
L3	5.	30	0.209		
х		0.25	(4))	0.010	
у	= [0.10	1577	0.004	
DIM -	MILIME	ETERS	INC	HES	
	MIN	MAX	MIN	MAX	
b3	#	1.15	7#8	0.045	
b4	-	5.55	A 5 0.	0.219	
11	-	2.77	\$(4 3)	0.109	
12		5.50	350	0.217	
13		10.40	-	0.409	

Dimension in mm/inches



Notice

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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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