

20 V, N-channel Trench MOSFET 24 March 2015

**Product data sheet** 

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1006-3 (SOT883) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- Very fast switching
- ElectroStatic Discharge (ESD) protection: 2 kV HBM
- Leadless ultra small SMD plastic package: 1.0 x 0.6 x 0.48 mm

### 3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

### 4. Quick reference data

Table 1. Quid	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	20	V
V <sub>GS</sub>	gate-source voltage			-8	-	8	V
I <sub>D</sub>	drain current	$V_{GS}$ = 4.5 V; $T_{amb}$ = 25 °C	[1]	-	-	1.2	А
Static characte	Static characteristics						
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 1.2 A; T <sub>j</sub> = 25 °C		-	270	320	mΩ

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.



# 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	1	D
2	S	source	2 2 3	
3	D	drain	Transparent top view DFN1006-3 (SOT883)	G S 017aaa255

# 6. Ordering information

Table 3.         Ordering information							
Type number	Package						
	Name	Description	Version				
PMZ290UNE2	DFN1006-3	DFN1006-3: leadless ultra small plastic package; 3 solder lands	SOT883				

# 7. Marking

Table 4. Marking codes	
Type number	Marking code
PMZ290UNE2	SC

### 8. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	20	V
V <sub>GS</sub>	gate-source voltage			-8	8	V
I <sub>D</sub>	drain current	$V_{GS}$ = 4.5 V; $T_{amb}$ = 25 °C	[1]	-	1.2	А
		$V_{GS}$ = 4.5 V; $T_{amb}$ = 100 °C	[1]	-	0.8	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	4	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	350	mW
			[1]	-	715	mW
		T <sub>sp</sub> = 25 °C		-	5430	mW
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-dra	in diode	,	1			
ls	source current	T <sub>amb</sub> = 25 °C	[1]	-	0.7	А

Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.
 Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

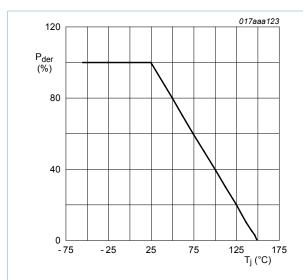
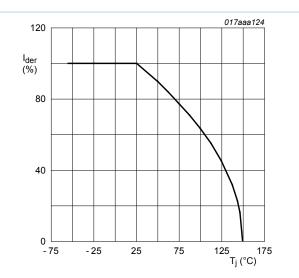


Fig. 1. Normalized total power dissipation as a function of junction temperature

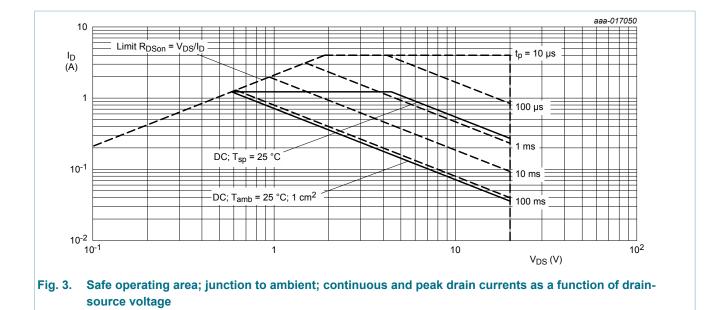
$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$





$$I_{der} = \frac{I_D}{I_{D(25^\circ C)}} \times 100 \%$$

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### 9. Thermal characteristics

#### Table 6. Thermal characteristics

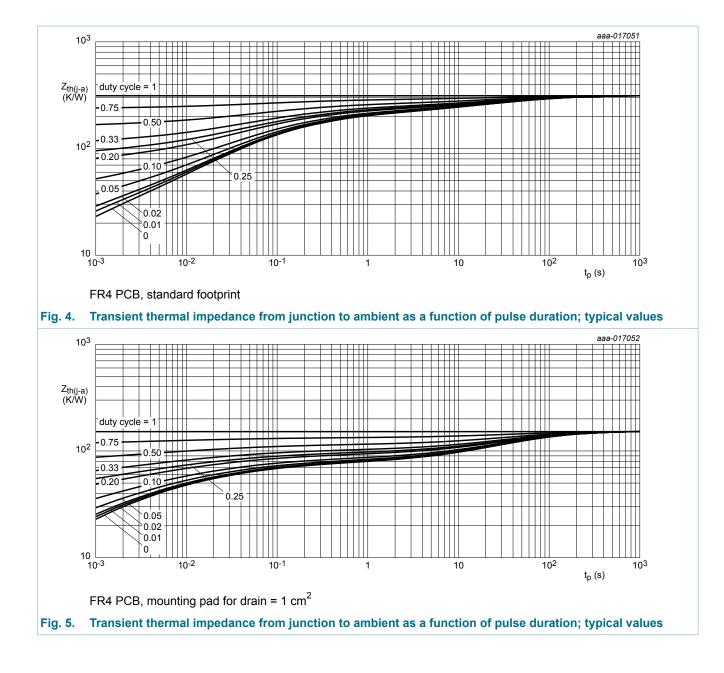
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	-	[1]	-	315	360	K/W
			[2]	-	150	175	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	20	23	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.

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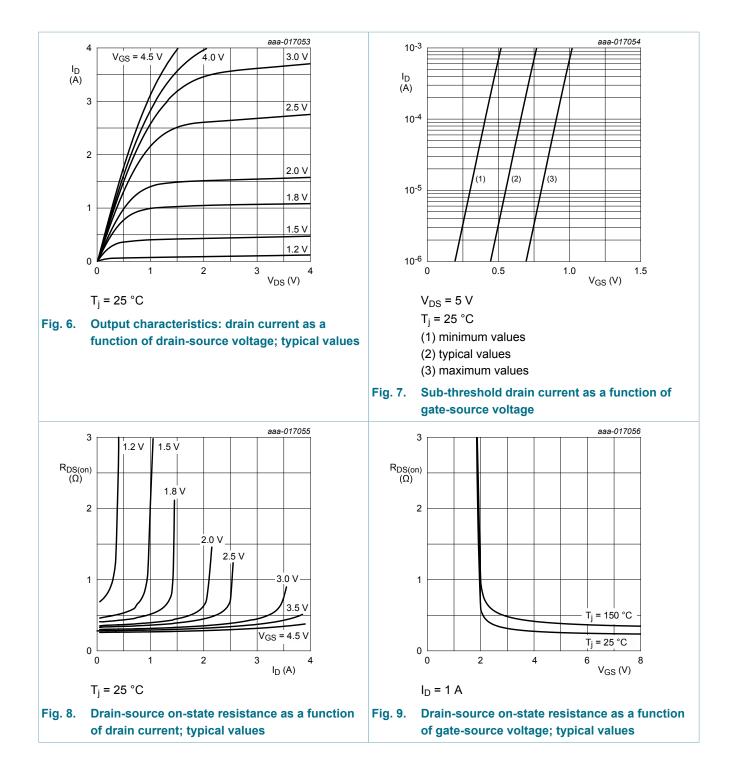


# **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	octeristics	· · · · · ·				
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	20	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	$I_D$ = 250 µA; $V_{DS}$ = $V_{GS}$ ; $T_j$ = 25 °C	0.45	0.7	0.95	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = 20 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	5	μA
		$V_{GS}$ = -8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-5	μA
		$V_{GS}$ = 4.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	1	μA
		$V_{GS}$ = -4.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-1	μA
		V <sub>GS</sub> = 2.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
		$V_{GS}$ = -2.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-100	nA
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 1.2 A; T <sub>j</sub> = 25 °C	-	270	320	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 1.2 A; T <sub>j</sub> = 150 °C	-	400	475	mΩ
		V <sub>GS</sub> = 2.5 V; I <sub>D</sub> = 1.0 A; T <sub>j</sub> = 25 °C	-	360	480	mΩ
		V <sub>GS</sub> = 1.8 V; I <sub>D</sub> = 0.12 A; T <sub>j</sub> = 25 °C	-	470	680	mΩ
		V <sub>GS</sub> = 1.5 V; I <sub>D</sub> = 0.01 A; T <sub>j</sub> = 25 °C	-	600	1190	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; I <sub>D</sub> = 1.23 A; T <sub>j</sub> = 25 °C	-	1.9	-	S
Dynamic ch	aracteristics	· · · · · · · · · · · · · · · · · · ·				
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = 10 V; I <sub>D</sub> = 1.0 A; V <sub>GS</sub> = 4.5 V;	-	0.8	1.4	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.1	-	nC
Q <sub>GD</sub>	gate-drain charge		-	0.2	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS}$ = 10 V; f = 1 MHz; $V_{GS}$ = 0 V;	-	46	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	9.6	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	7.7	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 10 V; I <sub>D</sub> = 1.0 A; V <sub>GS</sub> = 4.5 V;	-	6	-	ns
t <sub>r</sub>	rise time	R <sub>G(ext)</sub> = 6 Ω; T <sub>j</sub> = 25 °C	-	10	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	11	-	ns
t <sub>f</sub>	fall time		-	4	-	ns
Source-drai	n diode		I			
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 0.7 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	0.9	1.2	V

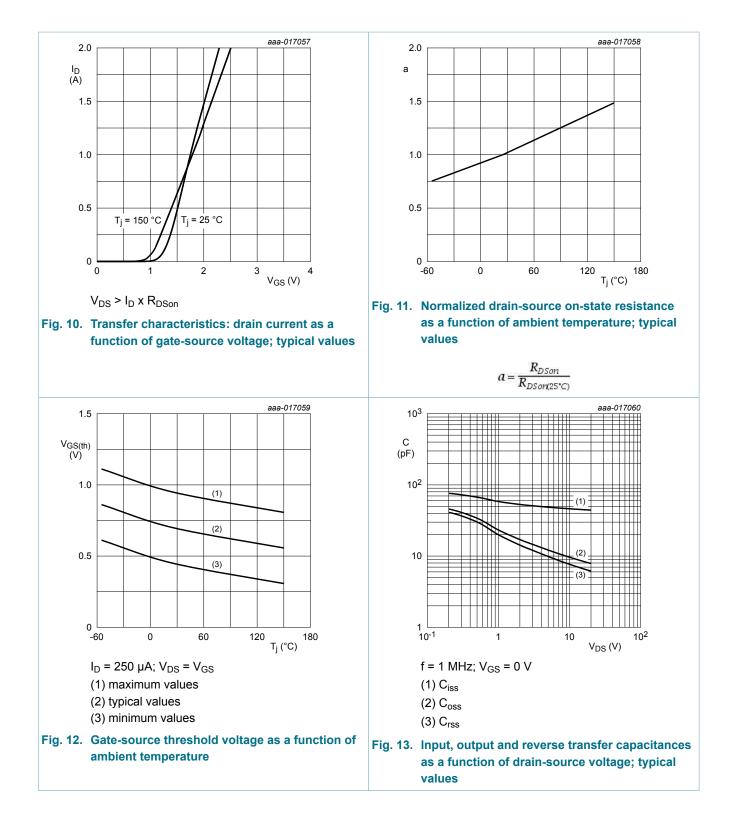
### **PMZ290UNE2**

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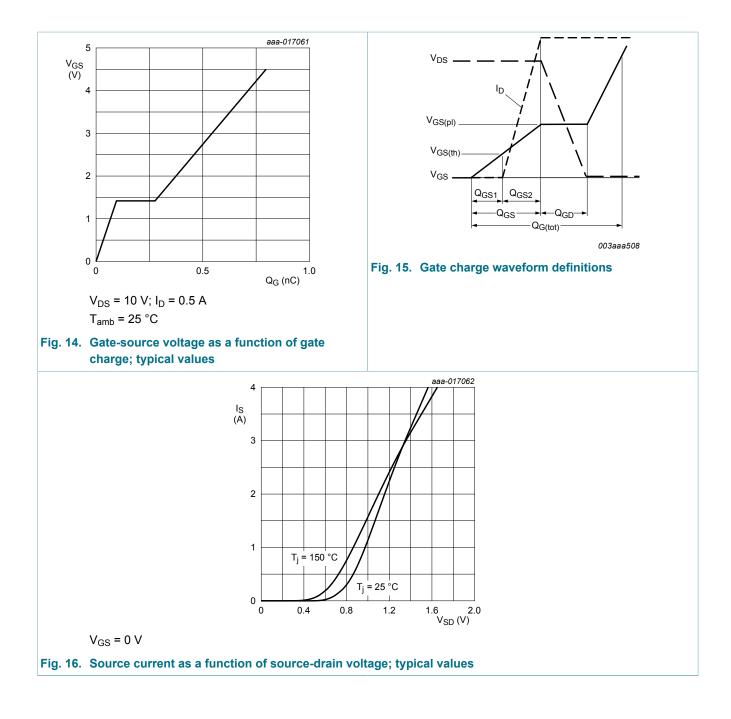
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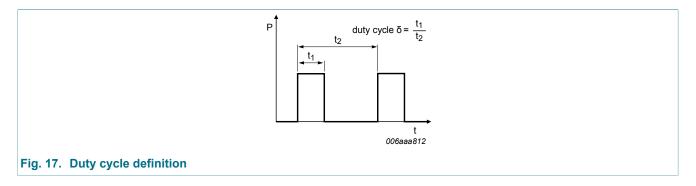
# **PMZ290UNE2**

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#### 20 V, N-channel Trench MOSFET

# **11. Test information**



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## 12. Package outline

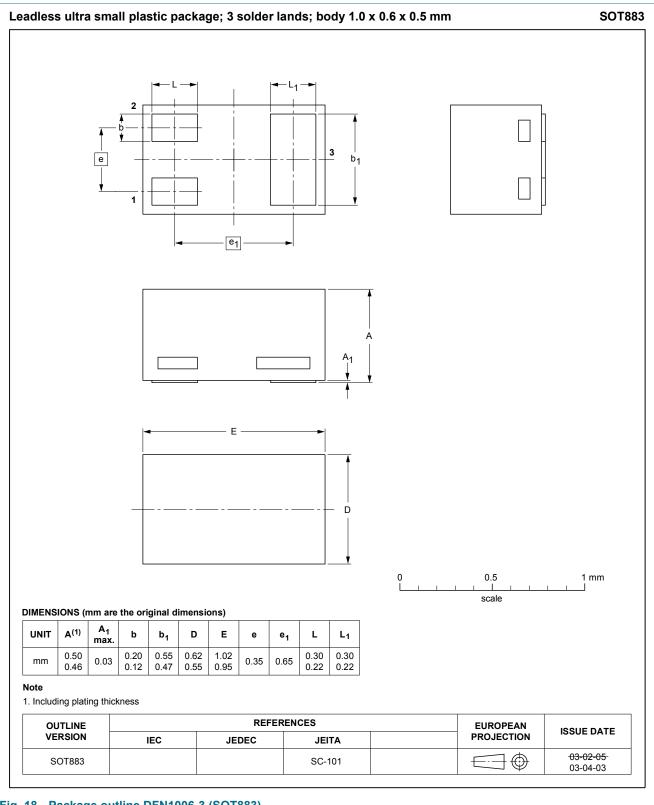


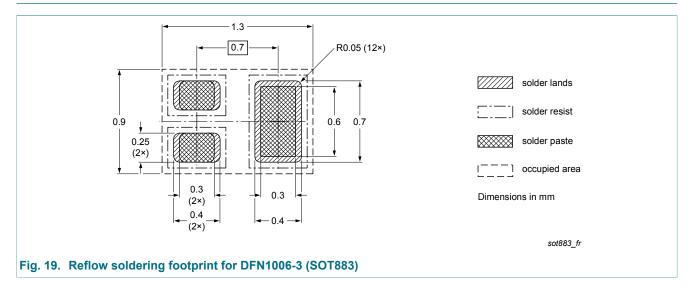
 Fig. 18. Package outline DFN1006-3 (SOT883)

 PMZ290UNE2
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### 13. Soldering



# 14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMZ290UNE2 v.1	20150324	Product data sheet	-	-

#### 20 V, N-channel Trench MOSFET

### 15. Legal information

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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