Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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DATA SHEET

RENESAS

BIPOLAR ANALOG INTEGRATED CIRCUIT μ PC29S00 Series

LOW DROPOUT VOLTAGE REGULATOR WITH ON/OFF FUNCTION

DESCRIPTION

The μ PC29S00 series is a low dropout regulator, which has 100 mA capable for the output current. This series features ON/OFF function to control output voltage.

The μ PC29S00 series is suitable for NEC Electronics' single chip microcomputers, which have on-chip flash memory. The μ PC29S00 series is use of erasing and writing data on its flash memory.

FEATURES

- ON/OFF control function (active high)
- Output current excess of 100 mA
- ★ Surface mount device package: 8-pin plastic SOP (5.72 mm (225)) (7.8 V output, 10 V output)
 - High accuracy output voltage: ±2% (7.8 V output)

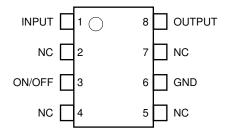
-2 to +1% (10 V output)

• On-chip all kinds of protection circuit

★ ORDERING INFORMATION

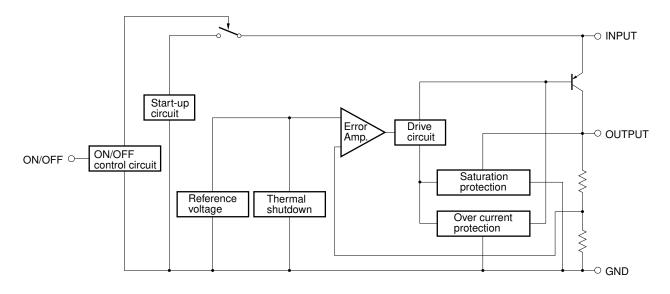
Part Number	Package	Output Voltage
μ PC29S78GR	8-pin plastic SOP (5.72 mm (225))	7.8 V
μ PC29S10GR	8-pin plastic SOP (5.72 mm (225))	10 V

PIN CONFIGURATION (Marking Side)



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BLOCK DIAGRAM



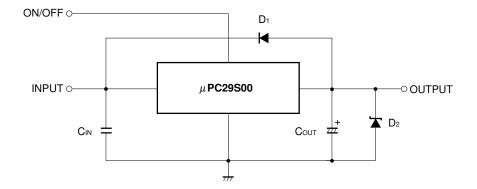
ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Input Voltage	VIN	20	V
Internal Power Dissipation	P⊤ ^{Note}	0.48	W
Operating Ambient Temperature	Та	-30 to +85	°C
Operating Junction Temperature	TJ	-30 to +150	°C
Storage Temperature	Tstg	-55 to +150	°C
Thermal Resistance (Junction to Ambient)	Rth(J-A)	260	°C/W

Note T_A ≤ 25°C, internally limited. When the operating junction temperature rises up to 150°C, the internal circuit shuts down the output voltage.

Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

★ TYPICAL CONNECTION



- C_{IN}: 0.1 to 0.47 μF. Be sure to connect to prevent abnormal oscillation. For using capacitors, film capacitors whose voltage and temperature characteristics are excellent are recommended. Take care that some monolithic ceramic capacitor is inferior in the temperature and voltage characteristics. When using the monolithic ceramic capacitor, the C_{IN} needs to be held these capacities in voltage and temperature used.
- Cout: 10 μ F or higher. Be sure to connect to prevent oscillation and to improve the transient load stabilization. Connect the CIN and Cout as close as possible to the IC pins (within 2 cm).

D1: Need for
$$V_0 > V_{IN}$$

D2: Need a shottky barrier diode for Vo < GND

- Cautions 1. When output is off (VON/OFF = low level), the OUTPUT pin should not be supplied higher voltage than VIN voltage from external.
 - 2. Design your circuit and mounting with consideration for heat radiation when using this device.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Input Voltage	VIN	μPC29S78	8.8		18	V
		μPC29S10	11		18	V
Output Current	lo		0		100	mA
Operating Ambient Temperature	Та		-30		+85	°C
Operating Junction Temperature	TJ		-30		+125	°C

Caution If the Absolute Maximum Rating is not exceeded, there is no problem for using recommended operating range or more. Use and evaluate the μ PC29S00 series since the leeway is decreased with the Absolute Maximum Rating. Moreover, the recommended operating range is not prescribed to use when all parameters are maximum value.

ELECTRICAL CHARACTERISTICS

μ PC29S78 (T_J = 25°C, V_{IN} = 12 V, lo = 50 mA, VON/OFF = 5 V, unless otherwise specified)

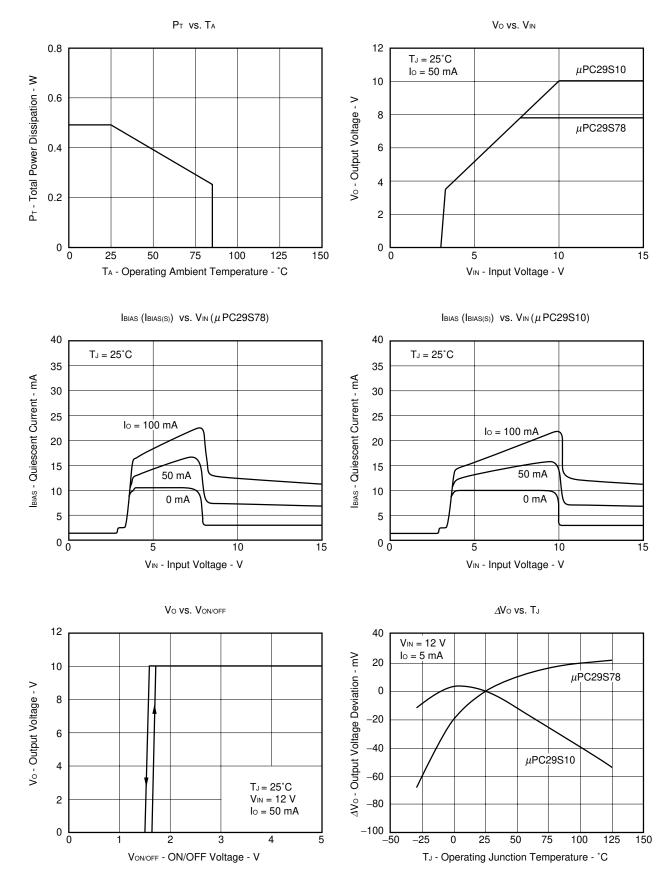
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		7.64	7.8	7.96	V
		$8.8 \text{ V} \leq V_{\text{IN}} \leq 18 \text{ V},$	7.56		8.04	V
		$0 \text{ mA} \leq I_0 \leq 50 \text{ mA},$				
		$0^{\circ}C \leq T_{\text{J}} \leq +125^{\circ}C$				
		$0 \text{ mA} \le I_0 \le 100 \text{ mA},$	7.56		8.04	V
		$0^{\circ}C \leq T_{J} \leq +125^{\circ}C$				
Line Regulation	REGIN	$8.8~V \leq V_{IN} \leq 18~V$		22	75	mV
Load Regulation	REG∟	$0 \text{ mA} \le I_0 \le 100 \text{ mA}$		21	75	mV
Quiescent Current	Ibias	lo = 0 mA		3.0	5.0	mA
		lo = 100 mA		11	25	mA
Startup Quiescent Current	BIAS(S)1	VIN = 7.3 V, Io = 0 mA		10	20	mA
	BIAS(S)2	V _{IN} = 7.3 V, lo = 100 mA			50	mA
Quiescent Current Change	ΔI_BIAS	$8.8 \text{ V} \leq \text{Vin} \leq 18 \text{ V},$			10	mA
		$0^{\circ}C \leq T_{J} \leq +125^{\circ}C$				
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		160		μVr.m.s.
Ripple Rejection	R•R	f = 120 Hz, 8.8 V \leq VIN \leq 13.5 V	42	51		dB
Dropout Voltage	VDIF	$I_0 = 100 \text{ mA}, 0^\circ \text{C} \le T_J \le +125^\circ \text{C}$			1.0	V
Peak Output Current	Opeak	VIN = 9.8 V	150	250	400	mA
Short Circuit Current	Oshort	VIN = 18 V		250		mA
Temperature Coefficient of	ΔVo/ΔT	$I_0 = 5 \text{ mA}, 0^\circ \text{C} \le T_J \le +125^\circ \text{C}$		-0.4		mV/°C
Output Voltage						
ON/OFF Voltage	VON/OFF1	VIN = 12 V, Io = 10 mA		1.8	2.0	V
	VON/OFF2	VIN = 12 V, Io = 0 mA	0.8	1.6		V
ON/OFF Current	ION/OFF1	$V_{ON/OFF} = 2.7 V$, $I_O = 0 mA$		250	450	μA
	ION/OFF2	$V_{ON/OFF} = 5 V, I_O = 0 mA$		450	800	μA
Standby Current	BIASOFF	VON/OFF = 0 V, IO = 0 mA			10	μA

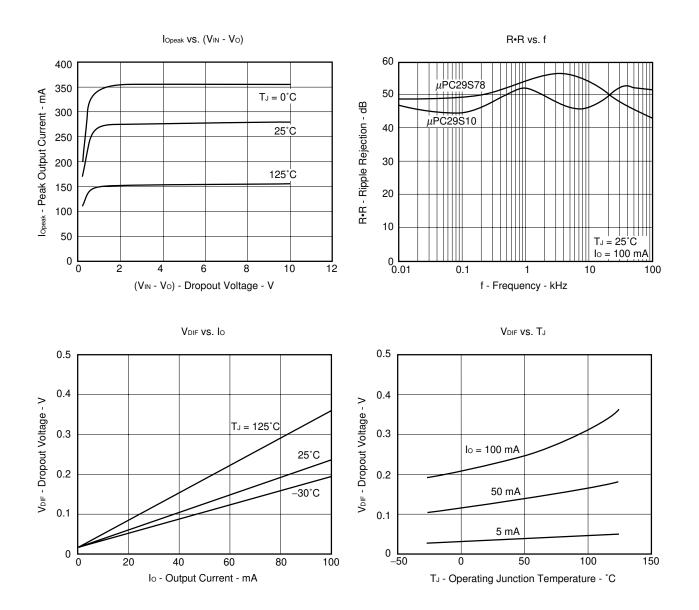
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		9.80	10.00	10.10	V
		$11 \ V \leq V_{IN} \leq 18 \ V,$	9.70		10.20	V
		$0 \text{ mA} \le I_0 \le 50 \text{ mA},$				
		$0^{\circ}C \leq T_{\text{J}} \leq +125^{\circ}C$				
		$0 \text{ mA} \le I_0 \le 100 \text{ mA},$	9.70		10.20	V
		$0^\circ C \leq T_J \leq +125^\circ C$				
Line Regulation	REGIN	$11~V \le V_{\text{IN}} \le 18~V$		27	100	mV
Load Regulation	REG∟	$0 \text{ mA} \le I_0 \le 100 \text{ mA}$		18	100	mV
Quiescent Current	Ibias	lo = 0 mA		3.3	5.0	mA
		lo = 100 mA		12	25	mA
Startup Quiescent Current	BIAS(S)1	VIN = 9.5 V, Io = 0 mA		10	20	mA
	BIAS(S)2	V _{IN} = 9.5 V, Io = 100 mA			50	mA
Quiescent Current Change	ΔI_BIAS	$11 \text{ V} \leq V_{\text{IN}} \leq 18 \text{ V},$		1.0	10	mA
		$0^{\circ}C \leq T_{J} \leq +125^{\circ}C$				
Output Noise Voltage	Vn	10 Hz \leq f \leq 100 kHz		210		μVr.m.s.
Ripple Rejection	R•R	f = 120 Hz, 11 V \leq VIN \leq 13.5 V	40	48		dB
Dropout Voltage	VDIF	$I_0 = 100 \text{ mA}, 0^{\circ}C \le T_J \le +125^{\circ}C$		0.4	1.0	V
Peak Output Current	Opeak	VIN = 12 V	150	250	400	mA
Short Circuit Current	lOshort	VIN = 18 V		180		mA
Temperature Coefficient of	<i>Δ</i> Vo/ <i>Δ</i> T	$I_0 = 5 \text{ mA}, 0^\circ \text{C} \le \text{T}_J \le +125^\circ \text{C}$		-0.5		mV/°C
Output Voltage						
ON/OFF Voltage	VON/OFF1	VIN = 12 V, Io = 10 mA		1.8	2.0	V
	VON/OFF2	VIN = 12 V, Io = 0 mA	0.8	1.6		V
ON/OFF Current	ON/OFF1	$V_{ON/OFF} = 2.7 V$, $I_O = 0 mA$		250	450	μA
	ION/OFF2	$V_{ON/OFF} = 5 V, I_O = 0 mA$		450	800	μA
Standby Current	IBIASOFF	$V_{ON/OFF} = 0 V, I_O = 0 mA$			10	μA

μ PC29S10 (T_J = 25°C, V_{IN} = 12 V, Io = 50 mA, VON/OFF = 5 V, unless otherwise specified)



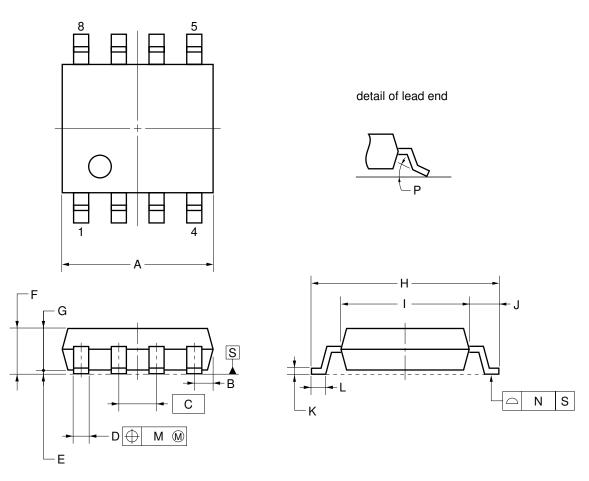
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★ PACKAGE DRAWINGS (Unit: mm)

8-PIN PLASTIC SOP (5.72 mm (225))



NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
А	$5.2 \begin{array}{c} +0.17 \\ -0.20 \end{array}$
В	0.78 MAX.
С	1.27 (T.P.)
D	$0.42\substack{+0.08\\-0.07}$
E	0.1±0.1
F	1.59±0.21
G	1.49
Н	6.5±0.3
I	4.4±0.15
J	1.1±0.2
К	$0.17\substack{+0.08 \\ -0.07}$
L	0.6±0.2
М	0.12
Ν	0.10
Р	$3^{\circ}^{+7^{\circ}}_{-3^{\circ}}$
	S8GM-50-225B-6

RECOMMENDED SOLDERING CONDITIONS

When soldering this product, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, of if the soldering is performed under different conditions, please make sure to consult with our sales offices.

For more details, refer to below our document.

"Semiconductor Device Mounting Technology Manual (C10535E)"

Type of Surface Mount Device

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 235°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 3 times.	IR35-00-3
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 3 times.	VP15-00-3
Wave Soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time, Pre-heating temperature: 120°C or below (Package surface temperature).	WS60-00-1
Partial Heating Method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (Per each side of the device).	-

μ PC29S78GR, μ PC29S10GR: 8-PIN PLASTIC SOP (5.72 mm (225))

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

CAUTION ON USE

When using the μ PC29S00 series at the input voltage which is lower than in the recommended operating condition the big quiescent current flows through device because the transistor of the output paragraph is saturated (Refer to the IBIAS (IBIAS(S)) vs. VIN curves in TYPICAL CHARACTERISTICS). The specification of this characteristics is the start-up quiescent current, IBIAS(S). The μ PC29S00 series has saturation protection circuit, but they sometimes need about 50 mA current. Therefore, the power supply on the input needs the enough current capacity to pass this quiescent current when the device starts up.

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★ REFERENCE DOCUMENTS

Usage of Three-Terminal Regulators (G12702E) Quality Grades on NEC Semiconductor Devices (C11531E) Semiconductor Device Mounting Technology Manual (C10535E) Review of Quality and Reliability Handbook (C12769E) Semiconductor Selection Guide -Products and Packages- (X13769X)

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