

SM7 Varistor Series



Description

The Littelfuse SM7 is a plastic-encapsulated surface mount MOV in a PPS material molded package with tin-plated terminal which is fully qualified in lead free soldering and available for both reflow and wave soldering process. SM7 is constructed with 7mm varistor element disc. Its maximum operating ambient temperature is 85°C with peak pulse current of 1200A.

Features

- Electrical equivalent to leaded types LA/ZA series
- AC Voltage Rating 50 to 510VAC rms
- No De-Rating up to 85°C ambient
- Good solderability
- Available in tape and reel
- Application of AC power meters
- Lead free and RoHS compliant

Agency Approvals

Agency	Agency Approval	Agency File Number
	UL1449	E320116

Additional Information



Datasheet



Resources



Samples

Absolute Maximum Ratings

• For ratings of individual members of a series, see Device Ratings and Specifications chart

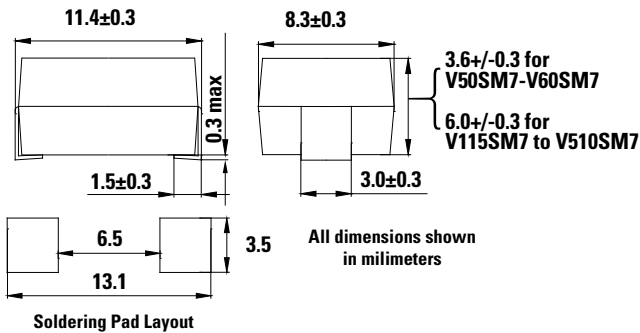
Continuous	SM7 Series	Units
Steady State Applied Voltage:		
AC Voltage Range ($V_{M(ACRMS)}$)	50 to 510	V
DC Voltage Range ($V_{M(DC)}$)	68 to 675	V
Transients:		
Peak Pulse Current (I_{TM})		
For 8/20 μ s Current Wave (See Figure 2)	1200	A
Single Pulse Energy Range		
For 10/1000 μ s Current Wave (W_{TM})	10 to 40	J
Operating Ambient Temperature Range (T_A)	-40 to +85	°C
Storage Temperature Range (T_{STG})	-40 to +125	°C
Temperature Coefficient (α') of Clamping Voltage (V_C) at Specified Test Current	<0.01	%/°C
Hi-Pot Encapsulation (COATING Isolation Voltage Capability) (Dielectric must withstand indicated DC voltage for one minute per MIL-STD 202, Method 301)	2500	V
COATING Insulation Resistance	1000	M Ω

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

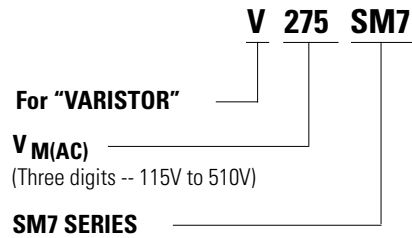
SM7 Series Ratings & Specifications

Part Number	Maximum Rating (85°C)				Specifications (25°C)				
	Continuous		Transient		Varistor Voltage at 1mA DC Test Current		Maximum Clamping Voltage 8 x 20 μs		Typical Capacitance f = 1MHz
	V _{RMS}	V _{DC}	Energy 10 x 1000μs	Peak Current 8 x 20μs					
	V _{M(AC)}	V _{M(DC)}	W _{TM}	I _{TM}	V _{NOM} Min	V _{NOM} Max	V _C	I _{PK}	C
(V)	(V)	(J)	(A)	(V)	(V)	(V)	(A)	(pF)	
V50SM7	50	68	4	1200	73	91	135	10	700
V60SM7	60	81	5	1200	90	110	165	10	600
V115SM7	115	153	10	1200	162	198	300	10	200
V130SM7	130	175	11	1200	184	228	340	10	180
V140SM7	140	180	12	1200	198	242	360	10	160
V150SM7	150	200	13	1200	212	268	395	10	150
V175SM7	175	225	15	1200	247	303	455	10	130
V230SM7	230	300	20	1200	324	396	595	10	100
V250SM7	250	330	21	1200	354	429	650	10	90
V275SM7	275	369	23	1200	389	473	710	10	80
V300SM7	300	405	25	1200	420	517	775	10	70
V320SM7	320	420	25	1200	462	565	850	10	65
V385SM7	385	505	27	1200	558	682	1025	10	60
V420SM7	420	560	30	1200	610	748	1120	10	55
V460SM7	460	615	37	1200	640	790	1190	10	55
V480SM7	480	640	35	1200	670	825	1240	10	50
V510SM7	510	675	40	1200	735	910	1200	10	45

Product Dimensions



Part Numbering System



Lead (Pb) Soldering Recommendations

The principal techniques used for the soldering of components in surface mount technology are IR Re-flow and Wave soldering. Typical profiles are shown on the right.

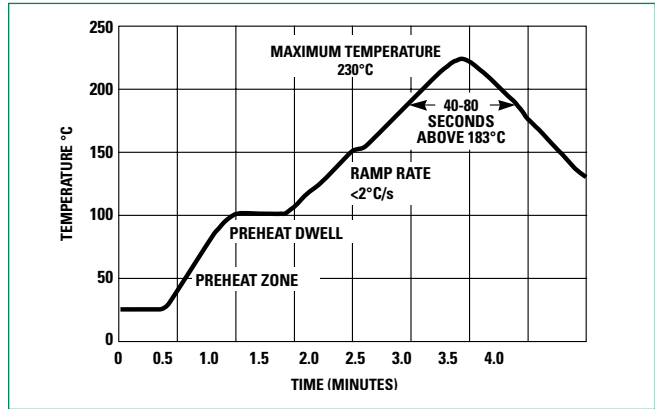
The terminals of SM7 series devices are tin plated copper, and the recommended solder is 62/36/2 (Sn/Pb/Ag), 60/40 (Sn/Pb) or 63/37 (Sn/Pb). Littelfuse also recommends an RMA solder flux.

Wave soldering is the most strenuous of the processes. To avoid the possibility of generating stresses due to thermal shock, a preheat stage in the soldering process is recommended, and the peak temperature of the solder process should be rigidly controlled.

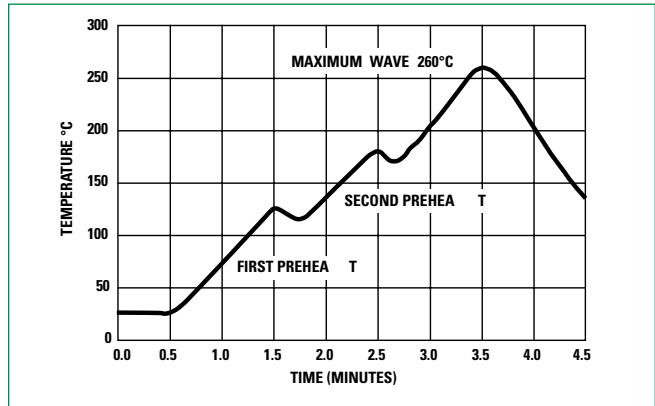
When using a reflow process, care should be taken to ensure that the SM7 chip is not subjected to a thermal gradient steeper than 4 degrees per second; the ideal gradient being 2 degrees per second. During the soldering process, preheating to within 100 degrees of the solder's peak temperature is essential to minimize thermal shock.

Once the soldering process has been completed, it is still necessary to ensure that any further thermal shocks are avoided. One possible cause of thermal shock is hot printed circuit boards being removed from the solder process and subjected to cleaning solvents at room temperature. The boards must be allowed to cool gradually to less than 50°C before cleaning.

Reflow Solder Profile



Wave Solder Profile



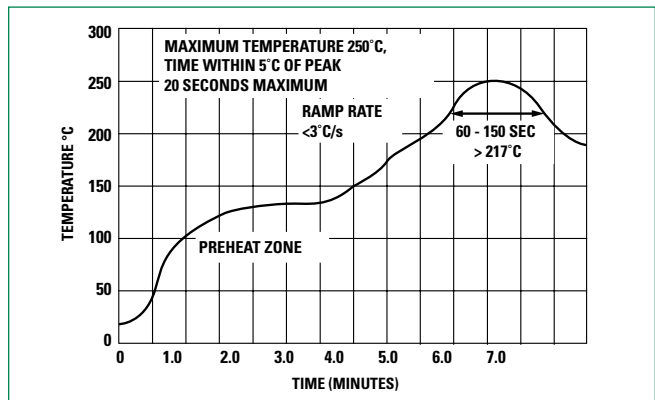
Lead-free (Pb-free) Soldering Recommendations

The terminals of SM7 series devices are tin plated copper, and the recommended Lead-free solder is 96.5/3.0/0.5 (SnAgCu) with an RMA flux, though there is a wide selection of pastes and fluxes available that should be compatible.

The reflow profile must be constrained by the maximums in the Lead-free Reflow Profile. For Lead-free Wave soldering, the Wave Solder Profile still applies.

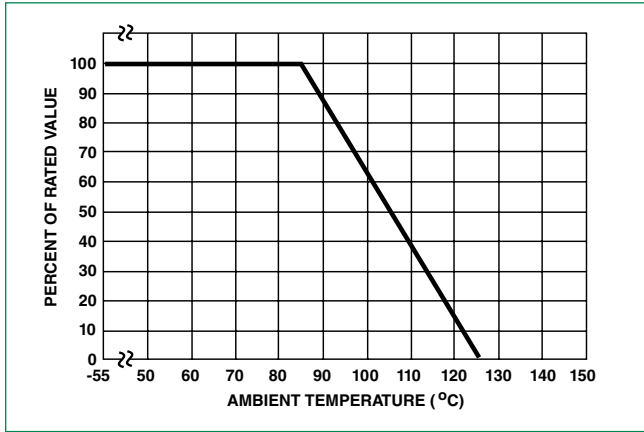
Note: the Lead-free paste, flux and profile were used for evaluation purposes by Littelfuse, based upon industry standards and practices. There are multiple choices of all three available, it is advised that the customer explores the optimum combination for their process as processes vary considerably from site to site.

Lead-free Re-flow Solder Profile

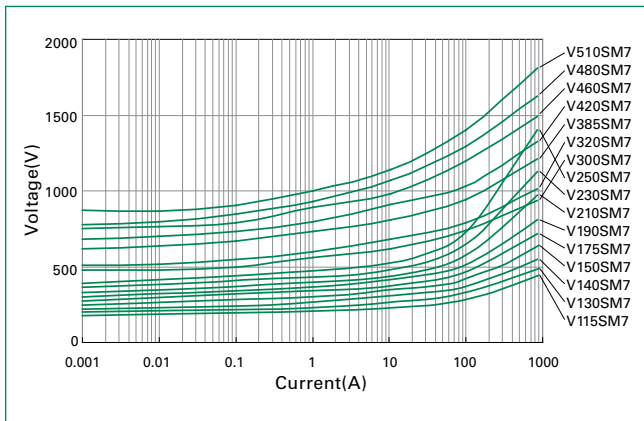


Peak Current, Energy and Power Derating Curve

For applications exceeding 85°C ambient temperature, the peak surge current and energy ratings must be reduced as shown below



V-I Limit Curves

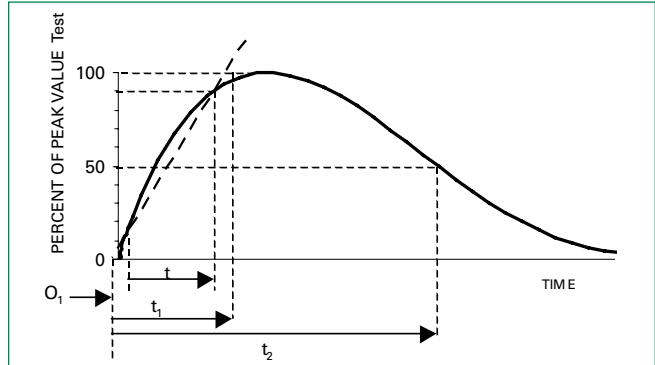


NOTE: If pulse ratings are exceeded, a shift of V_{NDC} (at specified current) of more than $\pm 10\%$ could result. This type of shift, which normally results in a decrease of V_{NDC} , may result in the device not meeting the original published specifications, but it does not prevent the device from continuing to function, and to provide ample protection.

Physical Specifications

Lead Material	Folded Tin Plated Metal Leads
Soldering Characteristics	J-STD-002
Coating	Molded Plastic Meets UL94 V-0 Requirement

Peak Pulse Current Test Waveform for Clamping Voltage

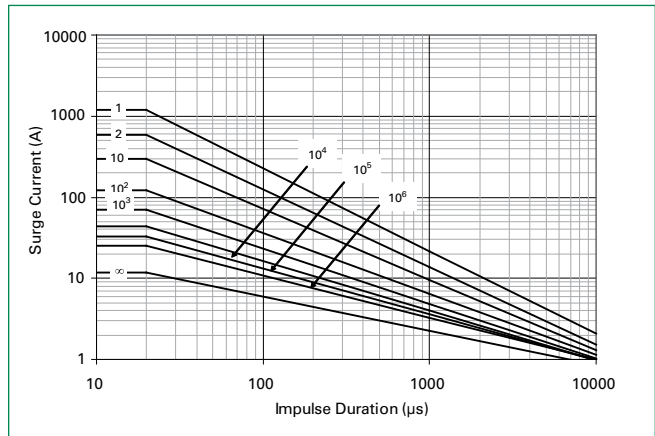


I_1 = Virtual Origin of Wave
 T = Time from 10% to 90% of Peak
 T_1 = Rise Time = $1.25 \times T$
 T_2 = Decay Time

Example - For an $8/20 \mu s$ Current Waveform:

$8 \mu s = T_1$ = Rise Time
 $20 \mu s = T_2$ = Decay Time

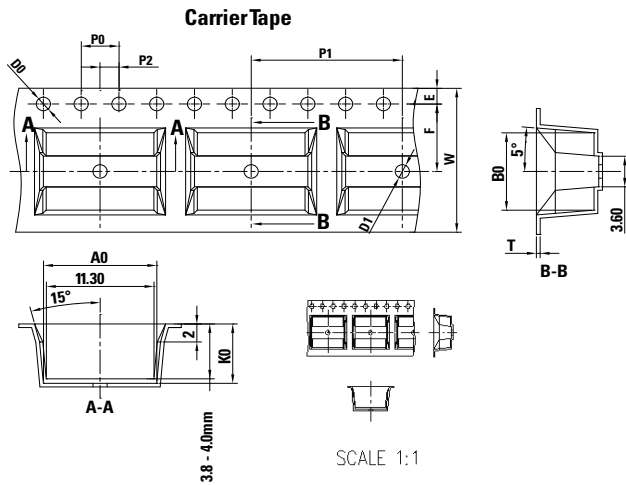
Pulse Rating Curves



Environmental Specifications

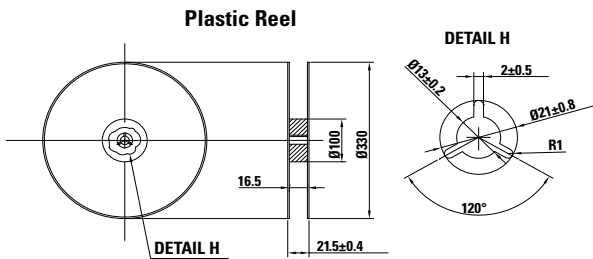
Operating Ambient Temperature Range (T_A)	-40°C to +85°C
Storage Temperature Range (T_{STG})	-40°C to 125°C
Solvent Resistance	MIL STD 202, Method 215
Moisture Sensitivity	Level 1, J-STD-020

Tape & Reel Specifications for V50SM7 and V60SM7



Part	mm
W	16.00±0.30
T	0.40±0.05
A0	11.80±0.10
B0	8.60±0.10
K0	4.80±0.10
E	1.75±0.10
F	7.50±0.10
P0	4.00±0.10
P1	16.00±0.10
P2	2.00±0.10
D0	ø1.50 +0.10/-0
D1	ø1.50 +0.10/-0

- NOTES:**
 1) All dimensions per EIA-481-c
 2) 10 pitches cumulative tolerance on tape ±0.20mm
 3) Quantity per 13 inch (330 mm) reel: 600 pcs



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W	16.00±0.30
T	0.40±0.05
A0	11.80±0.10
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K0	6.80±0.10
E	1.75±0.10
F	7.50±0.10
P0	4.00±0.10
P1	16.00±0.10
P2	2.00±0.10
D0	ø1.50 +0.10/-0
D1	ø1.50 +0.10/-0

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