Nch 600V 11A Power MOSFET

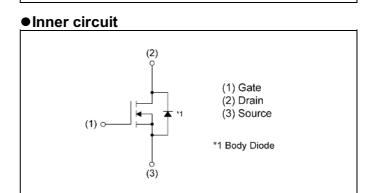
| V_{DSS} | 600V |
|----------------------------|-------|
| R _{DS(on)} (Max.) | 0.39Ω |
| I _D | ±11A |
| P _D | 124W |

-

Outline
TO-252

Features

- 1) Low on-resistance
- 2) Ultra fast switching speed
- 3) Parallel use is easy
- 4) Pb-free plating; RoHS compliant



Application

Switching

Packaging specifications

| Packing | Embossed Tape |
|----------------|---------------|
| Packing code | TL1 |
| Marking | R6011K |
| Quantity (pcs) | 2500 |

ullet Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

| Parameter | Symbol | Value | Unit | |
|--|-------------------|--------------------|-------------|----|
| Drain - Source voltage | V _{DSS} | 600 | V | |
| Continuous drain current (T _c = 25°C) | I _D *1 | ±11 | Α | |
| Pulsed drain current | | I _{DP} *2 | ±33 | Α |
| Cata Sauma valtaga | static | ., | ±20 | V |
| Gate - Source voltage | AC(f>1Hz) | V _{GSS} | ±30 | V |
| Avalanche current, single pulse | · | I _{AS} | 1.8 | Α |
| Avalanche energy, single pulse | | E _{AS} *3 | 210 | mJ |
| Power dissipation (T _c = 25°C) | P _D | 124 | W | |
| Junction temperature | T _j | 150 | °C | |
| Operating junction and storage tempera | ature range | T _{stg} | -55 to +150 | °C |

●Thermal resistance

| Davamatav | Cymah al | Values | | | l lesit |
|--|----------------------|--------|------|------|---------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| Thermal resistance, junction - case | R _{thJC} *4 | - | - | 1.0 | °C/W |
| Thermal resistance, junction - ambient | R _{thJA} *5 | - | - | 147 | °C/W |
| Soldering temperature, wavesoldering for 10s | T _{sold} | - | - | 265 | °C |

●Electrical characteristics (T_a = 25°C)

| Darameter | Cumb al | Conditions | Values | | | Unit |
|---|--|--|--------|------|------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offic |
| Drain - Source breakdown voltage | $V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$ | | 600 | - | - | V |
| | | V _{DS} = 600V, V _{GS} = 0V | | | | |
| Zero gate voltage drain current | I _{DSS} | $T_j = 25^{\circ}C$ | - | - | 100 | μΑ |
| | | T _j = 125°C | - | - | 1000 | |
| Gate - Source leakage current | I _{GSS} | $V_{GS} = \pm 20V, V_{DS} = 0V$ | 1 | 1 | ±100 | nA |
| Gate threshold voltage | V _{GS(th)} | V _{DS} = 10V, I _D = 1mA | 3 | - | 5 | V |
| | | V _{GS} = 10V, I _D = 3.8A | | | | |
| Static drain - source on - state resistance | R _{DS(on)} *6 | $T_j = 25^{\circ}C$ | - | 0.34 | 0.39 | Ω |
| | | $T_j = 125^{\circ}C$ | - | 0.72 | - | |
| Gate resistance | R_{G} | f = 1MHz, open drain | - | 1.5 | - | Ω |

● Electrical characteristics (T_a = 25°C)

| Davamatar | Cymah al | Conditions | Values | | | Unit | |
|--------------------------------|--|---------------------------------------|--------|------|------|-------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offic | |
| Forward Transfer Admittance | $ Y_{fs} ^{*6}$ $V_{DS} = 10V, I_D = 5.5A$ | | 2.9 | 5.8 | - | S | |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 740 | - | | |
| Output capacitance | C _{oss} | V _{DS} = 25V | - | 630 | - | pF | |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | - | 30 | - | | |
| Turn - on delay time | t _{d(on)} *6 | $V_{DD} \simeq 300V$, $V_{GS} = 10V$ | - | 20 | - | | |
| Rise time | t _r *6 | I _D = 5.5A | - | 25 | - | 20 | |
| Turn - off delay time | t _{d(off)} *6 | $R_L \simeq 54.9\Omega$ | - | 40 | - | ns | |
| Fall time | t _f *6 | $R_G = 10\Omega$ | - | 20 | - | | |

● Gate charge characteristics (T_a = 25°C)

| Davanatas | Current el | Conditions | Values | | | 1.1:4 |
|----------------------|------------------------|--|--------|------|------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Total gate charge | Q_g^{*6} | V _{DD} ≈ 300V | - | 22 | - | |
| Gate - Source charge | Q _{gs} *6 | I _D = 11A | - | 6 | - | nC |
| Gate - Drain charge | Q _{gd} *6 | V _{GS} = 10V | - | 10 | - | |
| Gate plateau voltage | V _(plateau) | V _{DD} ≈ 300V, I _D = 11A | - | 6.7 | - | V |

^{*1} Limited only by maximum channel temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \doteqdot 100mH, V_{DD}=50V, R_G=25 Ω , STARTING T_i=25 $^{\circ}$ C

^{*4} T_C=25°C

^{*5} Mounted on a epoxy PCB FR4 (25mm x 27mm x 0.8mm)

^{*6} Pulsed

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Parameter | Cymbol | Conditions | Values | | | Unit | |
|-------------------------------|--------------------|---|--------|------|------|-------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offic | |
| Source current | I _S *1 | | | - | 11 | Α | |
| Pulsed source current | I _{SP} *2 | T _C = 25°C | 1 | - | 33 | Α | |
| Source-Drain voltage | V _{SD} *6 | $V_{GS} = 0V, I_{S} = 11A$ | - | - | 1.5 | V | |
| Reverse recovery time | t _{rr} *6 | | - | 355 | - | ns | |
| Reverse recovery charge | Q _{rr} *6 | I _S = 11Α di/dt = 100Α/μs | - | 3.8 | - | μC | |
| Peak reverse recovery current | I _{rr} *6 | | - | 22 | - | А | |

Fig.1 Power Dissipation Derating Curve

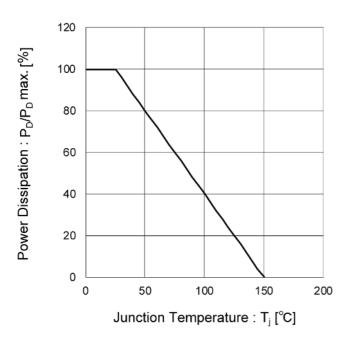


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

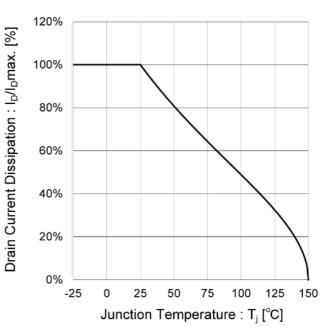


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

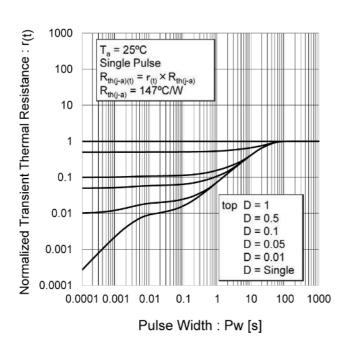
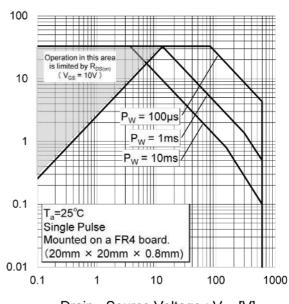


Fig.4 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]

Drain Current : Ip [A]

Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

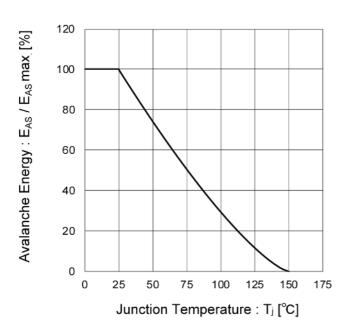


Fig.6 Breakdown Voltage vs.
Junction Temperature

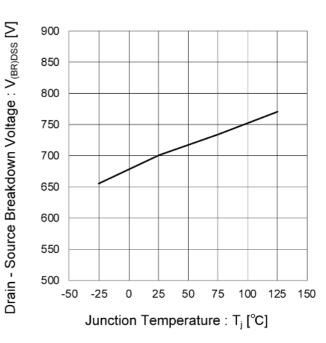
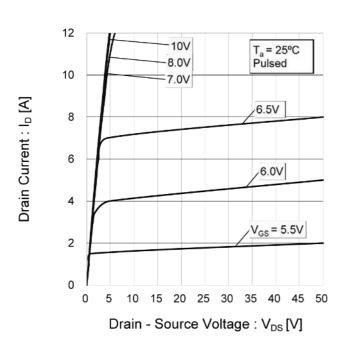
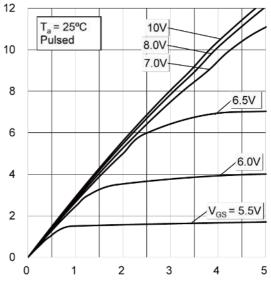


Fig.7 Typical Output Characteristics(I)



12 _____

Fig.8 Typical Output Characteristics(II)



Drain Current: Ip [A]

Fig.9 Typical Transfer Characteristics

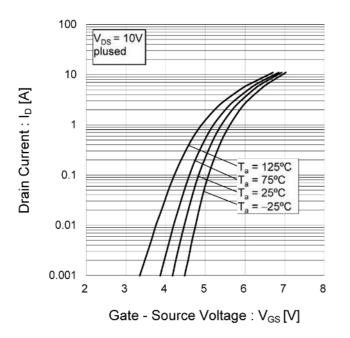


Fig.10 Gate Threshold Voltage vs.
Junction Temperature

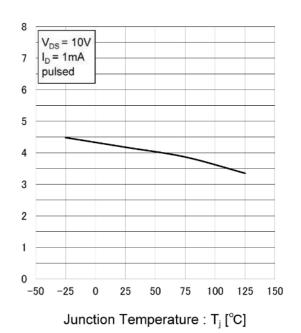
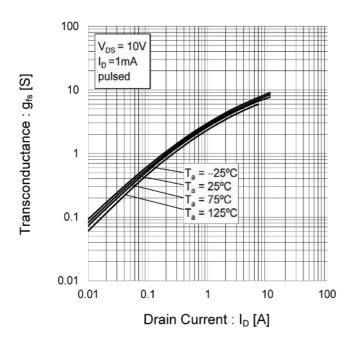


Fig.11 Forward Transfer Admittance vs.

Drain Current



Gate Threshold Voltage: VGS(th) [V]

Fig.12 Static Drain - Source On - State Resistance vs. Drain Current

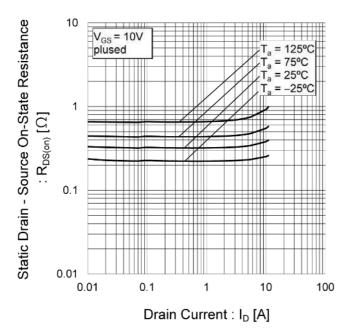


Fig.13 Static Drain - Source On - State Resistance vs. Gate Source Voltage

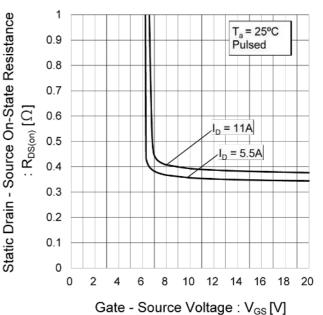
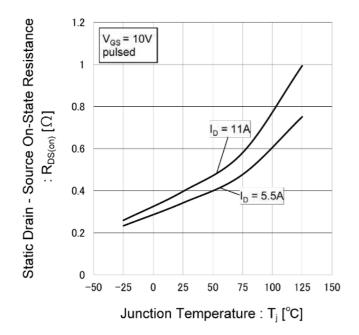


Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



8/12

Fig.15 Typical Capacitance vs.

Drain - Source Voltage

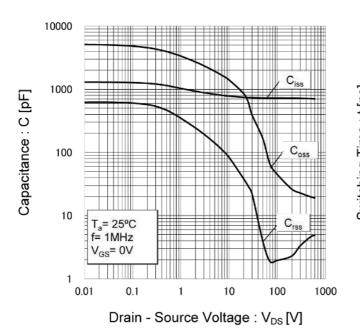


Fig.16 Switching Characteristics

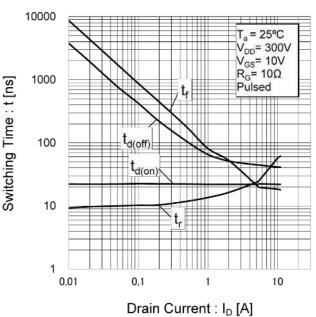
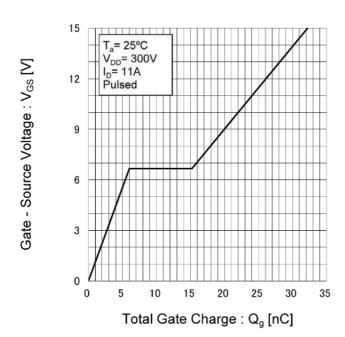


Fig.17 Typical Gate Charge



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Fig.18 Source Current vs. Source - Drain Voltage

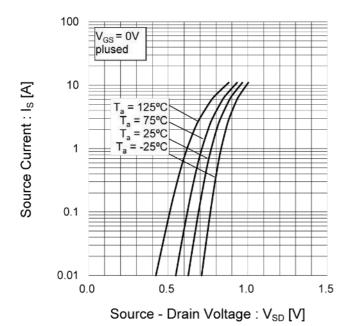
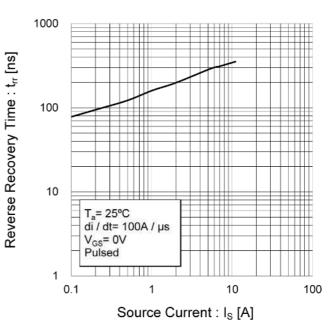


Fig.19 Reverse Recovery Time vs. Source Current



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Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

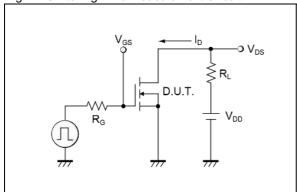


Fig.2-1 Gate Charge Measurement Circuit

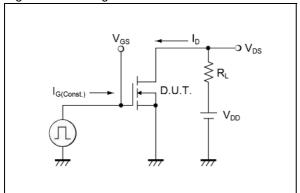


Fig.3-1 Avalanche Measurement Circuit

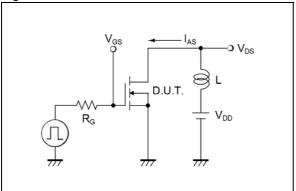


Fig.4-1 trr Measurement Circuit

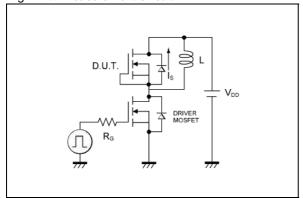


Fig.1-2 Switching Waveforms

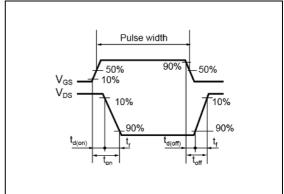


Fig.2-2 Gate Charge Waveform

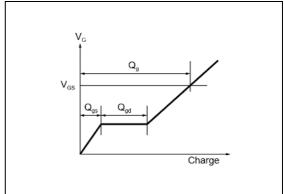


Fig.3-2 Avalanche Waveform

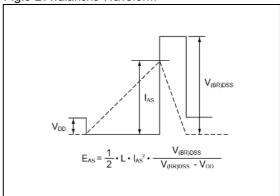
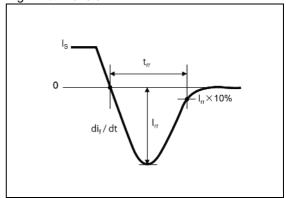
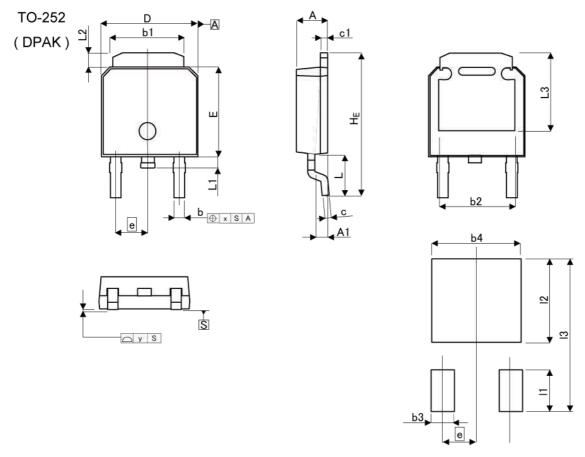


Fig.4-2 trr Waveform



Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

| DIM - | MILIME | ETERS | INC | HES |
|---------|--------|-------|--------------|-------|
| ן ואווט | MIN | MAX | MIN | MAX |
| Α | 2.20 | 2.40 | 0.087 | 0.094 |
| A1 | 0.70 | 1.10 | 0.028 | 0.043 |
| b | 0.60 | 0.90 | 0.024 | 0.035 |
| b1 | 5.20 | 5.50 | 0.205 | 0.217 |
| b2 | 4. | 80 | 0.1 | 89 |
| С | 0.40 | 0.60 | 0.016 | 0.024 |
| c1 | 0.40 | 0.60 | 0.016 | 0.024 |
| D | 6.40 | 6.80 | 0.252 | 0.268 |
| е | 2. | 30 | 0.0 | 91 |
| E | 6.00 | 6.40 | 0.236 | 0.252 |
| HE | 9.40 | 10.40 | 0.370 | 0.409 |
| L | 2. | 90 | 0.114 | |
| L1 | 0.60 | 1.00 | 0.024 | 0.039 |
| L2 | 0.70 | 1.30 | 0.028 | 0.051 |
| L3 | 5. | 30 | 0.2 | 209 |
| х | ₩ | 0.25 | \$ 150 miles | 0.010 |
| у | 76 | 0.10 | 070 | 0.004 |
| 5 | MILIME | ETERS | INC | HES |
| DIM | MIN | MAX | MIN | MAX |
| b3 | 8 | 1.15 | 9 4 6 | 0.045 |
| b4 | | 5.55 | 0.50 | 0.219 |
| 11 | E . | 2.77 | S(#2)) | 0.109 |
| 12 | Wi Wi | 5.50 | 95% | 0.217 |
| 13 | ¥: | 10.40 | 2000 | 0.409 |

Dimension in mm/inches



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| JAPAN | USA | EU | CHINA |
|---------|----------|------------|--------|
| CLASSⅢ | CL ACCTI | CLASS II b | СГУССШ |
| CLASSIV | CLASSII | CLASSⅢ | CLASSⅢ |

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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