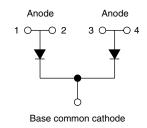
# Vishay High Power Products

**UFL200CB60P** 

## Not Insulated SOT-227 Power Module Ultrafast Rectifier, 200 A



SOT-227



PRODUCT SUMMARY					
V <sub>R</sub>	600 V				
$I_{F(AV)}$ at $T_C$ = 136 °C per module $^{(1)}$	200 A				
t <sub>rr</sub>	111 ns				

Note

(1) All 4 anode terminals connected

### FEATURES

- Not insulated package
- Ultrafast reverse recovery
- Ultrasoft reverse recovery current shape
- Low forward voltage
- Optimized for power conversion: welding and industrial SMPS applications
- · Plug-in compatible with other SOT-227 packages
- · Easy to assemble
- · Direct mounting to heatsink
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

### DESCRIPTION

The UFL200CB60P not insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The planar structure of the diodes, and the platinum doping life time control, provide a ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, dc-to-dc converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V <sub>R</sub>		600	V	
Continuous forward current per diode	I <sub>F</sub> <sup>(1)</sup>	T <sub>C</sub> = 129 °C	142	•	
Single pulse forward current per diode	I <sub>FSM</sub> <sup>(2)</sup>	T <sub>C</sub> = 25 °C	1000	A	
Maximum power dissipation per module	PD	T <sub>C</sub> = 129 °C	484	W	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C	

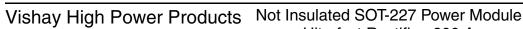
Notes

<sup>(1)</sup> Both anode terminals connected;

Maximum I<sub>RMS</sub> current per leg 200 A to do not exceed the maximum temperature of terminals, see fig. 6

<sup>(2)</sup> 10 ms sine or 6 ms rectangular pulse





# Ultrafast Rectifier, 200 A



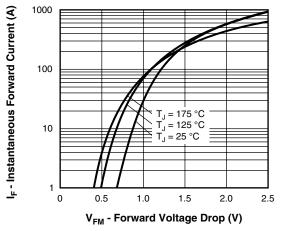
ELECTRICAL SPECIFICATIONS PER DIODE (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	$V_{BR}$	I <sub>R</sub> = 100 μA	600	-	-	
Forward voltage V <sub>FM</sub>		I <sub>F</sub> = 100 A	-	1.21	1.44	
	V	I <sub>F</sub> = 100 A, T <sub>J</sub> = 125 °C	-	1.09	1.24	V
	<b>V</b> FM	I <sub>F</sub> = 200 A	-	1.41	1.66	
	I <sub>F</sub> = 200 A, T <sub>J</sub> = 125 °C	-	1.33	1.55		
Reverse leakage current	I	$V_{R} = V_{R}$ rated	-	-	100	μA
	I <sub>RM</sub>	$T_J = 175 \ ^{\circ}C, \ V_R = V_R \ rated$	-	-	1	mA
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	80	-	pF

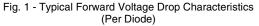
<b>DYNAMIC RECOVERY CHARACTERISTICS PER DIODE</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \ dI_F/dt = 400 \text{ A}/\mu \text{s}, \ V_R = 30 \text{ V}$		-	41	-	
Reverse recovery time	se recovery time t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 50 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	-	111	141	ns
		T <sub>J</sub> = 125 °C		-	215	293	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	11	14	A
		T <sub>J</sub> = 125 °C		-	23	27	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	610	990	nC
		T <sub>J</sub> = 125 °C		-	2470	3955	

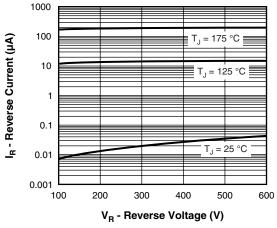
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	Б		-	-	0.19	
Junction to case, both leg conducting	– R <sub>thJC</sub>		-	-	0.095	°C/W
Case to heatsink, per module	R <sub>thCS</sub>	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	Nm

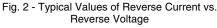


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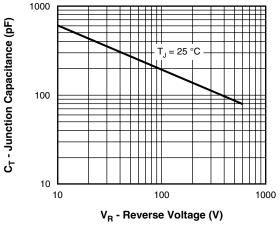


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

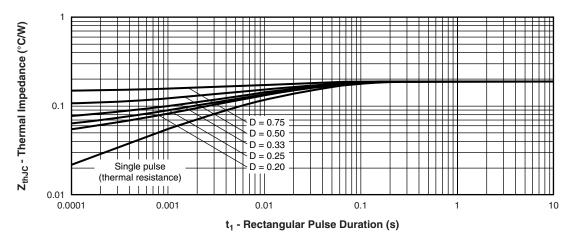
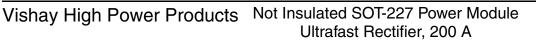


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Diode)



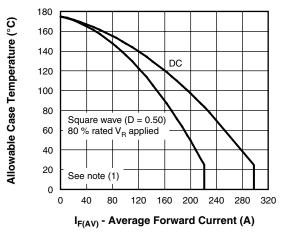


Fig. 5 - Maximum Allowable Case Temperature vs. Avarage Forward Current (Per Leg)

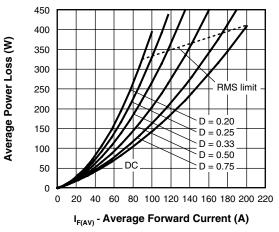


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

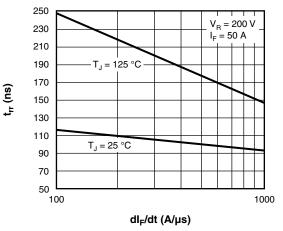


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

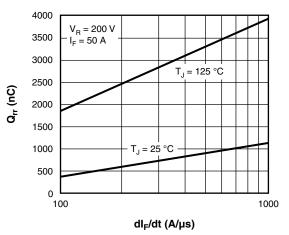
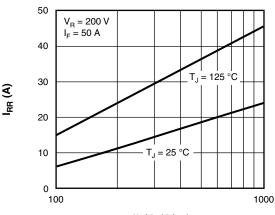


Fig. 8 - Reverse Recovery Charge vs. dl<sub>F</sub>/dt



dl<sub>F</sub>/dt (A/µs)

Fig. 9 - Typical Reverse Recovery Current vs. dl<sub>F</sub>/dt

### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$ 



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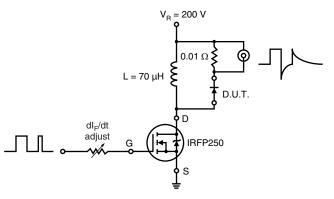


Fig. 10 - Reverse Recovery Parameter Test Circuit

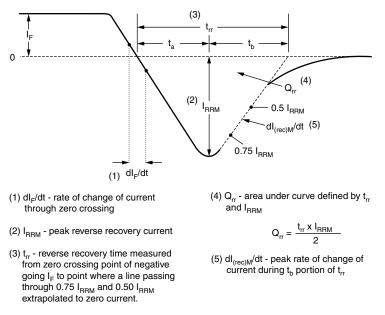
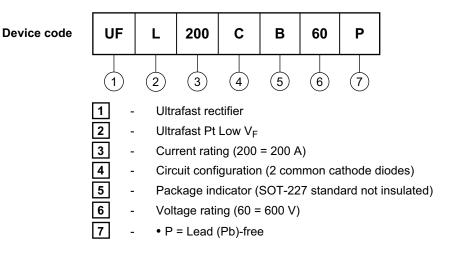


Fig. 11 - Reverse Recovery Waveform and Definitions



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### **ORDERING INFORMATION TABLE**



Quantity per tube is 10 pcs, M4 screw and washer included

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95036				
Packaging information	www.vishay.com/doc?95037			



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