

DRV8702-Q1 EVM User's Guide

1 DRV8702-Q1-EVM

This user's guide is provided with the DRV8702-Q1 customer evaluation module (EVM) as a supplement to the DRV8702-Q1 data sheet. This document details the hardware implementation of the EVM and how to use DRV8702-Q1-EVM GUI application.

1.1 Board Overview

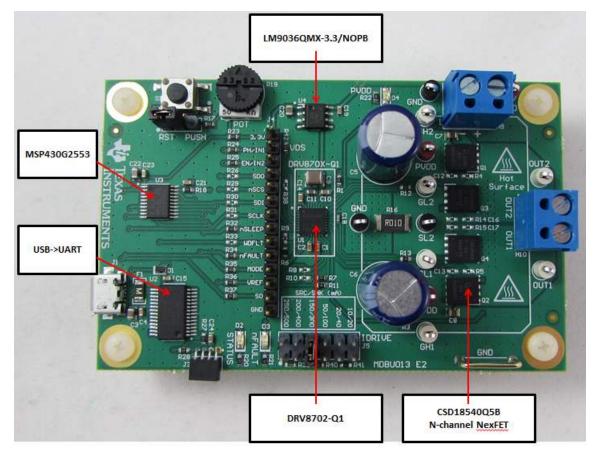
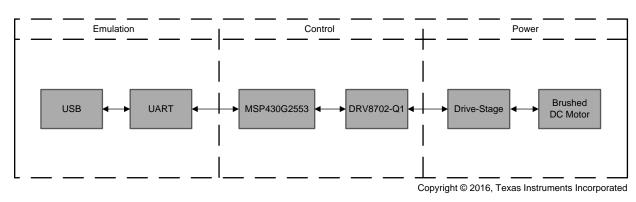


Figure 1. Board Components

The DRV8702-Q1-EVM serves as an evaluation kit to demonstrate TI's DRV8702-Q1 H-bridge gate driver in a 3.5-in × 2.2-in compact form factor. An MSP430G553 device is used to control the speed and direction of the motor, while also monitoring the motor current from the DRV8702-Q1 device. The power stage is created using the DRV8702-Q1 H-bridge gate driver and the CSD18540Q5B N-channel NexFET[™] power MOSFETs. The EVM is a high-performance, power-efficient, and cost-effective platform that speeds development for a quicker time to market.

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1.2 Jumper Settings and Test Points

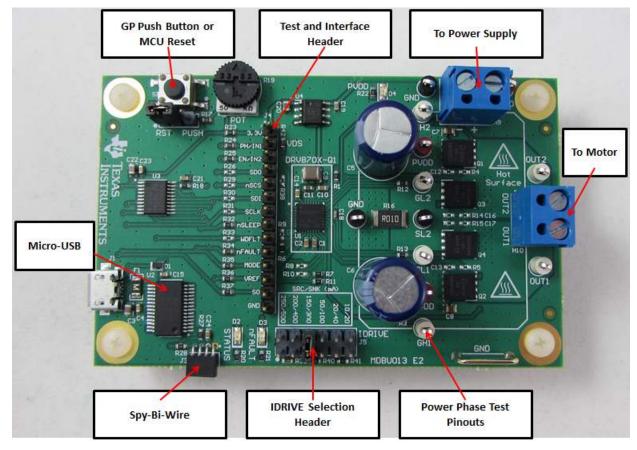


Figure 3. Board Jumpers and Test Points

The jumper settings and test points are as follows:

- micro-USB (J1) Use J1 to interface to a micro-USB cable used to download a program to the MSP430[™] MCU memory and run it.
- **GP Push Button or MCU Reset (J2)** Set J2 to RESET for MCU reset functionality or PUSH for general-purpose input functionality.

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Spy-Bi-Wire (J3) — J3 is for a serialized JTAG protocol used for MSP430 MCUs. J3 can connect an MSP430 Spy-Bi-Wire male header to this female header. Essentially it can be used to program the MSP430 on the EVM instead of using the USB-to-JTAG interfaces.

Test and Interface Header (J4) — J4 can be used to monitor input or output signals from the EVM or supply external control signals.

Power Phase Test Pinouts— These pinouts are to measure the single-H-bridge node voltages.

To Motor — Two-port bulk header to connect the brushed DC motor.

To Power Supply — Two-port power-supply bulk header. This header accepts supplies from 5.9 V to 45 V.

2 GUI Application

Motor Control		
Device Configuration	H Bridge Control	
MODE PH/EN Control	PH Directon	REVER
nSLEEP	ENABLED 0	100
MOTOR ENABLE	ENABLED	•
Status Outputs	Current Regulation Control	
INFAULT	VREF Regulation Voltage	100
SO Sense Cutput	0.1 V	
Motor Current	0.5 A	

Figure 4. DRV8702-Q1-EVM GUI (Motor Control Page)

2.1 Installation

Follow these steps to get started:

Step 1. Install the GUI.

Download and run the installer, *Setup_DRV8702-Q1_DRV8702Q1EVM.exe*, to install the GUI application.

Step 2. Install the FTDI device driver.

If the FTDI USB device driver is not already installed, the driver must be installed manually. The device driver setup can be found in the C:\Program Files (x86)\Texas Instruments\DRV8702-Q1\FTDI_USB_DRIVER folder after successfully completing installation in . Run the *CDM21216_Setup.exe* file in this folder and follow the installation instructions.



2.2 Hardware Setup

The hardware required to run the motor control is a micro-USB cable, the DRV8702-Q1-EVM, and a power supply with a DC output from 5.9 to 45 V. First the brushed DC motor is connected to the motor header on the DRV8702-Q1-EVM. Next the micro-USB is connected to the PC and to the DRV8702-Q1-EVM. Finally the power supply is connected to the DRV8702-Q1-EVM. Verify any faults by testing the nFAULT pin voltage to have a high logical value (pulled logic low if a fault condition exists).

2.3 DRV8702-Q1-EVM GUI

The DRV8702-Q1-EVM_GUI is provided with the DRV8702-Q1-EVM to control a brushed DC motor and manipulate various settings. The GUI provides functionality for adjusting the speed and direction of the motor, adjusting the current-regulation limit, observing the measured drive current, and monitoring the device status.

Use these steps to control the DRV8702-Q1 EVM through the GUI application

- Step 1. Attach the brushed DC motor.
- Step 2. Plug in the micro-USB cable.
- Step 3. Enable your motor power supply (see Section 2.2).
- Step 4. Click on the DRV8702-Q1-EVM shortcut either on the desktop or from the start menu to run the GUI application.

The Serial Port page displays the list of COMs available for opening the connection as shown in Figure 5. If nothing is physically connected to the PC, the COM drop-down list displays -- No Ports --.

COM selection					
СОМЗЗ	• Open	COM Details			
		Field	Value		
		comName	COM33		
		manufacturer	FTDI		
		pnpld	FTDIBUS\VID_040 3+PID_6001+AL01 XWDLA\0000		
		FriendlyName	USB Serial Port (COM33)		

Figure 5. DRV8702-Q1-EVM GUI (List COM Ports)

- Step 5. Select the relevant COM from the drop-down list and click on the Open button.
- Step 6. After the GUI connects, the window in Figure 6 is displayed. Verify that the COM port name (COM port number may differ), and baud rate match what is shown in Figure 6. The bottom left corner of the status bar shows a green indicator to indicate the connection with the opened COM mentioned in the bracket.



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GUI Application

erial Port				
COM selection				
COM33 Close	COM Details			
	Field	Value		
	comName	COM33		
	manufacturer	FTDI		
	pnpld	FTDIBUS\VID_040 3+PID_6001+AL01 XWDLA\0000		
	FriendlyName	USB Serial Port (COM33)		
	baudrate	9600		

Figure 6. DRV8702-Q1-EVM GUI (COM Opened)

- Step 7. Click on the *Menu* icon in the top-left corner of the GUI to open a side-bar menu. Using the side-bar menu, navigate to the following pages or sub-pages at any time:
 - Introduction
 - Device
 - EVM
 - Registers

•

- Motor Control
- Serial Port

2.3.1 Introduction Page

The *Introduction* page has the general information about the DRV8702-Q1 device. The sub-pages, *Device* and *EVM*, under *Introduction* page have the detailed description about the device and EVM respectively as shown in Figure 7.



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GUI Application

/8702-Q1 File To	ske Hølp – v						
Menu							
Introduction Device EVM	n						
Motor Control	5.5V to 45 V						
Serial Port	Controller PH/EN or PWM nSLEEP H-Bridge Gate VREF Grive Shunt Amp Current Reg Protection						
	ingle H-bridge gate driver that uses four external N-channel MOSFETs targeted to drive a bidirectional brushed-DC motor. 4-bridge, or PWM interface allows simple interfacing to controller circuits. An internal sense amplifier provides adjustable current control. The gate driver includes circuitry to regulate the winding current 4-current chopping.						
	h high- and low-side FETs with 10.6-V V _{GS} gate drive. The gate drive current for all external FETs is configurable with a single external resistor.						
	e is provided which shuts down internal circuitry to achieve a very low quiescent current draw.						
	lons are provided: undervoitage lockouts; charge pump faults, overcurrent shutdown, short-circuit protection; gate driver faults, and overtemperature. Fault conditions are indicated on an nFAULT pin.						
	this device, go to:						
	et at a second se						

Figure 7. DRV8702-Q1-EVM GUI (Introduction Page)

2.3.2 Motor Control Page

This page has different widget controls to control the motor as shown in Figure 8.

Motor Control		
Device Configuration	H Bridge Control	
MODE PH/EN Cantrol	+ PH Direction	REVER
nSLEEP ENABLE	D EN Duty Cycle 0	100
MOTOR ENABLE		100
Status Outputs	Current Regulation Control	
nFAULT	VREF Regulation Voltage	3.3
SO Sense Output 0.1 V		0
Motor Current 0.5 A		



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