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- 3-State Buffer-Type Outputs Drive Bus Lines Directly
- Bus-Structured Pinout
- Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK), and Plastic (NT) and Ceramic (JT) DIPs

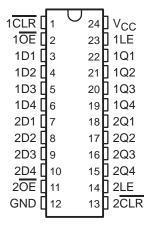
description

These dual 4-bit D-type latches feature 3-state outputs designed specifically for bus driving. This makes these devices particularly suitable for implementing buffer registers, I/O ports, bidirectional bus drivers, and working registers.

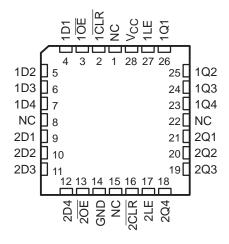
The dual 4-bit latches are transparent D-type latches. While the latch-enable (LE) input is high, the Q outputs follow the data (D) inputs in true form, according to the function table. When LE is low, the outputs are latched. When the clear ($\overline{\text{CLR}}$) input goes low, the Q outputs go low independently of LE. The outputs are in a high-impedance state when the output-enable ($\overline{\text{OE}}$) input is at a high logic level.

The SN54ALS873B and SN54AS873 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS873B and SN74AS873A are characterized for operation from 0°C to 70°C.

SN54ALS873B, SN54AS873 . . . JT PACKAGE SN74ALS873B, SN74AS873A . . . DW OR NT PACKAGE (TOP VIEW)



SN54ALS873B, SN54AS873 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

FUNCTION TABLE (each latch)

	INPU	JTS		OUTPUT
OE	CLR	LE	D	Q
L	L	Х	Χ	L
L	Н	Н	Н	Н
L	Н	Н	L	L
L	Н	L	Χ	Q ₀
Н	Х	Χ	Χ	Z

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logic symbol†

2D3

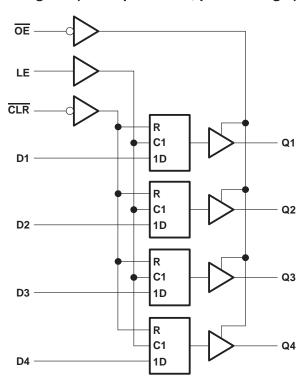
2D4

10

2 10E ΕN 23 1LE C1 1 1CLR С 22 1D1 1D \triangleright ∇ 1Q1 21 1D2 1Q2 5 20 1Q3 1D3 6 19 1D4 1Q4 11 2OE 14 2LE C1 13 2CLR С 18 1D 2D1 ∇ 2Q1 \triangleright 17 8 2D2 2Q2 9 16

Pin numbers shown are for the DW, JT, and NT packages.

logic diagram (each quad latch, positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

2Q3

2Q4

15

Supply voltage, V _{CC}	7 V
Input voltage, V _I	
Voltage applied to a disabled 3-state output	
Operating free-air temperature range, T _A : SN54ALS873B	−55°C to 125°C
SN74ALS873B	0°C to 70°C
Storage temperature range	-65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54ALS873B SN74ALS873B			3B	UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V _{IL}	Low-level input voltage			0.7			0.8	V
lOH	High-level output current			-1			-2.6	mA
loL	Low-level output current			12			24	mA
TA	Operating free-air temperature	-55		125	0		70	°C



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST C	ONDITIONS	SN5	4ALS87	3B	SN7	4ALS87	3B	UNIT
PARAMETER	1231 00	TEST CONDITIONS		TYP [†]	MAX	MIN	TYP†	MAX	UNIT
VIK	V _{CC} = 4.5 V,	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2	!		V _{CC} -2			
Voн	V _{CC} = 4.5 V	$I_{OH} = -1 \text{ mA}$	2.4	3.3					V
	vCC = 4.5 v	$S = 4.5 \text{ V}$ $I_{OH} = -2.6 \text{ mA}$				2.4	3.2		
Val	V00 - 45 V	I _{OL} = 12 mA		0.25	0.4		0.25	0.4	V
VOL	V _{CC} = 4.5 V	I _{OL} = 24 mA					0.35	0.5	
lozh	V _{CC} = 5.5 V,	V _O = 2.7 V			20			20	μΑ
I _{OZL}	V _{CC} = 5.5 V,	V _O = 0.4 V			-20			-20	μΑ
lį	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.1			0.1	mA
l _{IH}	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V			20			20	μΑ
I _{IL}	V _{CC} = 5.5 V,	V _I = 0.4 V			- 0.2			- 0.2	mA
I _O ‡	V _{CC} = 5.5 V,	V _O = 2.25 V	-20		-112	-30		-112	mA
		Outputs high		11	21		11	21	mA
ICC	V _{CC} = 5.5 V	Outputs low		16	29		16	29	
		Outputs disabled		20	31		20	31	

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

timing requirements over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

			SN54AL	SN54ALS873B		SN74ALS873B		
			MIN	MAX	MIN	MAX	UNIT	
	Pulse duration	CLR low	15		15		no	
t _W		LE high	10		10		ns	
t _{su}	Setup time, data before LE \downarrow		10		10		ns	
t _h	Hold time, data after LE↓		7		7		ns	

[‡] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

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switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C _L : R1 : R2 :	C = 4.5 V = 50 pF, = 500 Ω, = 500 Ω, = MIN to			UNIT
			SN54AL	S873B	SN74AL	S873B	
			MIN	MAX	MIN	MAX	
t _{PLH}	D	Q	2	23	2	14	ns
^t PHL		ų ,	2	17	2	14	115
^t PLH	LE	LE Q	8	31	8	22	ns
^t PHL		ų ,	8	26	8	21	110
^t PHL	CLR	Q	6	27	6	20	ns
^t PZH	ŌĒ	Q	4	24	4	18	ns
^t PZL	OE		4	23	4	18	115
^t PHZ	ŌĒ	Q	2	12	2	10	nc
^t PLZ	OE .	Q Q	2	30	2	15	ns

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V _{CC}	7 V
Input voltage, V _I	7 V
Voltage applied to a disabled 3-state output	5.5 V
Operating free-air temperature range, T _A : SN54AS873	-55°C to 125°C
SN74AS873A	0°C to 70°C
Storage temperature range	-65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SI	N54AS87	3 SN74AS873A			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNII
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V _{IL}	Low-level input voltage			0.8			0.8	V
IOH	High-level output current			-12			-15	mA
l _{OL}	Low-level output current			32			48	mA
TA	Operating free-air temperature	-55		125	0		70	°C

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST C	ONDITIONS	SI	N54AS87	73	SN	74AS87	3A	UNIT
PARAMETER	1231 0			TYP†	MAX	MIN	TYP	MAX	UNII
VIK	$V_{CC} = 4.5 V,$	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2	2		V _{CC} -2	2		
Voн	V22 - 45 V	$I_{OH} = -12 \text{ mA}$	2.4	3.2					V
	V _{CC} = 4.5 V	$I_{OH} = -15 \text{ mA}$				2.4	3.3		
Voi	V00 - 45 V	I _{OL} = 32 mA		0.25	0.5				V
VOL	V _{CC} = 4.5 V	I _{OL} = 48 mA					0.35	0.5	
lozh	$V_{CC} = 5.5 V,$	V _O = 2.7 V			50			50	μΑ
lozL	$V_{CC} = 5.5 V,$	$V_0 = 0.4 V$			-50			-50	μΑ
lį	$V_{CC} = 5.5 V$,	V _I = 7 V			0.1			0.1	mA
lΗ	$V_{CC} = 5.5 V$,	V _I = 2.7 V			20			20	μΑ
IĮL	$V_{CC} = 5.5 V,$	V _I = 0.4 V			- 0.5			- 0.5	mA
1 ₀ ‡	$V_{CC} = 5.5 \text{ V},$	V _O = 2.25 V	-30		-112	-30		-112	mA
		Outputs high		68	110		68	110	
l _{CC}		Outputs low		67	109		67	109	mA
		Outputs disabled		80	129		80	129	

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

timing requirements over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

			SN54A	S873	SN74AS	S873A	UNIT
			SN54AS873A MIN MAX MIN MAX 5 5 5 6 5 2	ONIT			
t _W *	Dulgo duration	CLR low			5		
	Pulse duration LE high		6		5		ns
t _{su} *	Setup time, data before LE↓		2		2		ns
th*	Hold time, data after LE↓		4.5		4.5		ns

^{*} On products compliant to MIL-STD-883, Class B, these parameters are based on characterization data but are not production tested.



[‡] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

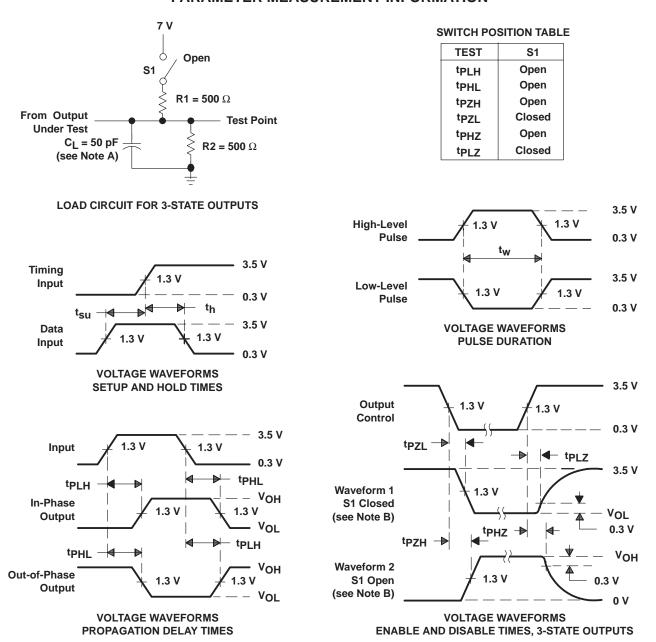
SN54ALS873B, SN54AS873, SN74ALS873B, SN74AS873A DUAL 4-BIT D-TYPE LATCHES WITH 3-STATE OUTPUTS SDAS036C - APRIL 1982 - REVISED SEPTEMBER 1994

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C _L R1 R2	2 500 pl 2 500 = 2 2 500 = 5	2,	V,	UNIT
			SN54A	S873	SN74A	S873A	
			MIN	MAX	MIN	MAX	
t _{PLH}	D	Q	3	9	3	9.5	ns
t _{PHL}		3	3	7	3	7.5	115
t _{PLH}	LE	Q	6	14	6	13	ns
t _{PHL}	LL	y	4	9	4	7.5	115
^t PHL	CLR	Q	3	10	3	9	ns
^t PZH	ŌĒ	Q	2	8	2	6.5	ns
t _{PZL}	OE	7	4	11	4	10.5	115
^t PHZ	ŌĒ	Q	2	8	2	7.5	ns
^t PLZ	OL .	ζ	2	8.5	2	7.5	115

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, $Z_O = 50 \Omega$, $t_f \leq 2 \text{ ns}$, $t_f \leq 2 \text{ ns}$.
- D. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

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