August 2001

**IGBT** 

# SGR20N40L / SGU20N40L

# **General Description**

Insulated Gate Bipolar Transistors (IGBTs) with a trench gate structure provide superior conduction and switching performance in comparison with transistors having a planar gate structure. They also have wide noise immunity. These devices are very suitable for strobe applications

## **Features**

- · High input impedance
- High peak current capability (150A)
- · Easy gate drive
- Surface Mount : SGR20N40LStraight Lead : SGU20N40L

## **Application**

Strobe flash.







# Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Description	SGR / SGU20N40L	Units
V <sub>CES</sub>	Collector - Emitter Voltage	400	V
V <sub>GES</sub>	Gate - Emitter Voltage	± 6	V
I <sub>CM (1)</sub>	Pulsed Collector Current	150	Α
P <sub>C</sub>	Maximum Power Dissipation @ T <sub>C</sub> = 25	5°C 45	W
T <sub>J</sub>	Operating Junction Temperature	-40 to +150	°C
T <sub>stg</sub>	Storage Temperature Range	-40 to +150	°C
T <sub>L</sub>	Maximum Lead Temp. for soldering purposes, 1/8" from case for 5 seconds	300	°C

#### Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

## **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3.0	°C/W
R <sub>θJA</sub> (D-PAK)	Thermal Resistance, Junction-to-Ambient (PCB Mount) (2)		50	°C/W
R <sub>θJA</sub> (I-PAK)	Thermal Resistance, Junction-to-Ambient		110	°C/W

#### Notes :

(2) Mounted on 1" square PCB (FR4 or G-10 Material)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V$ , $I_C = 1mA$	450			V
I <sub>CES</sub>	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			10	μΑ
I <sub>GES</sub>	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 0.1	μΑ
	racteristics	1			1	1
$V_{GE(th)}$	G-E Threshold Voltage	$I_C = 1 \text{ mA},  V_{CE} = V_{GE}$	0.5	1.0	1.4	V
V <sub>CE(sat)</sub>	C-E Saturation Current	$I_C = 150A, V_{GE} = 4.5V$	2.0	4.5	8.0	V
<b>Dynami</b> C <sub>ies</sub>	c Characteristics Input Capacitance	I		3800		рF
C	Output Capacitance	$V_{GE} = 0V, V_{CE} = 30V,$		50		рF
	' '	f = 1MHz		35		ρF
C <sub>oes</sub>	Reverse Transfer Capacitance					
C <sub>res</sub>	Reverse Transfer Capacitance  ng Characteristics					
<sub>Cres</sub> Switchii	·	V 200V L 150A		0.2		μs
C <sub>res</sub> Switchii t <sub>d(on)</sub>	ng Characteristics	V <sub>CC</sub> = 300V, I <sub>C</sub> = 150A,				μs μs
C <sub>res</sub>	ng Characteristics Turn-On Delay Time	$V_{CC} = 300V, I_{C} = 150A,$ $V_{GE} = 4.5V, R_{G} = 15\Omega^{*}$ Resistive Load		0.2	  0.5	•

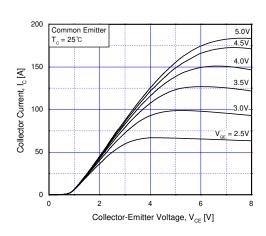


Fig 1. Typical Output Characteristics

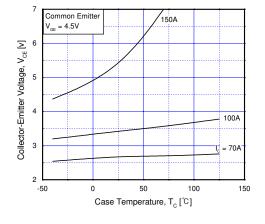


Fig 2. Saturation Voltage vs. Case
Temperature at Variant Current Level

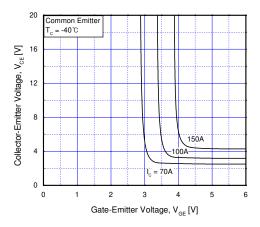


Fig 3. Saturation Voltage vs. V<sub>GE</sub>

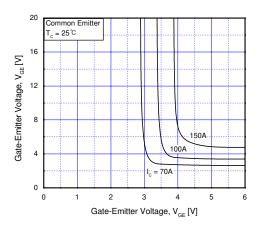


Fig 4. Saturation Voltage vs.  $V_{GE}$ 

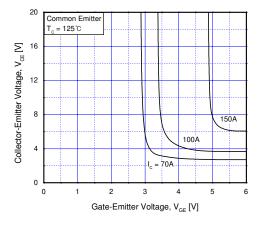


Fig 5. Saturation Voltage vs.  $V_{\text{GE}}$ 

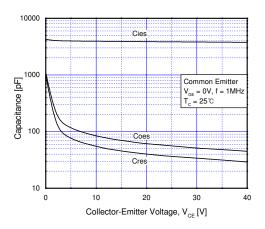
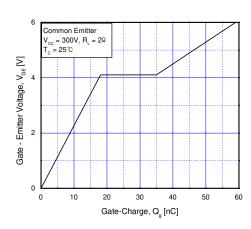


Fig 6. Capacitance Characteristics

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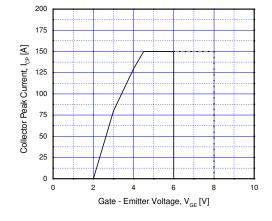


Fig 7. Turn-On Characteristics vs.
Gate Resistance

Fig 8. Collector Current Limit vs.
Gate - Emitter Voltage Limit

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Datasheet Identification	Product Status	Definition
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Product	<b>Product status</b>	Pricing*	Inventory check & ordering	Package type	Leads	Packing method
SGR20N40LTF	Full Production	\$2.10	Purchase	TO-252(DPAK)	2	TAPE REEL
SGR20N40LTM	Full Production	\$2.10	Purchase	TO-252(DPAK)	2	TAPE REEL

<sup>\*</sup> Fairchild 1,000 piece Budgetary Pricing

# back to top

## Models

Package & leads	Condition	Software version	Revision date
PSPICE			
TO-252(DPAK)-2	Electrical	9.2	May 23, 2001

# back to top

Application notes

<u>AN-9006: AN-9006 IGBT Application Note for Camera Strobe</u> (146 K) Sep 27, 2002

back to top

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