FAIRCHILD

SEMICONDUCTOR®

SGP6N60UFD

Ultra-Fast IGBT

General Description

Fairchild's UFD series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UFD series is designed for applications such as motor control and general inverters where high speed switching is a required feature.

Features

- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.1 \text{ V} @ I_C = 3 \text{ A}$
- High input impedance
- CO-PAK, IGBT with FRD : t_{rr} = 35ns (typ.)

Applications

AC & DC motor controls, general purpose inverters, robotics, and servo controls.



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Description		SGP6N60UFD	Units	
V _{CES}	Collector-Emitter Voltage		600	V	
V _{GES}	Gate-Emitter Voltage		± 20	V	
	Collector Current	@ T _C = 25°C	6	A	
I _C	Collector Current	@ T _C = 100°C	3	A	
I _{CM (1)}	Pulsed Collector Current		25	A	
I _F	Diode Continuous Forward Current	@ T _C = 100°C	4	A	
I _{FM}	Diode Maximum Forward Current		25	A	
P _D	Maximum Power Dissipation	@ T _C = 25°C	30	W	
	Maximum Power Dissipation	@ T _C = 100°C	12	W	
Т _Ј	Operating Junction Temperature		-55 to +150	°C	
T _{stg}	Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Secon	ds	300	°C	

Notes: (1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
R _{0JC} (IGBT)	Thermal Resistance, Junction-to-Case		4.0	°C/W
R _{0JC} (DIODE)	Thermal Resistance, Junction-to-Case		7.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		62.5	°C/W

SGP6N60UFD

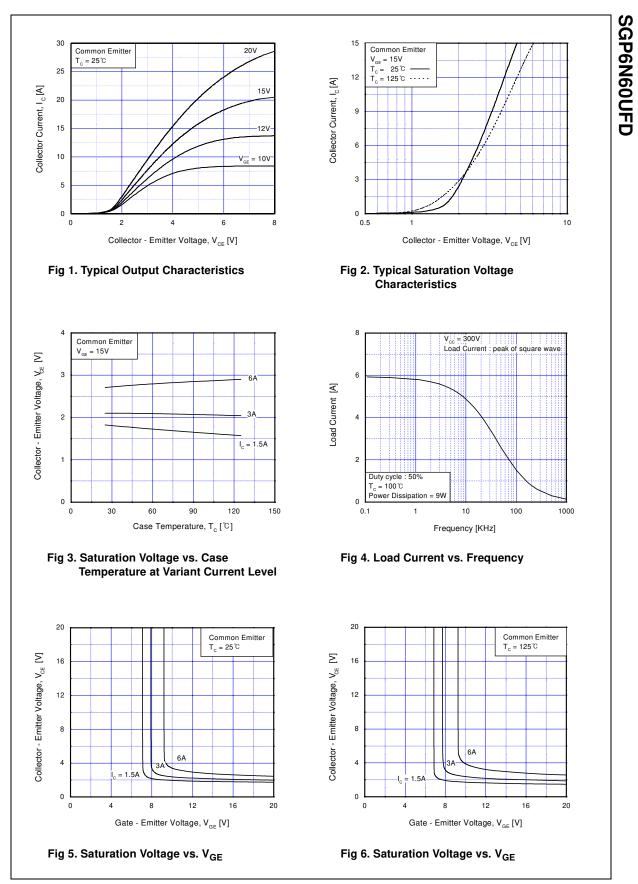
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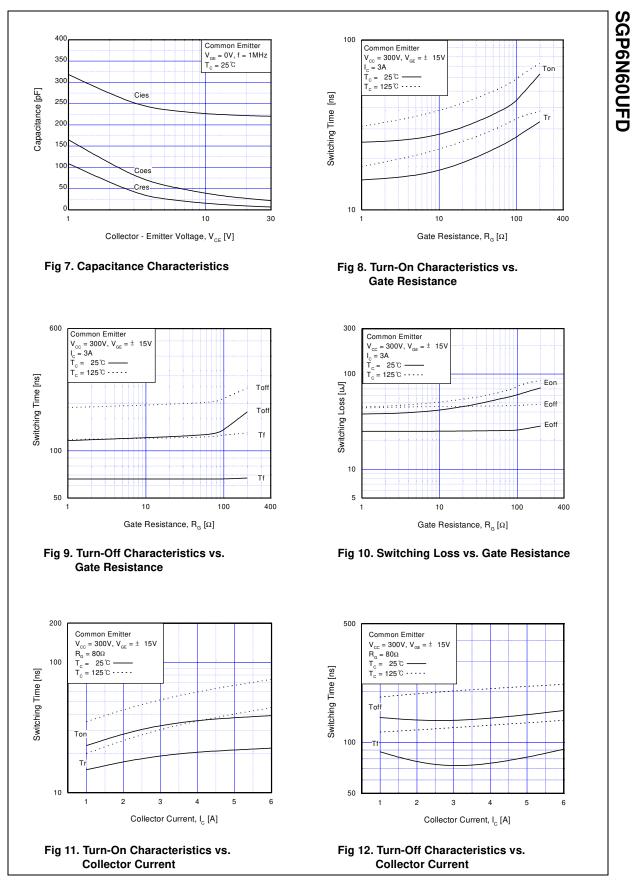
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	600			V
∆B _{VCES} / ∆T _J	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$		0.6		V/°C
CES	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			250	uA
GES	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 100	nA
On Cha	racteristics					
V _{GE(th)}	G-E Threshold Voltage	$I_{C} = 3mA, V_{CE} = V_{GE}$	3.5	4.5	6.5	V
	Collector to Emitter	I _C = 3A, V _{GE} = 15V		2.1	2.6	V
V _{CE(sat)}	Saturation Voltage	$I_{\rm C} = 6A, V_{\rm GE} = 15V$		2.6		V
Sies	c Characteristics Input Capacitance Output Capacitance	V _{CE} = 30V, V _{GE} = 0V,		220 22		pF pF
o _{ies}		$V_{CF} = 30V V_{GF} = 0V,$		-		
C _{oes} C _{res}	Reverse Transfer Capacitance	f = 1MHz		7		pr pF
d(on)	Turn-On Delay Time			15		ns
r	Rise Time	_		25		ns
d(off)	Turn-Off Delay Time	$V_{CC} = 300 \text{ V}, \text{ I}_{C} = 3\text{A},$		60	130	ns
ł	Fall Time	$R_G = 80\Omega$, $V_{GE} = 15V$,		70	150	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 25°C		57		uJ
E _{off}	Turn-Off Switching Loss	4		25		uJ
				82	120	uJ
E _{ts}	Total Switching Loss			20		ns
E _{ts}	Turn-On Delay Time			22		
E _{ts} d(on)	Turn-On Delay Time Rise Time			32		ns
E _{ts} t _{d(on)} t _r t _{d(off)}	Turn-On Delay Time Rise Time Turn-Off Delay Time	V _{CC} = 300 V, I _C = 3A,		32 80	200	ns
E _{ts} td(on) tr d(off) tf	Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time	R _G = 80Ω, V _{GE} = 15V,		32 80 122	200 300	ns ns
E _{ts} ^t d(on) tr ^t d(off) tf E _{on}	Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss			32 80 122 65	200 300 	ns ns uJ
E _{ts} t _{d(on)} t _r t _{d(off)} t _f E _{on} E _{off}	Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss	R _G = 80Ω, V _{GE} = 15V,		32 80 122 65 46	200 300 	ns ns uJ uJ
E <u>ts</u> kd(on) kr kd(off) kf E _{on} E _{off} E _{ts}	Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss	R _G = 80Ω, V _{GE} = 15V,	 	32 80 122 65 46 111	200 300 170	ns ns uJ uJ uJ
E _{ts} td(on) tr dd(off) tf Eon Eoff E _{ts} Q _g	Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Total Switching Loss Total Gate Charge	$R_G = 80\Omega$, $V_{GE} = 15V$, Inductive Load, $T_C = 125^{\circ}C$	 	32 80 122 65 46 111 15	200 300 170 22	ns ns uJ uJ uJ nC
E _{ts} t _{d(on)} t _r t _d (off) E _{on} E _{ts} Q _g Q _{ge}	Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Total Switching Loss Total Gate Charge Gate-Emitter Charge	$R_{G} = 80\Omega, V_{GE} = 15V,$ Inductive Load, $T_{C} = 125^{\circ}C$ $V_{CE} = 300 \text{ V}, I_{C} = 3A,$	 	32 80 122 65 46 111 15 5	200 300 170 22 8	ns ns uJ uJ uJ nC nC
bit Ets td(on) tr td(off) Eon Eoff Ets Qg Qge Qgc	Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Total Switching Loss Total Gate Charge	$R_G = 80\Omega$, $V_{GE} = 15V$, Inductive Load, $T_C = 125^{\circ}C$	 	32 80 122 65 46 111 15	200 300 170 22	ns ns uJ uJ uJ nC

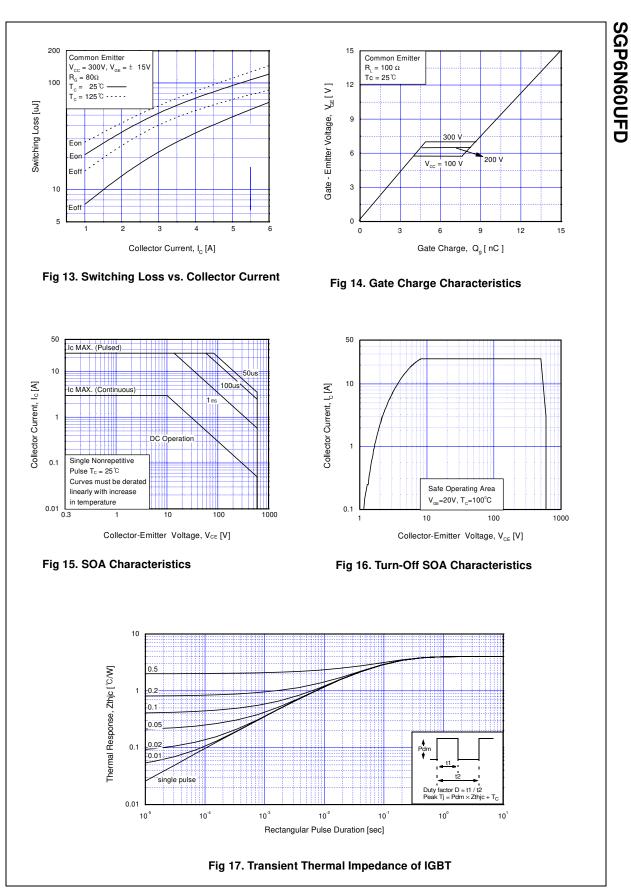
Electrical Characteristics of DIODE $T_{C} = 25^{\circ}C$ unless otherwise noted

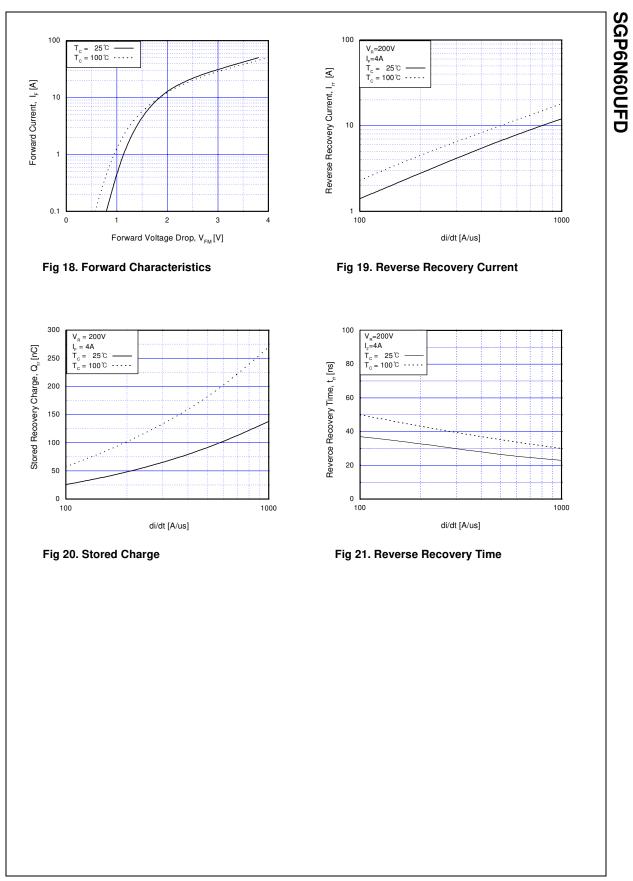
Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Units
V	Diada Farward Valtaga	1 40	$T_{C} = 25^{\circ}C$		1.4	1.7	v
V _{FM} Die	Diode Forward Voltage	I _F = 4A	$T_{C} = 100^{\circ}C$		1.3		v
•	Diada Bayaraa Baaayany Tima		$T_{C} = 25^{\circ}C$		35	52	20
t _{rr} Diode Reverse Recovery Tim	Didde neverse necovery Time		T _C = 100°C		53		ns
1	Diode Peak Reverse Recovery	I _F = 4A,	$T_{C} = 25^{\circ}C$		3.5	5.0	Α
Irr	r Current	di/dt = 200A/us	$T_{C} = 100^{\circ}C$		4.5		A
Q _{rr} Diode Reve	Diada Davarra Daaavarra Charra		$T_{C} = 25^{\circ}C$		60	135	nC
	Diode Reverse Recovery Charge		T _C = 100°C		120		nc

SGP6N60UFD









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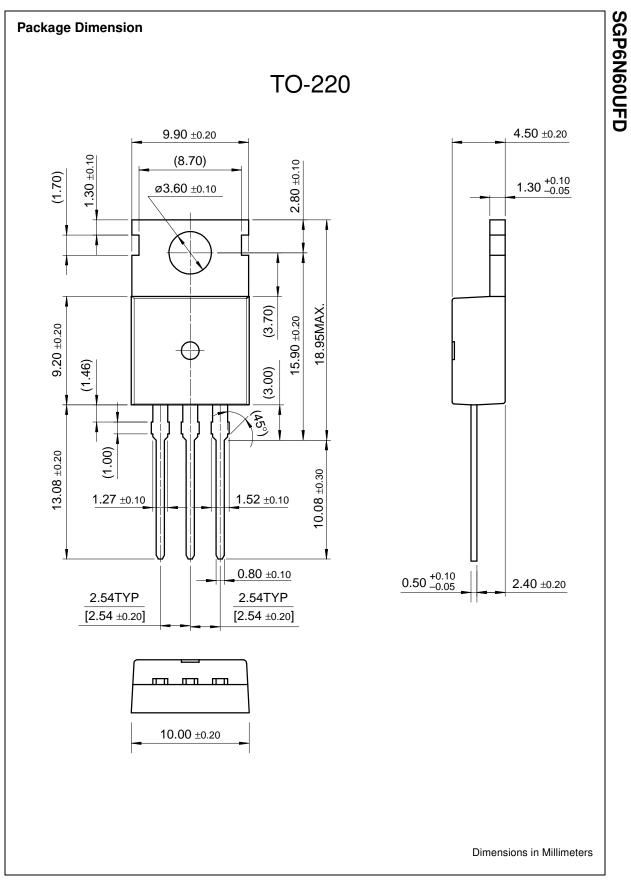
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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
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Microcontrollers Non-Volatile Memory Optoelectronics Markets and applications New products Product selection and parametric search Cross-reference	General description Fairchild's UFD series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UFD series is designed for applications such as motor control and general inverters where high speed switching is a required feature.	PDF e-mail this datasheet [E- This page <u>Print version</u>	Image: Construction of the construc
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technical support my Fairchild	 Features High Speed Switching Low Saturation Voltage : V _{CE(sat)} = 2.1 	-	
company	 V @ I_C = 3A High Input Impedance CO-PAK, IGBT with FRD : t_{rr} = 35ns 		

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(typ.)

Applications

AC &DC Motor controls,General Purpose Inverters, Robotics, Servo Controls

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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
SGP6N60UFDTU	Full Production	\$0.95	TO-220	3	RAIL

* 1,000 piece Budgetary Pricing

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