



80V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	RDS(ON)	Package	I _D T _C = +25°C	
80V	$7.5 \text{m}\Omega \text{ @V}_{GS} = 10 \text{V}$	TO220AB	111A	

Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

Applications

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

Features

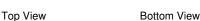
- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

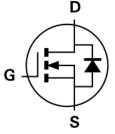
Mechanical Data

- Case: TO220AB
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram Below
- Weight: TO220AB 1.85 grams (Approximate)

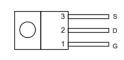








Equivalent Circuit



Top View Pin Out Configuration

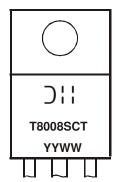
Ordering Information (Note 4)

<u> </u>		
Part Number	Case	Packaging
DMT8008SCT	TO220AB	50 Pieces/Tube

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



T8008SCT = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Last Two Digits of Year (ex: 21 = 2021)
WW or WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	80	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Drain Current (Note 6) V _{GS} = 10V	$T_C = +25^{\circ}C$ $T_C = +70^{\circ}C$	ΙD	111 89	А
Maximum Continuous Body Diode Forward Current (Note 6)	Is	111	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	444	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle =	lsм	444	Α	
Avalanche Current, L=0.1mH (Note 8)	las	40	Α	
Avalanche Energy, L=0.1mH (Note 8)	Eas	80	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P _D	2.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Reja	52	°C/W	
Total Power Dissipation $T_C = +25^{\circ}C$		PD	167	W
Thermal Resistance, Junction to Case (Note 7)	R ₀ JC	0.75	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	80	_	_	V	VGS = 0V, ID = 1mA	
Zero Gate Voltage Drain Current	IDSS			1	μΑ	V _{DS} = 60V, V _{GS} = 0V	
Gate-Source Leakage	Igss		_	±100	nA	V _{GS} = ±20V, V _{DS} = 0V	
ON CHARACTERISTICS (Note 9)	1033			2100	1171	VGS - ±20V, VDS - 0V	
Gate Threshold Voltage	V _{GS(TH)}	2		4	V	$V_{DS} = V_{GS}$, $I_D = 1mA$	
Static Drain-Source On-Resistance	RDS(ON)	_	5.8	7.5	mΩ	V _G S = 10V, I _D = 30A	
Diode Forward Voltage	VsD	_	0.8	1.2	V	V _G S = 0V, I _S = 30A	
DYNAMIC CHARACTERISTICS (Note 10)			l.	l.		1 , -	
Input Capacitance	Ciss	_	1950			V _{DS} = 40V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss		826	_	pF		
Reverse Transfer Capacitance	Crss		56	_			
Gate Resistance	Rg		1.7	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (VGS = 6V)	Qg		23	_		V _{DS} = 40V, I _D = 14A	
Total Gate Charge (V _{GS} = 10V)	Qg	_	34	_	nC		
Gate-Source Charge	Qgs		6	_	nC		
Gate-Drain Charge	Qgd	_	12	_			
Turn-On Delay Time	t _{D(ON)}		8	_		$V_{DD} = 40V$, $V_{GS} = 10V$, $I_{D} = 14A$, $R_{G} = 6\Omega$	
Turn-On Rise Time	tr	_	15	_			
Turn-Off Delay Time	tD(OFF)	_	29	_	ns		
Turn-Off Fall Time	tr		21	_			
Body Diode Reverse Recovery Time	trr		43	_	ns		
Body Diode Reverse Recovery Charge	Q _{RR}	_	49	_			

Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 7. Thermal resistance from junction to soldering point (on the exposed drain pad).
 8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 9. Short duration pulse test used to minimize self-heating effect.
 10. Guaranteed by design. Not subject to product testing.





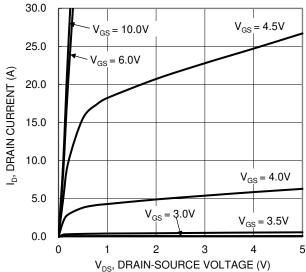


Figure 1. Typical Output Characteristic

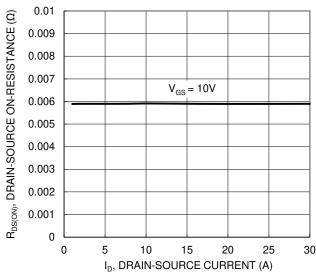


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

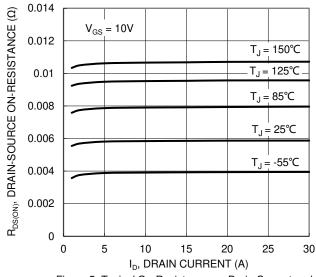


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

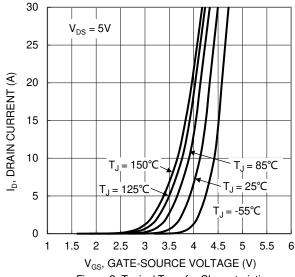


Figure 2. Typical Transfer Characteristic

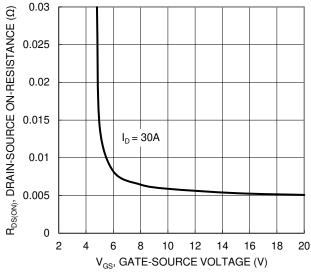


Figure 4. Typical Transfer Characteristic

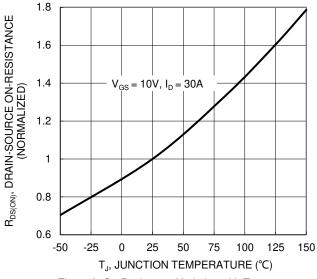


Figure 6. On-Resistance Variation with Temperature



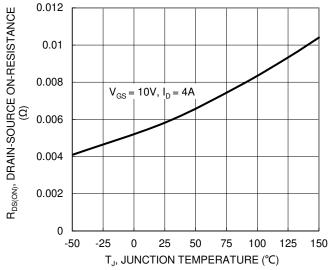


Figure 7. On-Resistance Variation with Temperature

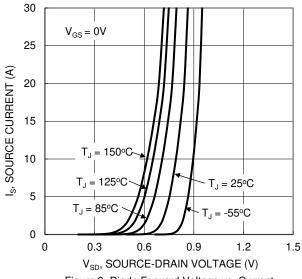


Figure 9. Diode Forward Voltage vs. Current

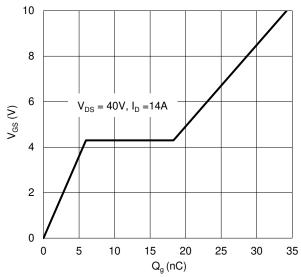


Figure 11. Gate Charge

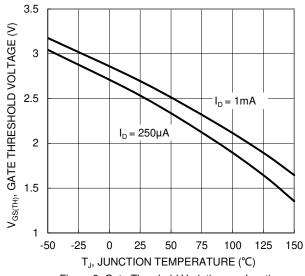


Figure 8. Gate Threshold Variation vs. Junction Temperature

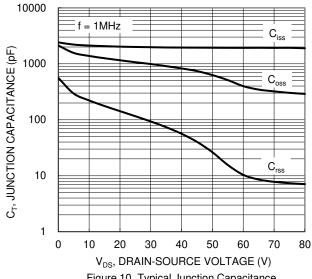


Figure 10. Typical Junction Capacitance

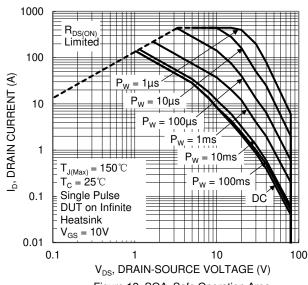


Figure 12. SOA, Safe Operation Area



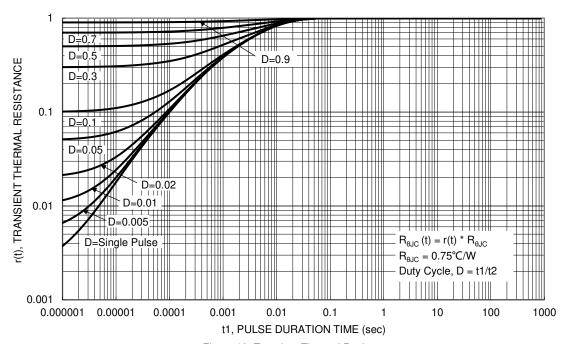


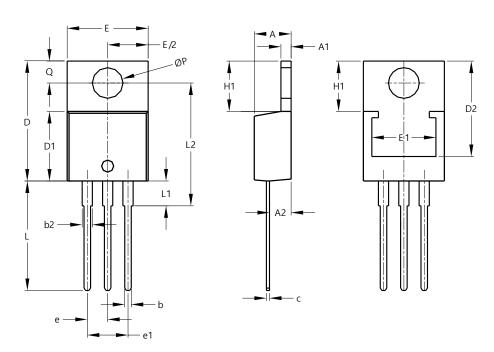
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO220AB



TO220AB					
Dim	Min	Max	Тур		
Α	3.56	4.82	-		
A1	0.51	1.39	-		
A2	2.04	2.92	-		
b	0.39	1.01	0.81		
b2	1.15	1.77	1.24		
C	0.356	0.61	-		
ם	14.22	16.51	-		
D1	8.39	9.01	-		
D2	11.45	12.87	-		
е	-	-	2.54		
e1	-	-	5.08		
Е	9.66	10.66	-		
E1	6.86	8.89	-		
Ħ	5.85	6.85	-		
L	12.70	14.73	-		
1	-	4.42	-		
L2	15.80	17.51	16.00		
Р	3.54	4.08	-		
Ø	2.54	3.42	-		
All Dimensions in mm					



IMPORTANT NOTICE

- 1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com