

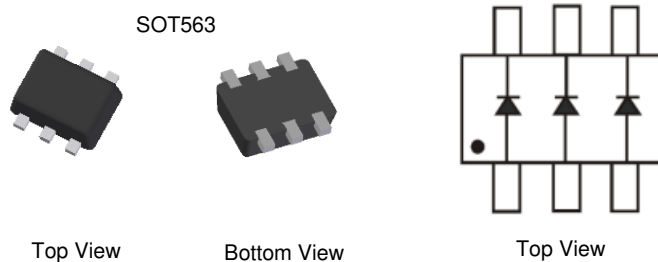
Features

- Fast Switching Speed
- Low Forward Voltage: Maximum of 0.715V at 1mA
- Fast Reverse Recovery: Maximum of 4ns
- Low Capacitance: Maximum of 1.5pF
- Low Leakage Current
- Triple Isolated Fast Switching Diode Array
- Ultra-Small Surface Mount Package
- Thermally Efficient Copper Alloy Leadframe for High-Power Dissipation
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DIODES™ BAS16VAQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: SOT563
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper Alloy leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Weight: 0.003 grams (approximate)



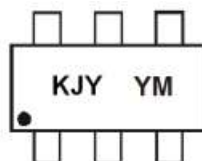
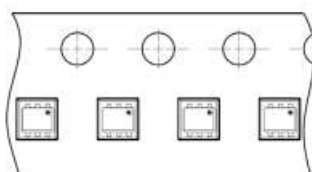
Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
BAS16VAQ-7	SOT563	3000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

Special taping orientation requirement, see below image for details.



KJY = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: J = 2022)
 M = Month (ex: 9 = September)

Date Code Key

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	M	N	O	P	R	S	T	U	V

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage	V_{RM}	100	V
Peak Repetitive Reverse Voltage	V_{RRM}	100	V
Working Peak Reverse Voltage	V_{RWM}		
DC Blocking Voltage	V_R		
RMS Reverse Voltage	$V_{R(RMS)}$	71	V
Forward Continuous Current (Note 5)	I_{FM}	200	mA
Non-Repetitive Peak Forward Surge Current	I_{FSM}	@ $t = 1.0\mu\text{s}$	4.0
		@ $t = 1.0\text{ms}$	1.0
		@ $t = 1.0\text{s}$	0.5

Thermal Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_D	350	mW
Thermal Resistance Junction to Ambient Air (Note 5)	$R_{\theta JA}$	357	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	$V_{(BR)R}$	100	—	V	$I_R = 100\mu\text{A}$
Forward Voltage	V_F	—	0.715	V	$I_F = 1.0\text{mA}$
		—	0.855		$I_F = 10\text{mA}$
		—	1.0		$I_F = 50\text{mA}$
		—	1.25		$I_F = 150\text{mA}$
Leakage Current (Note 6)	I_R	—	0.5	μA	$V_R = 80\text{V}$
		—	50	μA	$V_R = 80\text{V}, T_J = 150^\circ\text{C}$
		—	30	μA	$V_R = 25\text{V}, T_J = 150^\circ\text{C}$
		—	30	nA	$V_R = 25\text{V}$
Total Capacitance	C_T	—	1.5	pF	$V_R = 0, f = 1.0\text{MHz}$
Reverse Recovery Time	t_{rr}	—	4.0	ns	$I_F = I_R = 10\text{mA}, I_{rr} = 0.1 \times I_R, R_L = 100\Omega$

Notes: 5. Device mounted on FR-4 PCB, on minimum recommended, 2oz copper pad layout.
6. Short duration pulse test used to minimize self-heating effect.

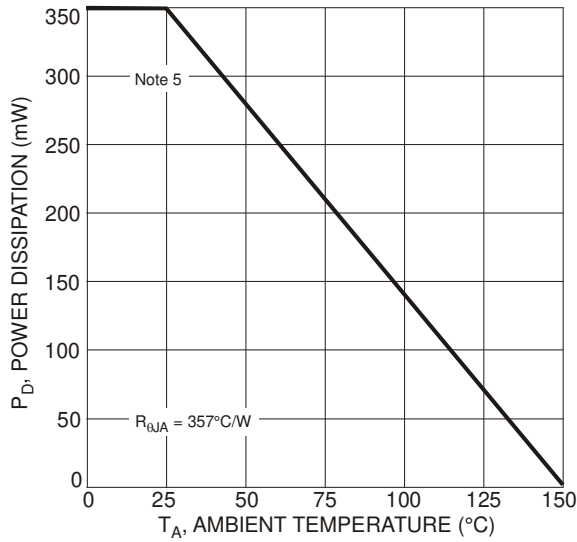


Fig. 1 Power Derating Curve, Total Package

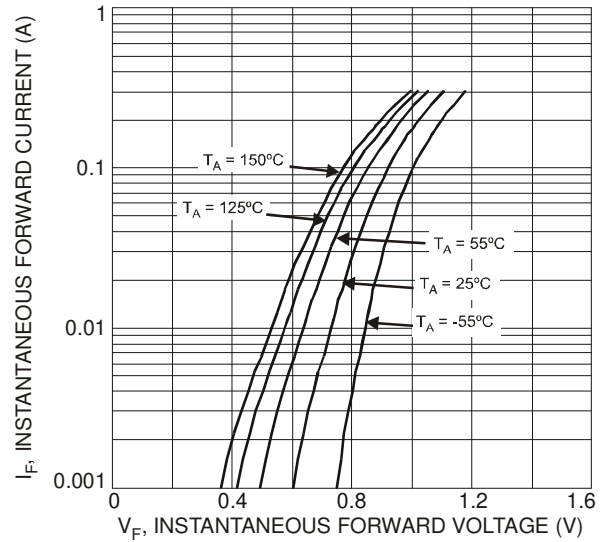


Fig. 2 Typical Forward Characteristics, Per Element

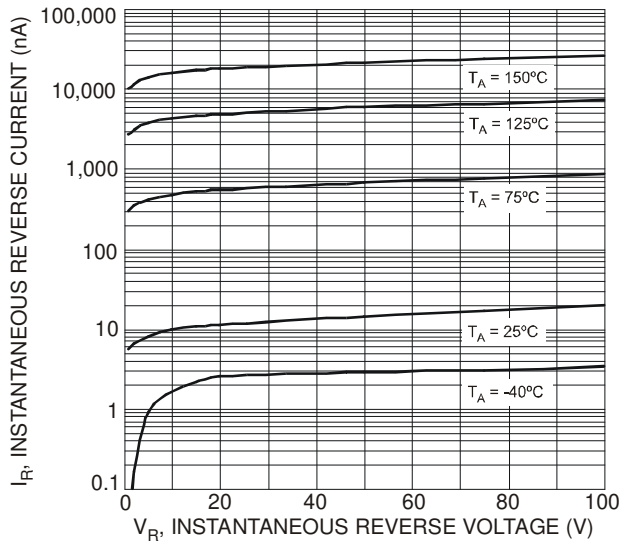


Fig. 3 Typical Reverse Characteristics, Per Element

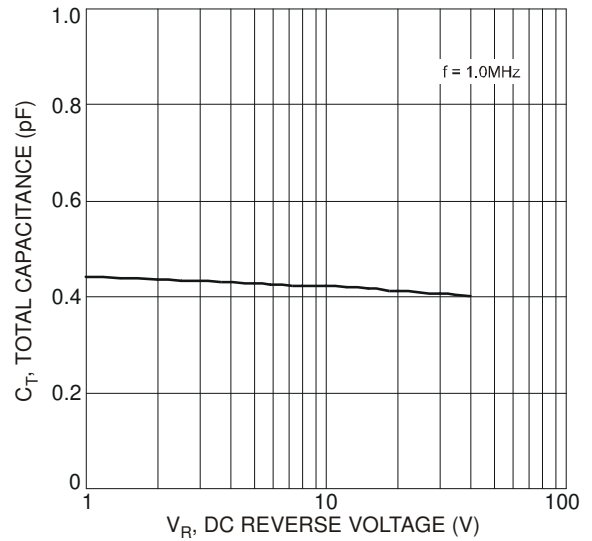
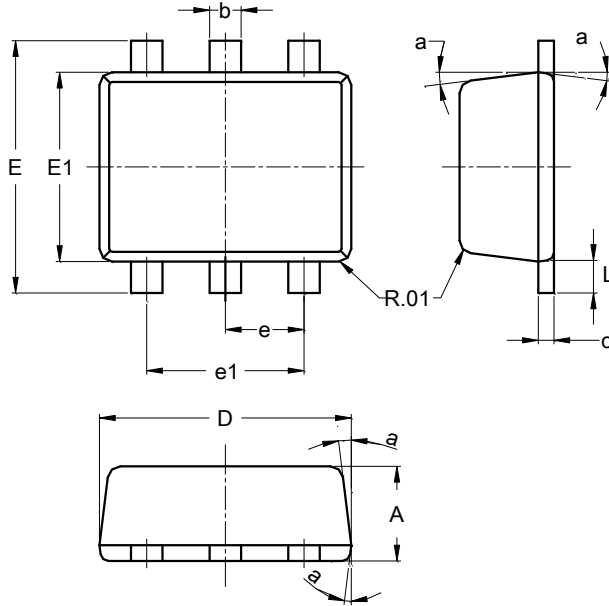


Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT563

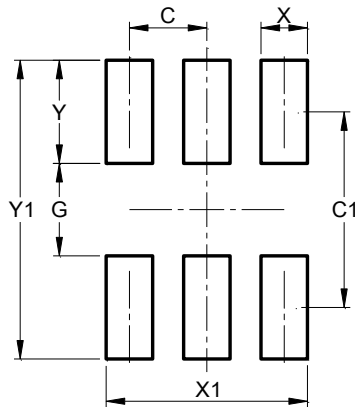


SOT563			
Dim	Min	Max	Typ
A	0.55	0.60	--
b	0.15	0.30	0.20
c	0.10	0.18	0.11
D	1.50	1.70	1.60
E	1.55	1.70	1.60
E1	1.10	1.25	1.20
e	--	--	0.50
e1	0.90	1.10	1.00
L	0.10	0.30	0.20
a	8°	9°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT563



Dimensions	Value (in mm)
C	0.500
C1	1.270
G	0.600
X	0.300
X1	1.300
Y	0.670
Y1	1.940

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