

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D $T_A = +25^\circ\text{C}$
20V	0.200Ω @ $V_{GS}=4.5\text{V}$	2.1A
	0.240Ω @ $V_{GS}=2.5\text{V}$	1.9A
	0.310Ω @ $V_{GS}=1.8\text{V}$	1.7A

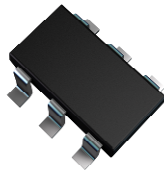
Description

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

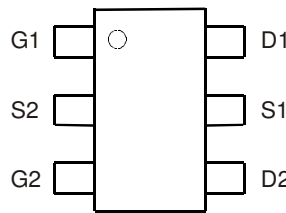
Applications

- Power Management Functions
- Disconnect Switches
- Relay Driving and Load Switching

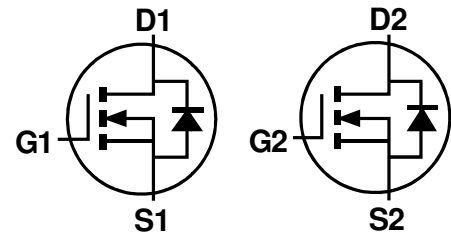
SOT26



Top View



Pin Out - Top View



Equivalent Circuit

Features and Benefits

- Low On-Resistance
- Low Gate Drive Capability
- SOT26 (dual) Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SOT26
- Case Material: Molded Plastic.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe.
Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.018 grams (Approximate)

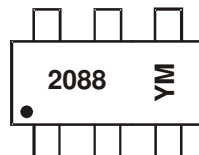
Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
ZXMN2088DE6TA	Standard	SOT26	3,000 / Tape & Reel
ZXMN2088DE6TC	Standard	SOT26	10,000 / Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

SOT26



2088 = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: C = 2015)
 M or \bar{M} = Month (ex: 9 = September)

Date Code Key

Year	2015	2016	2017	2018	2019	2020	2021	2022				
Code	C	D	E	F	G	H	I	J				
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

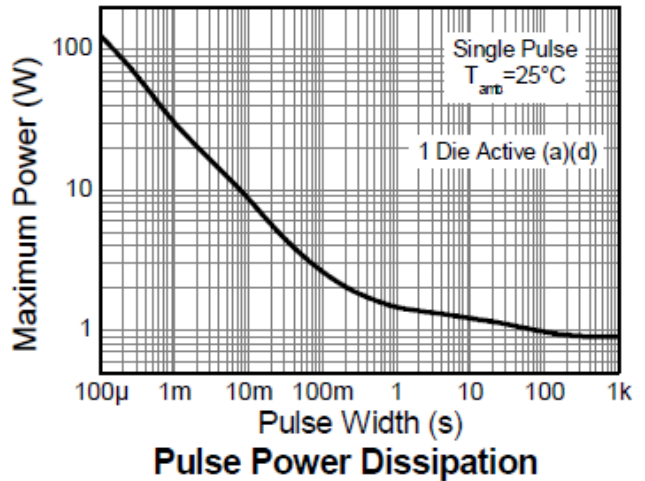
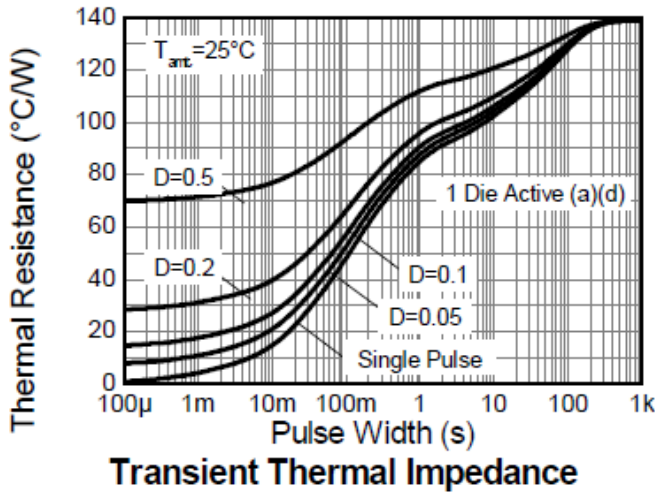
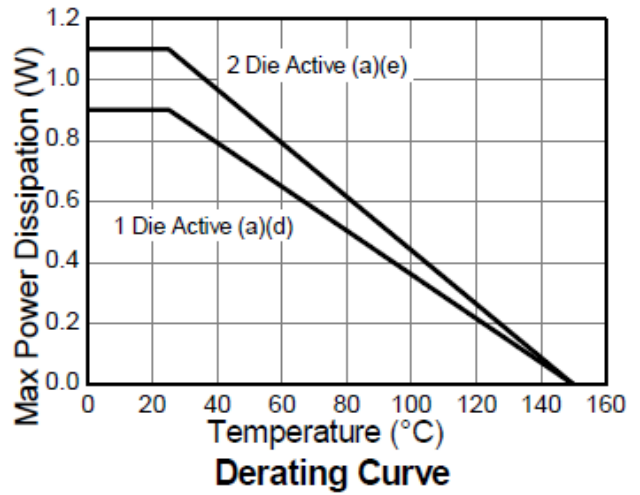
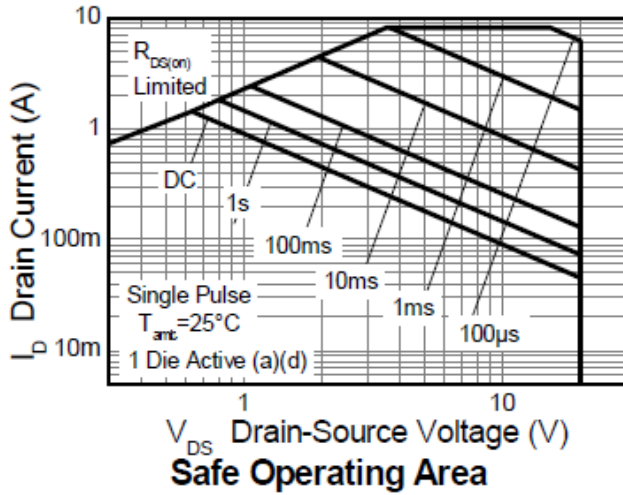
Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GS}	±8	V
Continuous Drain Current	V _{GS} = 4.5V	T _A = +25°C (Notes 6 & 8)	I _D	2.1	A
		T _A = +70°C (Notes 6 & 8)		1.7	
		T _A = +25°C (Notes 5 & 8)		1.7	
Pulsed Drain Current		(Note 7)	I _{DM}	8	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation at T _A = +25°C Linear Derating Factor	(Notes 5 & 8)	P _D	0.9 7.2	W mW/°C
	(Notes 5 & 9)	P _D	1.1 8.8	W mW/°C
	(Notes 6 & 8)	P _D	1.3 10.4	W mW/°C
Thermal Resistance, Junction to Ambient	(Notes 5 & 8)	R _{θJA}	139	°C/W
	(Notes 5 & 9)		113	
	(Notes 6 & 8)		96	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

- Notes:
5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
 6. Same as Note (5), except the device is measured at t ≤ 5 sec.
 7. Same as Note (5), except the device is pulsed with D = 0.02 and pulse width 300µs. The pulse width is limited by the maximum junction temperature.
 8. For device with one active die.
 9. For device with two active die running at equal power.

Thermal Characteristics

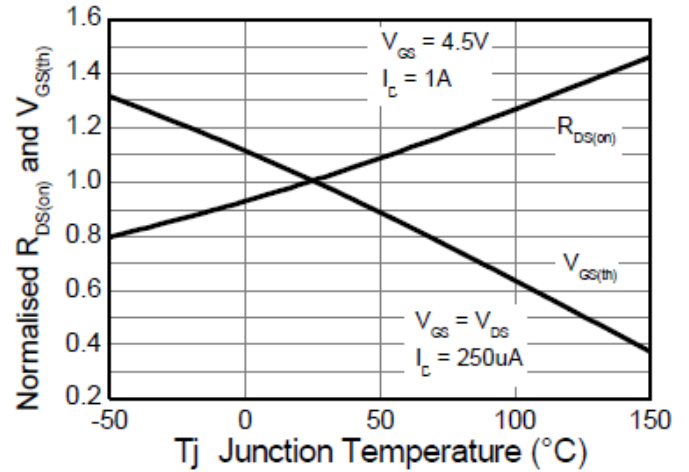
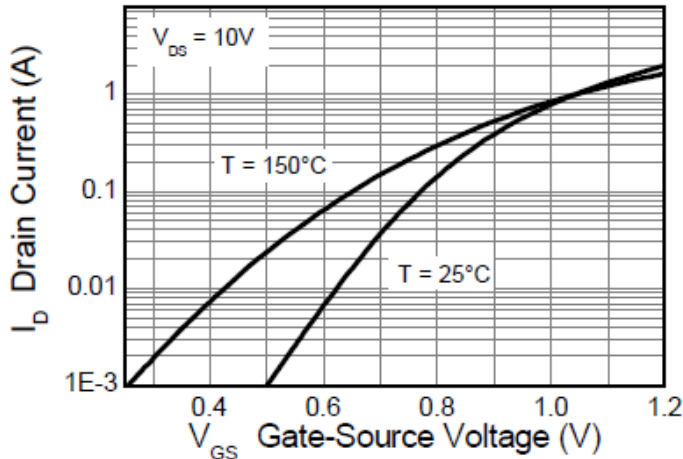
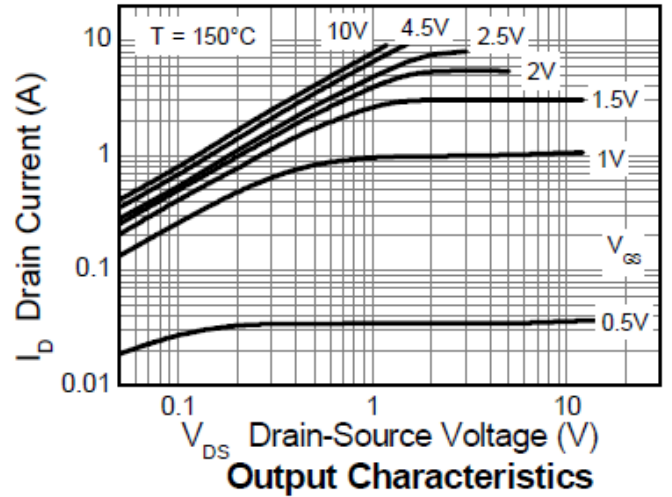
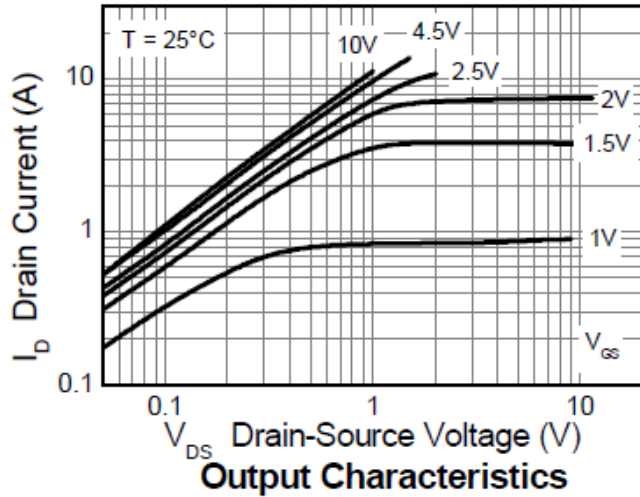


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	20	—	—	V	I _D = 250μA, V _{GS} = 0V
Zero Gate Voltage Drain Current	I _{DSS}	—	—	100	nA	V _{DS} = 3V, V _{GS} = 0V
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(th)}	0.4	—	1.0	V	I _D = 250μA, V _{DS} = V _{GS}
Static Drain-Source On-Resistance (Note 10)	R _{DS(on)}	—	0.112	0.200	Ω	V _{GS} = 4.5V, I _D = 1.0A
			0.137	0.240		V _{GS} = 2.5V, I _D = 0.6A
			0.165	0.310		V _{GS} = 1.8V, I _D = 0.3A
Forward Transconductance (Notes 10 & 12)	g _{fs}	—	4.6	—	S	V _{DS} = 10V, I _D = 1.0A
Diode Forward Voltage (Note 12)	V _{SD}	—	0.75	0.95	V	I _S = 1.0A, V _{GS} = 0V, T _J = +25°C
DYNAMIC CHARACTERISTICS (Note 12)						
Input Capacitance	C _{iss}	—	279	—	pF	V _{DS} = 10V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	52	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	29	—	pF	
Total Gate Charge	Q _g	—	3.8	—	nC	V _{GS} = 4.5V V _{DS} = 10V I _D = 2.4A
Gate-Source Charge	Q _{gs}	—	0.41	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.56	—	nC	
Turn-On Delay Time (Note 11)	t _{D(on)}	—	2.0	—	ns	V _{DD} = 10V, V _{GS} = 4.5V I _D = 1.0A, R _G ≅ 6.0Ω
Turn-On Rise Time (Note 11)	t _r	—	3.2	—	ns	
Turn-Off Delay Time (Note 11)	t _{D(off)}	—	12.7	—	ns	
Turn-Off Fall Time (Note 11)	t _f	—	6.2	—	ns	
Reverse Recovery Time	t _{rr}	—	6.6	—	ns	I _F = 1.24A, di/dt = 100A/μs, T _J = +25°C
Reverse Recovery Charge	Q _{rr}	—	1.6	—	nC	

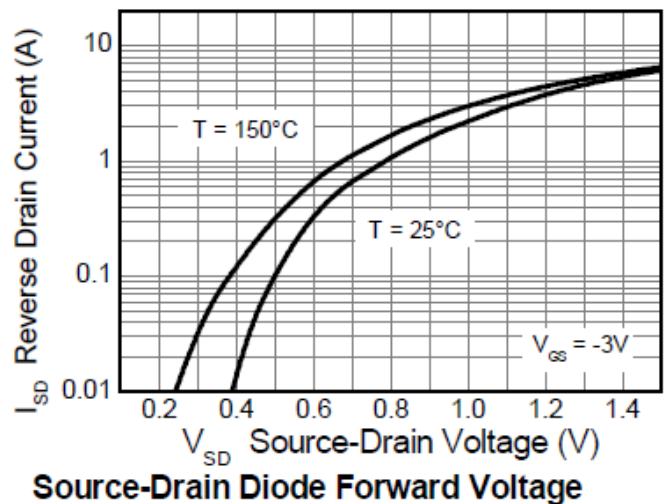
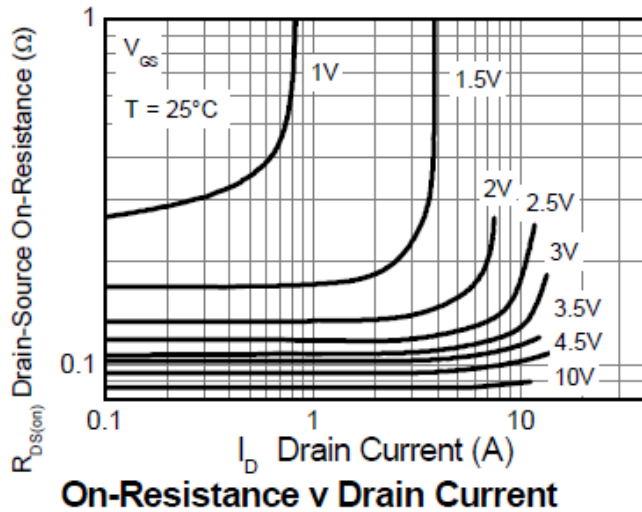
- Notes:
10. Measured under pulsed conditions. Pulse width ≤ 300μs; duty cycle ≤ 2%.
 11. Switching characteristics are independent of operating junction temperature.
 12. For design aid only, not subject to production testing.

Typical Characteristics

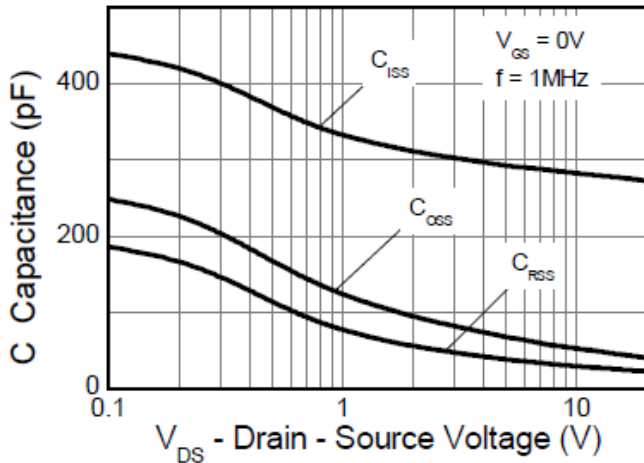


Typical Transfer Characteristics

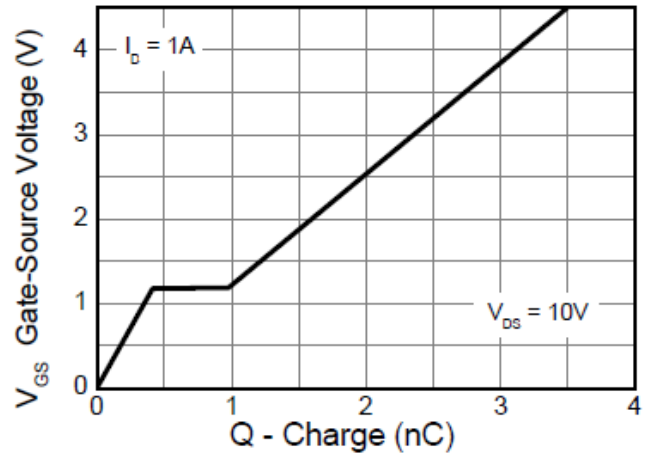
Normalised Curves v Temperature



Typical Characteristics (continued)

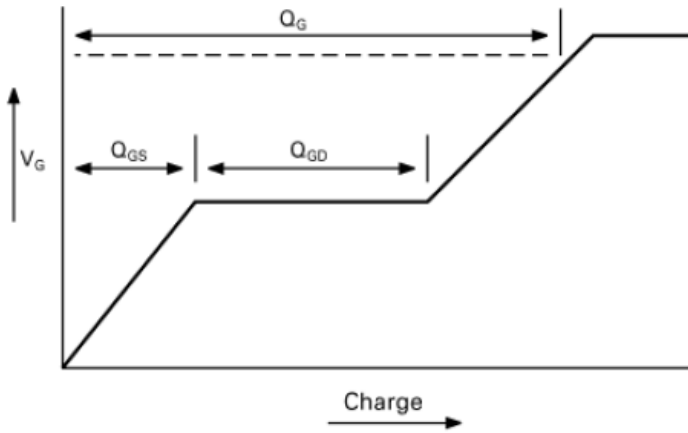


Capacitance v Drain-Source Voltage

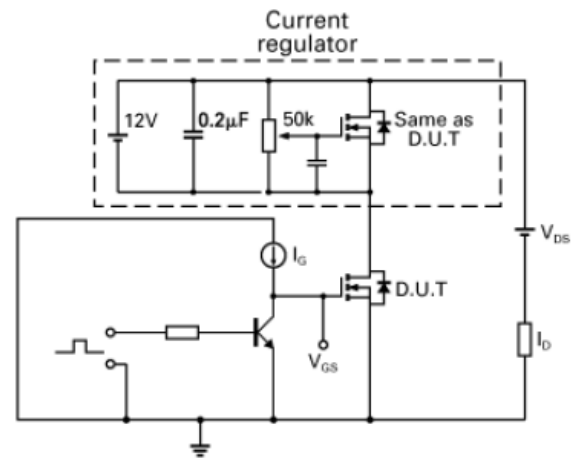


Gate-Source Voltage v Gate Charge

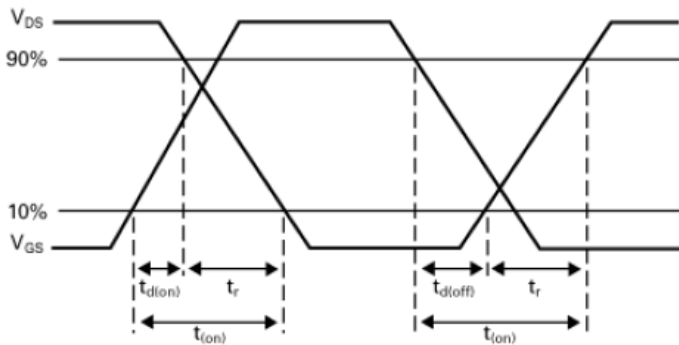
Test Circuits



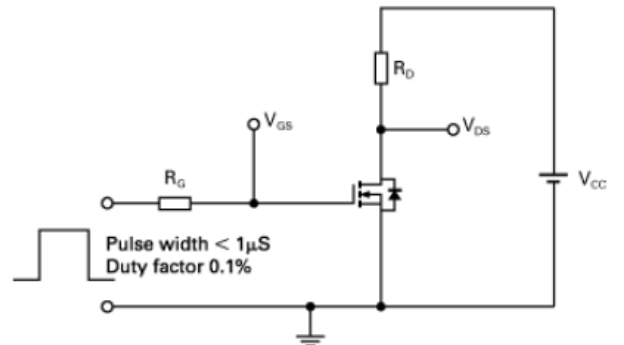
Basic gate charge waveform



Gate charge test circuit



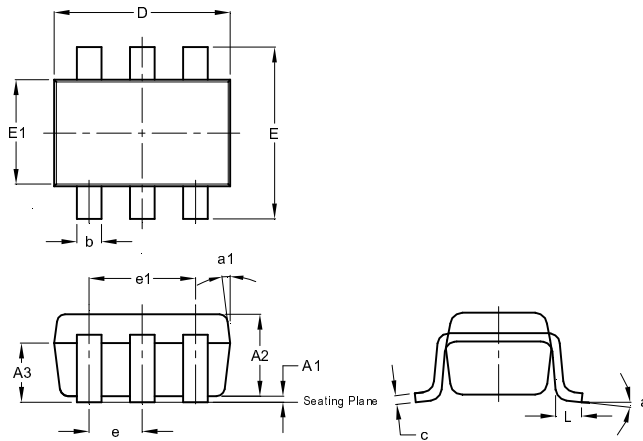
Switching time waveforms



Switching time test circuit

Package Outline Dimensions

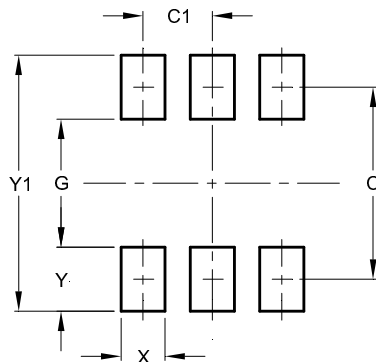
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SOT26			
Dim	Min	Max	Typ
A1	0.013	0.10	0.05
A2	1.00	1.30	1.10
A3	0.70	0.80	0.75
b	0.35	0.50	0.38
c	0.10	0.20	0.15
D	2.90	3.10	3.00
e	-	-	0.95
e1	-	-	1.90
E	2.70	3.00	2.80
E1	1.50	1.70	1.60
L	0.35	0.55	0.40
a	-	-	8°
a1	-	-	7°
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	2.40
C1	0.95
G	1.60
X	0.55
Y	0.80
Y1	3.20

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2015, Diodes Incorporated

www.diodes.com