

# **USB2ANY Interface Adapter**

This user's guide describes the USB2ANY Interface Adapter and its general setup and operation.



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#### Introduction

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## Trademarks

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# 1 Introduction

## 1.1 How to Use This Manual

This document contains the following chapters:

- Chapter 1 Introduction
- Chapter 2 Overview of Hardware Design
- Chapter 3 Connecting the USB2ANY
- Appendices

# 1.2 Information About Cautions and Warnings



## CAUTION

The EVM contains components that can potentially be damaged by electrostatic discharge. Always transport and store the EVM in the supplied ESD bag, when not in use. Handle using an antistatic wristband. Operate on an antistatic work surface. For more information on proper handling, refer to *Electrostatic Discharge (ESD)*.

Due to the limited use and location of use (antistatic professional laboratory or other controlled environment) the equipment under test (EUT) will not see an ESD event above 2 kV, and was tested accordingly. Manufacturer's specifications in the manual include this limitation.



# 1.3 FCC Warning

This equipment is intended for use in a laboratory test environment only. The equipment generates, uses, and can radiate radio frequency energy and has not been tested for compliance with the limits of computing devices pursuant to subpart J of part 15 of FCC rules, which are designed to provide reasonable protection against radio frequency interference. Operation of this equipment in other environments may cause interference with radio communications, in which case, the user, at their own expense, is required to take whatever measures may be required to correct this interference.

# 1.4 Description

The USB2ANY is a small dongle intended to allow a computer to control an electronic evaluation module (EVM) via a USB connection. These EVMs cannot normally connect directly to the computer because of their specialized interfaces, which are typically supported on commercial computers.

The USB2ANY is a USB 2.0 device supporting several different interfaces, including:

- 13 general purpose input/output (GPIO) (shared) lines with switchable pullup or pulldown resistors
- I<sup>2</sup>C with programmable 1.5-k $\Omega$  pullup resistors
- SPI up to 8-MHz SCLK
- UART (300 to 230,400 Baud)
- · Four analog inputs, 12-bit analog-to-digital converter (ADC)
- Two analog outputs, 8-bit digital-to-analog converter (DAC)
- Four PWM outputs
- · Several other interfaces
- Switchable 3.3-V or 5-V power to DUT

The USB2ANY board can source regulated 3.3-V and 5.0-V DC power to the target device, up to a maximum of 300 mA (combined 3.3 V and 5.0 V). The voltage outputs have built-in current sensing and limiting circuits that protect both the EVM and the USB2ANY from overcurrent, shorts, and so forth.

The USB2ANY is shipped with a USB cable, a 10-pin cable (for most common interfaces), and a 30-pin cable (for all supported interfaces).



Figure 1. USB2ANY and the Supplied Cables

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Introduction



## 2 Overview of Hardware Design

The USB2ANY is based on TI's MSP430F5529 microcontroller, which has the following features making it ideal for a multi-interface controller:

- 16-bit RISC CPU
- Operates at 3.3 V
- 128KB flash memory
- 10KB SRAM
- 63 I/O pins
- Fast execution (25-MHz clock)
- Four 16-bit counters and timers
- Two universal serial communication interfaces (supports SPI, I<sup>2</sup>C, UART)
- Full-speed universal serial bus (USB) with internal PHY supporting USB 2.0
- 12-bit ADC
- · Hardware multiplier supporting 32-bit operations
- Three-channel internal DMA

See Appendix A: USB2ANY Schematic for schematic drawings.

The block diagram in Figure 2 shows the fundamental parts of the USB2ANY:

- 1. USB connector The USB connector connects to the host computer. The USB2ANY is a USB 2.0 full-speed device using a HID-class driver. The board has a Mini-USB connector installed.
- Power Management The power management circuitry allows the application program to turn the +3.3-V or the +5.0-V external power outputs on or off. It also detects power faults (for example, overload or short circuit) and notifies the application program.
- 3. **MSP430F5529** This MSP430 microcontroller communicates with the application program and executes the received API function commands. It is programmed with the USB2ANY firmware, which controls all of the interface peripherals and other necessary functions (for example, timing, scheduling, encoding and decoding data).
- 4. **DAC1, DAC2** These are the DAC outputs. Under program control, these outputs can source a voltage from 0 V to 3.3 V, in 256 steps.
- 5. **GPIO** These GPIO signals are used to read/write single-bit digital signals. When used as inputs, they can optionally be programmed to be pulled up, pulled down, or floating (MOS input with no resistor).
- I<sup>2</sup>C The inter-integrated circuit interface is an industry-standard serial interface often used to communicate between devices in a closed system. I<sup>2</sup>C requires two signal wires plus a ground connection. The I<sup>2</sup>C interface of the USB2ANY uses a 7-bit address, which allows the bus to support up to 127 devices.
- 7. SPI The industry-standard serial peripheral interface is generally faster than I<sup>2</sup>C, but more difficult to implement. It is often used to communicate between devices in a closed system. SPI usually requires four signal wires plus a ground connection, but can use as few as three wires or as many as needed to select among the number of devices connected to the bus. While there is no real limit as to the maximum number of devices on a SPI bus, the number is usually limited by practical issues like fanout, cable length, signaling speed, and capacitive loading.
- PWM The pulse-width modulated outputs allow the USB2ANY to produced pulse trains with variable frequency and duty cycle. This can be used for clocks, integrators (such as those used in PWM servo motors), tone generation, and so forth.
- Analog Inputs The USB2ANY has four analog inputs. Two of the inputs (ADC0, ADC1) have no external amplifiers (effectively unity gain) and the other two inputs (ADC2, ADC3) have a gain of two. The gain allows smaller signals to be amplified slightly, giving them a larger *scale* at the ADC inputs. The MSP430F5529 utilizes 12-bit ADCs, giving a full-scale digital value of 4095.
- 10. **Ext.**  $V_{REF}$  The external voltage reference allows selection of the voltage used as the reference by the ADCs. By default,  $V_{REF}$  is set to use AV<sub>CC</sub>, which is physically connected to  $V_{cc}$  (3.3-V nominal).





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### Figure 2. USB2ANY Block Diagram

Figure 3 shows the USB2ANY PCB without the enclosure. One end has three interface connectors that are typically connected to an EVM (J3, J4, and J5). Connectors J3 and J5 have eight pins and J4 has 10 pins. Most applications will use only J4 because it provides access to the most commonly used interfaces (see *Using the 10-Pin Cable* in *Connecting the USB2ANY*). Alternatively, a 30-pin cable is provided that connects to all three connectors (J3, J4, and J5) and provides access to all available interface signals.



Figure 3. USB2ANY PCB

The Status LED indicates the status of the electronics and firmware, as follows:

## Table 1. Status LEDs

LED State	Meaning
OFF	No power to the board. USB cable may be unplugged.
ON	Power is applied and the board is operating normally.
Blinking	A hardware, firmware, or USB error occurred. Board must be power-cycled.

**NOTE:** The LED may also be programmed using the software API to be ON, OFF, or blinking. In that case, the LED may not indicate the actual status of the electronics or firmware.

**Switch S1** is used to put the firmware into bootstrap loader (BSL) mode. The BSL mode is used to load or update the USB2ANY firmware and is entered whenever the board is powered-up while *Switch S1* is depressed.

The **USB Mini-B** (J2) connector is used to connect the USB2ANY to the host computer. A cable with a USB Mini-B connector on one end and a USB A connector on the other end is provided with the USB2ANY.

The **JTAG** connector (J1) is used only for firmware development and is normally not populated with a physical connector.



## 3 Connecting the USB2ANY

**NOTE:** This section assumes you are using the USB2ANY in its gray plastic enclosure (as shown in Figure 1). If the USB2ANY board has been removed from the enclosure, or the board came without an enclosure, some interpretation and adjustments may need to be made in these instructions.

The USB2ANY ships with two cables, one 10-pin and one 30-pin. Both cables are standard 0.050-in (1.25-mm) pitch ribbon cables with IDC-type connectors. The connectors have a key to assure that they are installed correctly.



Figure 4. 10-Pin Cable With Key

**NOTE:** If you are using the USB2ANY board without its enclosure, the key must face away from the PCB. Be sure to carefully align the male pins on the PCB with the cable connector.



Connecting the USB2ANY

## 3.1 Using the 10-Pin Cable



Figure 5. 10-pin Cable Connection

The 10-pin cable is connected to J4 as Figure 5 shows and can be used for most of the popular interfaces, including:

- I2C: SDA and SCL
- SPI: SCLK, MOSI, MISO, and CS
- UART: RX and TX
- Power: 3.3-V external
- GPIO: GPIO0, GPIO1, GPIO2, GPIO3, GPIO4, GPIO5, GPIO6, and GPIO7
- **PWM**: PWM0, PWM1, and PWM2
- OneWire: OW1, OW2, and OW3
- **µWire**: SCLK, MOSI, MISO, and CS
- **RFFE**: SCLK and SDATA

## 3.2 Using the 30-Pin Cable

The **30-pin cable** is connected across **J3**, **J4**, and **J5** as Figure 6 shows, and can be used to access all of the available interfaces signals (see Table 2 for a list of all the signals).



Figure 6. 30-Pin Cable Connection



## 3.3 Connecting the USB2ANY to the Computer

The USB2ANY has a USB Mini-B connector on the right side. Plug the provided USB cable (or any USB cable with a Mini-B connector) into the USB2ANY. Plug the other end of the cable (USB 'A') into the computer.



## Figure 7. USB Connector

When connected, the LED on the USB2ANY should flicker quickly a few times, and then stay on.

# 3.4 Connecting the USB2ANY to an EVM

Depending on the EVM, choose either the 10-pin ribbon cable or the 30-pin ribbon cable. The majority of EVMs use the 10-pin version, as Figure 8 shows.



Figure 8. Connection Between the USB2ANY and the EVM



Appendix A SNAU228–January 2018

# **USB2ANY Schematic**

Figure 9, Figure 10, and Figure 11 illustrate the USB2ANY schematics.







Appendix A









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Appendix B SNAU228–January 2018

# **USB2ANY Cable Connections**

The USB2ANY has four interface connectors: one USB 2.0 connector (J2) and three I/O connectors (J3, J4, and J5). The USB connector is a standard 'A' type mini USB receptacle. The I/O connectors are standard dual-row, 0.1-in center, pin headers.

I/O connectors J3 and J5 are 8-pin type and J4 is a 10-pin type. They are configured such that they will accept either individual cable connections or a single 30-pin connection.





The standard USB2ANY kit (HPA665-001) includes both a 10-pin cable and a 30-pin cable. The 10-pin cable is intended to be connected to J4. This single connection will supply the needs for many users as it provides access to the following interfaces and signals:

- I<sup>2</sup>C: SDA and SCL
- SPI: SCLK, MOSI, MISO, and CS
- UART: RX and TX
- Power: 3.3 V External
- GPIO: GPIO0, GPIO1, GPIO2, GPIO3, GPIO4, GPIO5, GPIO6, and GPIO7
- PWM: PWM0, PWM1, and PWM2
- OneWire: OW1, OW2, and OW3
- μWire: SCLK, MOSI, MISO, and CS
- RFFE: SCLK and SDATA

## • Interrupts: INT0, INT1, and INT2

The 10-pin cable is about 6 in long and has a keyed female 10-pin IDC connector on each end. Connect the cable to the USB2ANY board as Figure 13 shows (note that the key must be facing up, away from the board).



Figure 13. 10-Pin IDC Cable Connector

The opposite end of the cable is intended to be connected to the EVM or other target device. The red stripe on the cable indicates pin 1.





Schematic Pin Number	Cable Pin Number	Signals Available
J4-10	1	GPIO7, PWM0, INT2, OW2, OW5
J4-9	2	GPIO6, PWM1, RFFE:SCLK, SPI:CS, INT1, µWIRE:CS, OW1
J4-8	3	GPIO5, SPI:SOMI, UART:RXD, μWIRE:SOMI
J4-7	4	GPIO4, SPI:SIMO, UART:TXD, μWIRE:SIMO
J4-6	5	3.3VEXT
J4-5	6	GND
J4-4	7	GPIO3, PWM2, RFFE:SDATA, INT0
J4-3	8	GPIO2, ES:DOUT, SPI:SCLK, µWIRE:SCLK

	•	· · · · ·
Schematic Pin Number	Cable Pin Number	Signals Available
J4-2	9	GPIO1, I2C:SCL, OW3
J4-1	10	GPIO0, I2C:SDA, OW4

# **NOTE:** The pin numbers for J4 on the schematics are for reference only and do not correspond to the pin numbers for cable connections.

The 30-pin cable is also about 6 inches in length and has a keyed female 30-pin IDC connector on each end. This cable provides access to all available signals. The cable should be connected to the USB2ANY board as Figure 15 shows (note that the key must be facing up, away from the board).



Figure 15. 30-Pin IDC Cable Connector

Connect the opposite end of the cable to the EVM or other target device. The red stripe on the cable indicates pin 1.



Figure 16. 30-Pin IDC Cable Detail

### Table 3. 30-pin Cable Pinouts

Schematic Pin Number	Cable Pin Number	Signals Available
J3-8	1	DAC0
J3-7	2	PIO12, CLOCK, EFC0, INT3
J3-6	3	+5VEXT

Schematic Pin Number	Cable Pin Number	Signals Available
J3-5	4	GND
J3-4	5	PWM3
J3-3	6	GND
J3-2	7	DAC1
J3-1	8	GND
_	9	(unused)
_	10	(unused)
J4-10	11	GPIO7, PWM0, INT2, OW2, OW5
J4-9	12	GPIO6, PWM1, RFFE:SCLK, SPI:CS, INT1, µWIRE:CS, OW1
J4-8	13	GPIO5, SPI:SOMI, UART:RXD, μWIRE:SOMI
J4-7	14	GPIO4, SPI:SIMO, UART:TXD, μWIRE:SIMO
J4-6	15	3.3VEXT
J4-5	16	GND
J4-4	17	GPIO3, PWM2, RFFE:SDATA, INT0
J4-3	18	GPIO2, ES:DOUT, SPI:SCLK, μWIRE:SCLK
J4-2	19	GPIO1, I2C:SCL, OW3
J4-1	20	GPIO0, I2C:SDA, OW4
_	21	(unused)
_	22	(unused)
J5-8	23	ADC0
J5-7	24	ADC1
J5-6	25	GPIO9, ADC2, ES:AIN
J5-5	26	GPIO8, ADC3
J5-4	27	GND
J5-3	28	GND
J5-2	29	GPIO11, VREF+
J5-1	30	GPIO10, VREF-

# Table 3. 30-pin Cable Pinouts (continued)

**NOTE:** The pin numbers for J3, J4, and J5 on the schematics are for reference only and do not correspond to the pin numbers for cable connections.

### STANDARD TERMS FOR EVALUATION MODULES

- 1. Delivery: TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
  - 1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM ("Software") shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software
  - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
- 2 Limited Warranty and Related Remedies/Disclaimers:
  - 2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
  - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
  - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.
- 3 Regulatory Notices:
  - 3.1 United States
    - 3.1.1 Notice applicable to EVMs not FCC-Approved:

**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

#### FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### 3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

#### **Concerning EVMs Including Detachable Antennas:**

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

#### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

- 3.3 Japan
  - 3.3.1 Notice for EVMs delivered in Japan: Please see http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_01.page 日本国内に 輸入される評価用キット、ボードについては、次のところをご覧ください。 http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_01.page
  - 3.3.2 Notice for Users of EVMs Considered "Radio Frequency Products" in Japan: EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

- 1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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- 2. 実験局の免許を取得後ご使用いただく。
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- 3.3.3 Notice for EVMs for Power Line Communication: Please see http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_02.page 電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_02.page
- 3.4 European Union
  - 3.4.1 For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

- 4 EVM Use Restrictions and Warnings:
  - 4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.
  - 4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.
  - 4.3 Safety-Related Warnings and Restrictions:
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