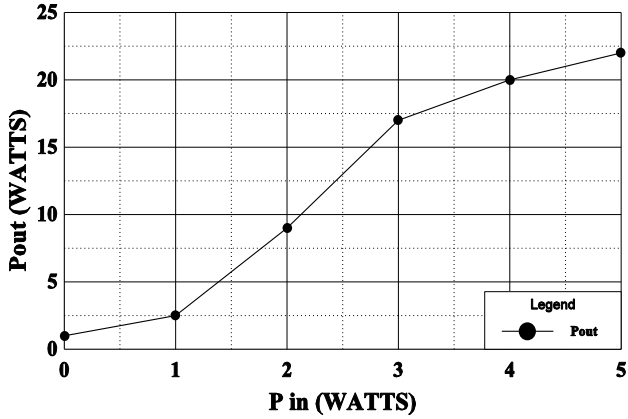




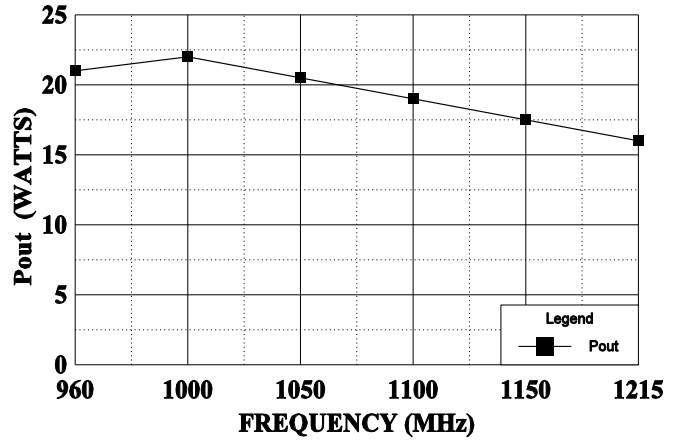
**POWER OUTPUT vs POWER INPUT**

Vcc = 40 V, 1090 MHz



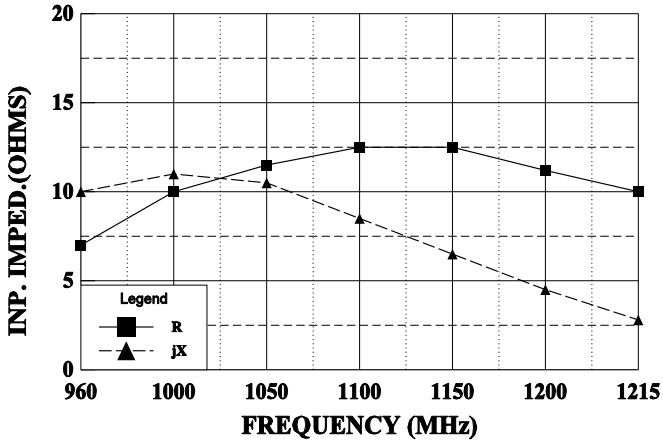
**POWER OUTPUT VS FREQUENCY**

Vcc = 40V, Pin = 3.0 W



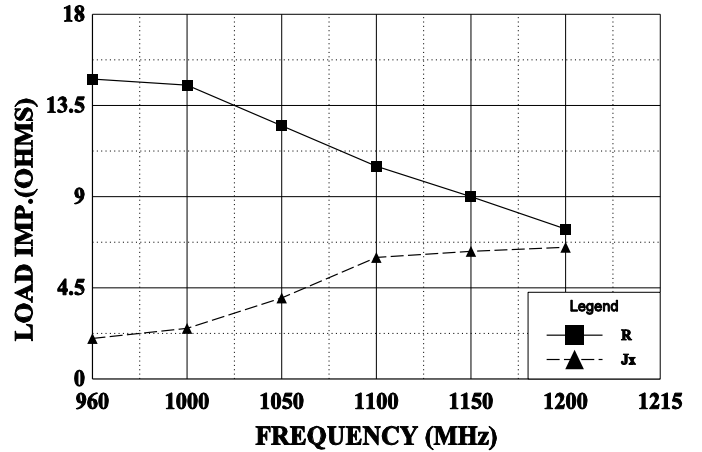
**SERIES INPUT IMPEDANCE vs FREQUENCY**

Vcc = 40 V, Pin = 15 W



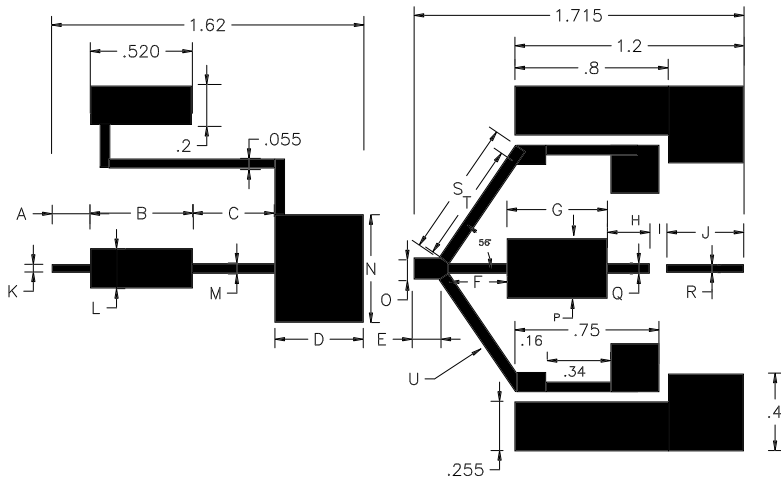
**SERIES LOAD IMPEDANCE vs FREQUENCY**

Vcc = 40 V, Po = 15 W



REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
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DIM	INCHES
A	.200
B	.530
C	.430
D	.460
E	.125
F	.300
G	.520
H	.240
I	.070
J	.400
K	.040
L	.205
M	.050
N	.560
O	.110
P	.310
Q	.050
R	.040
S	.710
T	.610
U	.060

TAN 15 TEST CIRCUIT

file:tan15ckt.dwg 8/17/95 jc

DIELECTRIC = 15 MIL THICK TFE Er = 2.55



**CHz TECHNOLOGY**

CAGE  
OPJR2

DWG NO.

**TAN 15**

REV —

SCALE

**1/1**

SHEET