

MJE340

High Voltage General Purpose Applications

- High Collector-Emitter Breakdown Voltage
- Suitable for Transformer
- Complement to MJE350



NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	300	V	
V _{CEO}	Collector-Emitter Voltage	300	V	
V _{EBO}	Emitter-Base Voltage	5	V	
I _C	Collector Current	500	mA	
P _C	Collector Dissipation (T _C =25°C)	20	W	
T _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 65 ~ 150	°C	

Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 1 \text{ mA}, I_B = 0$	300		V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 300V, I_{E} = 0$		100	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{BE} = 3V, I_{C} = 0$		100	μΑ
h _{FE}	DC Current Gain	$V_{CE} = 10V, I_{C} = 50mA$	30	240	

Typical Characteristics

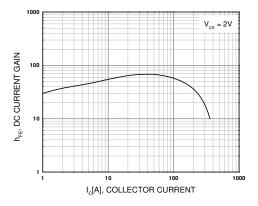


Figure 1. DC current Gain

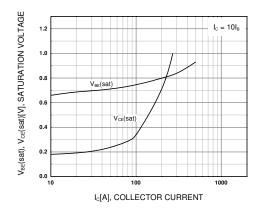


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

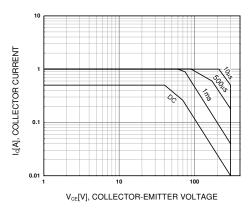


Figure 3. Safe Operating Area

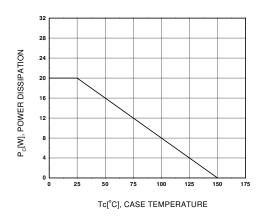
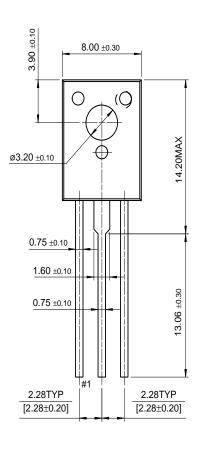
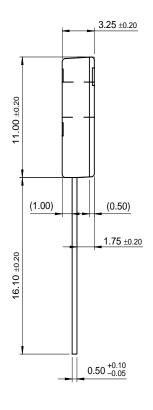


Figure 4. Power Derating

Package Demensions

TO-126







Dimensions in Millimeters

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