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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# 2SK3142

# Silicon N Channel MOS FET High Speed Power Switching

REJ03G1071-0300

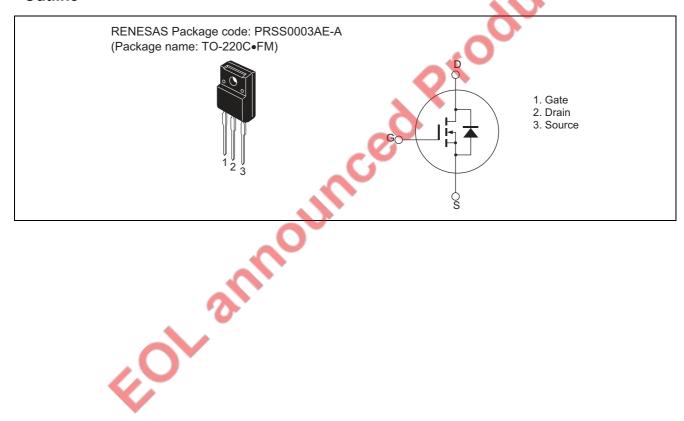
(Previous: ADE-208-681A)

Rev.3.00 Sep 07, 2005

#### **Features**

- Low on-resistance  $R_{DS(on)} = 4 \text{ m}\Omega \text{ typ.}$
- Low drive current
- 4 V gate drive device can be driven from 5 V source

#### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

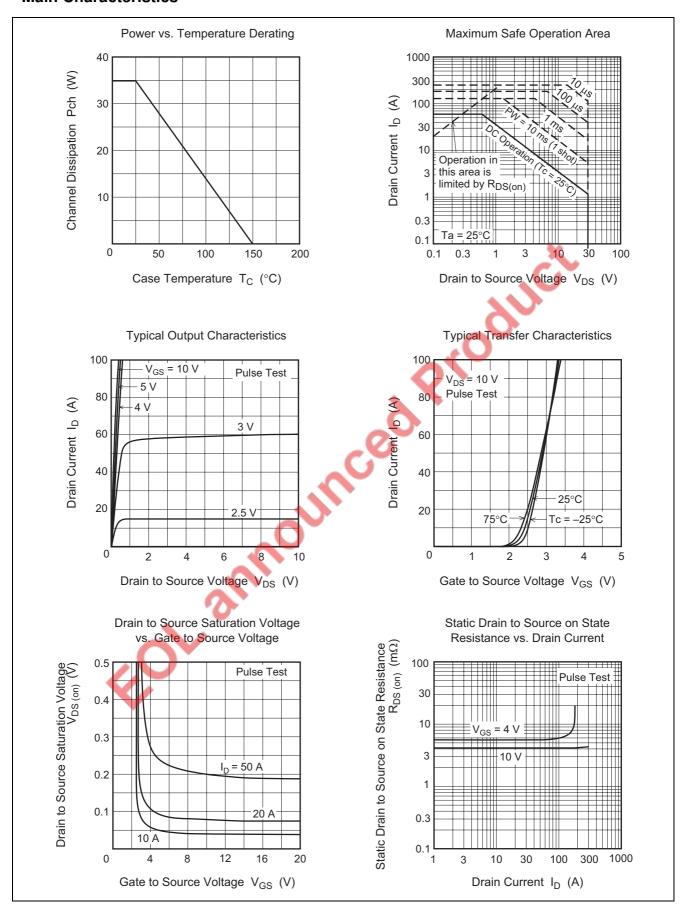
Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	30	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	60	Α
Drain peak current	I <sub>D(pulse)</sub> Note 1	240	Α
Body-drain diode reverse drain current	I <sub>DR</sub>	60	Α
Avalanche current	I <sub>AP</sub> Note 3	35	Α
Avalanche energy	E <sub>AR</sub> Note 3	122	mJ
Channel dissipation	Pch Note 2	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	−55 to +150	°C

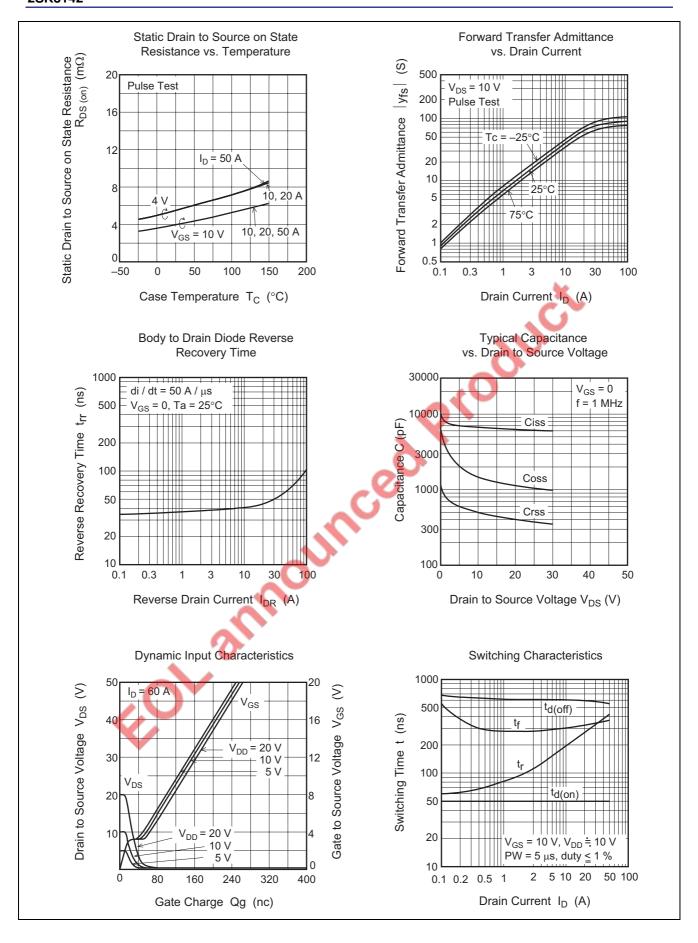
#### **Electrical Characteristics**

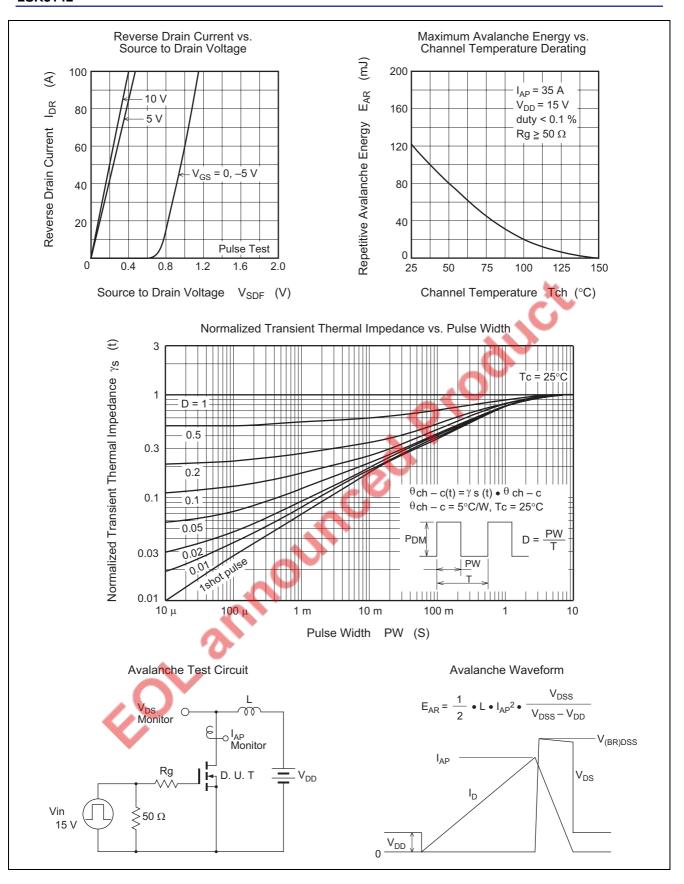
Storage temperature		Tstg		−55 to +150		0 °C		
Notes: 1. $PW \le 10 \infty$ , duty cycle $\le$	1 %					<b>M</b>		
<ol><li>Value at Tc = 25°C</li></ol>								
3. Value at Tch = 25°C, Rg $\geq$ 50 $\Omega$								
<ul> <li>Notes: 1. PW ≤ 10 ∞s, duty cycle ≤ 1 %</li> <li>2. Value at Tc = 25°C</li> <li>3. Value at Tch = 25°C, Rg ≥ 50 Ω</li> <li>Electrical Characteristics</li> </ul>								
						$(Ta = 25^{\circ}C)$		
Item	Symbol	Min	Тур	Max	Unit	Test Conditions		
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	_		V	$I_D = 10 \text{ mA}, V_{GS} = 0$		
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	∝A	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$		
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	10	∝A	$V_{DS} = 30 \text{ V}, V_{GS} = 0$		
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.5	V	$I_D = 1$ mA, $V_{DS} = 10$ V Note 4		
Static drain to source on state	R <sub>DS(on)</sub>	_	4.0	5.0	mΩ	$I_D = 30 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$		
resistance		_	5.5	8.5	mΩ	$I_D = 30 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note 4}}$		
Forward transfer admittance	y <sub>fs</sub>	45 🤞	75	_	S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$		
Input capacitance	Ciss	-	6800	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$		
Output capacitance	Coss	~	1550	_	pF	f = 1 MHz		
Reverse transfer capacitance	Crss	<b>O</b>	500	_	pF			
Total gate charge	Qg	_	130	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 10 \text{ V},$		
Gate to source charge	Qgs	_	16	_	nC	I <sub>D</sub> = 60 A		
Gate to drain charge	Qgd	_	30	_	nC			
Turn-on delay time	$t_{d(on)}$	_	50		ns	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A},$		
Rise time	t <sub>r</sub>	_	340	_	ns	$R_L = 0.33 \Omega$		
Turn-off delay time	$t_{d(off)}$	_	560	_	ns			
Fall time	t <sub>f</sub>	_	350	_	ns			
Body-drain diode forward voltage	$V_{DF}$	_	1.0	_	V	$I_F = 60 \text{ A}, V_{GS} = 0$		
Body-drain diode reverse recovery	t <sub>rr</sub>	_	70	_	ns	$I_F = 60 \text{ A}, V_{GS} = 0$		
time						di <sub>F</sub> / dt = 50 A/≪s		

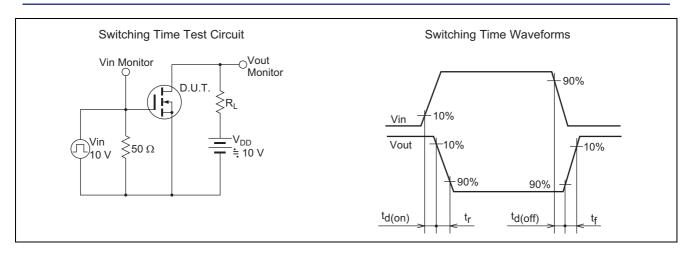
Note: 4. Pulse test

#### **Main Characteristics**



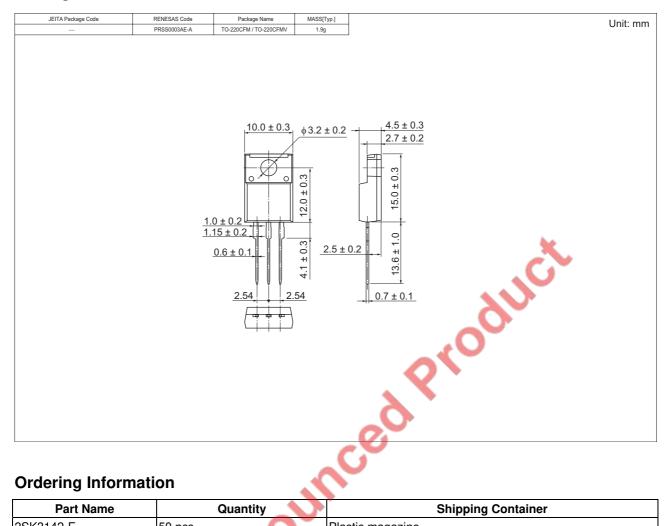








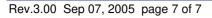
#### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	S	Shipping Container
2SK3142-E	50 pcs		Plastic magazine

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