

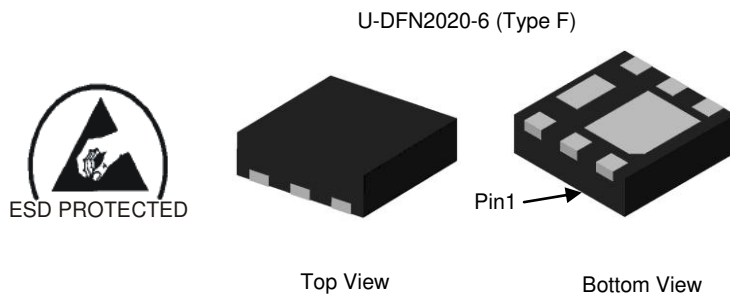
## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
65V	18mΩ @ V <sub>GS</sub> = 10V	8.1A
	23mΩ @ V <sub>GS</sub> = 4.5V	7.1A

## Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- DC-DC Converter
- Adaptor Switch
- Wireless Charging

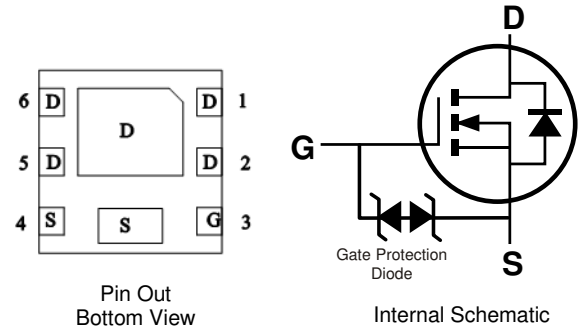


## Features and Benefits

- 100% Unclamped Inductive Switching (UIS) Test in Production—Ensures More Reliable and Robust End Application
- 0.6mm Profile—Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low On-Resistance
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.007 grams (Approximate)

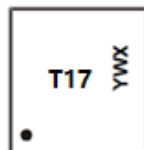


## Ordering Information (Note 4)

Part Number	Package	Quantity per Reel
DMT6017LFDF-7	U-DFN2020-6 (Type F)	3,000
DMT6017LFDF-13	U-DFN2020-6 (Type F)	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



T17 = Product Type Marking Code  
 YWX = Date Code Marking  
 Y = Year (ex: 9 = 2019)  
 W = Week (ex: a = Week 27; z Represents Week 52 and 53)  
 X = Internal Code (ex: U = Monday)

### Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	9	0	1	2	3	4	5	6	7
Week	1-26			27-52			53		
Code	A-Z			a-z			z		
Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
Code	T	U	V	W	X	Y	Z		

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	65	V
Gate-Source Voltage	V <sub>GSS</sub>	±16	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	I <sub>D</sub>	T <sub>A</sub> = +25°C	8.1
		T <sub>A</sub> = +70°C	6.5
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	50	A
Maximum Body Diode Continuous Current	I <sub>S</sub>	8	A
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)	I <sub>SM</sub>	50	A
Avalanche Current (Note 7) L = 0.1mH	I <sub>AS</sub>	19	A
Avalanche Energy (Note 7) L = 0.1mH	E <sub>AS</sub>	18	mJ

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	157	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	1.76	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	71	°C/W
Thermal Resistance, Junction to Case (Note 6)	R <sub>θJC</sub>	10	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	65	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 10mA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±12.8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS</b> (Note 8)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	1.4	2.3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	13.2	18	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A
		—	17	23		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4A
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
<b>DYNAMIC CHARACTERISTICS</b> (Note 9)						
Input Capacitance	C <sub>iss</sub>	—	891	—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	223	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	29	—		
Gate Resistance	R <sub>g</sub>	—	1.57	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	7.5	—	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 6A
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	15.3	—		
Gate-Source Charge	Q <sub>gs</sub>	—	1.8	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	3.1	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	4.0	—	ns	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, R <sub>g</sub> = 3.3Ω, I <sub>D</sub> = 6A
Turn-On Rise Time	t <sub>R</sub>	—	5.9	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	11.7	—		
Turn-Off Fall Time	t <sub>F</sub>	—	3.3	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	21.1	—	ns	I <sub>F</sub> = 6A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	11.9	—	nC	

- Notes:
- Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
  - I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

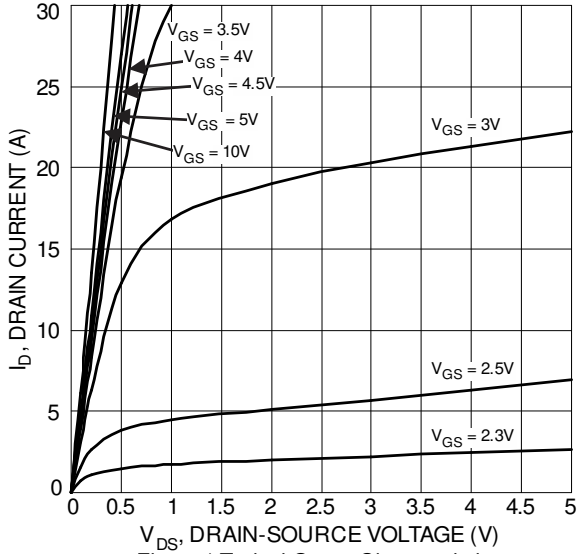


Figure 1 Typical Output Characteristic

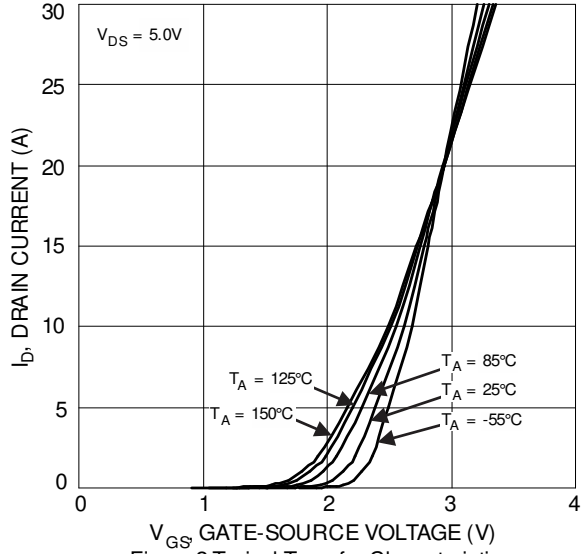


Figure 2 Typical Transfer Characteristics

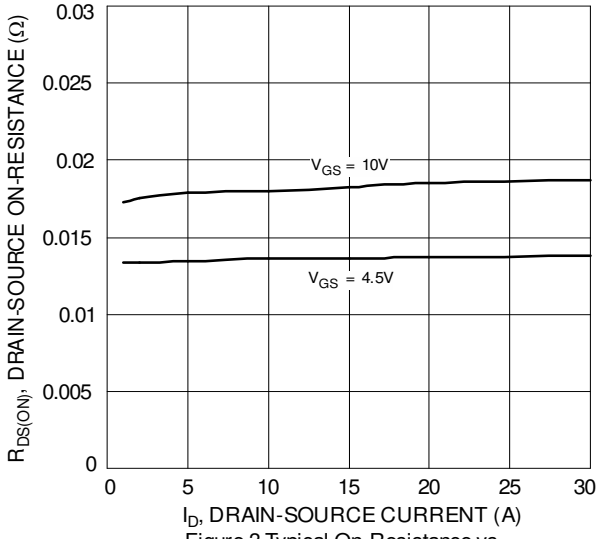


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

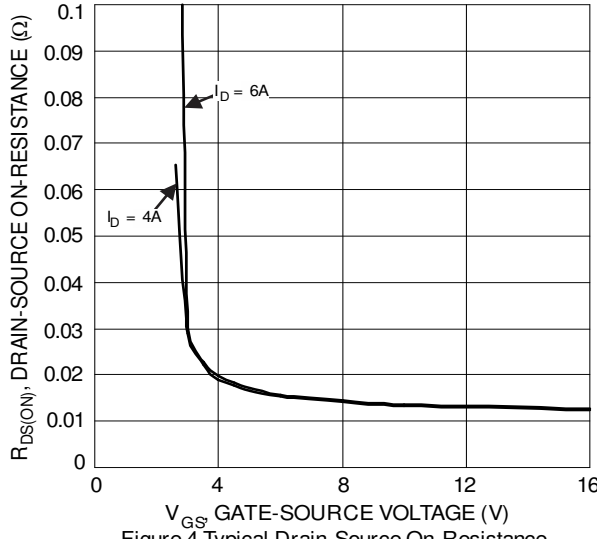


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

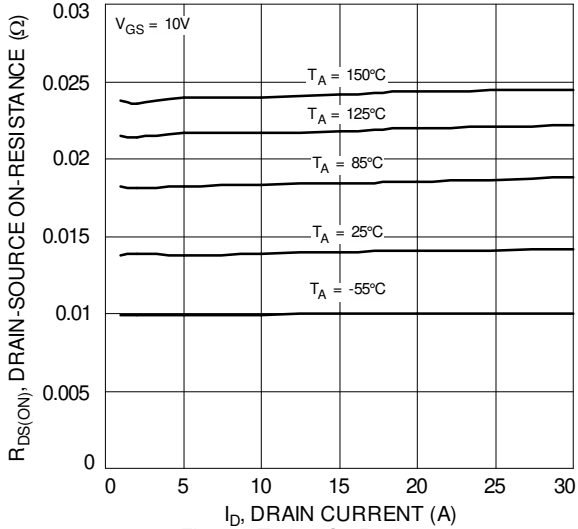


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

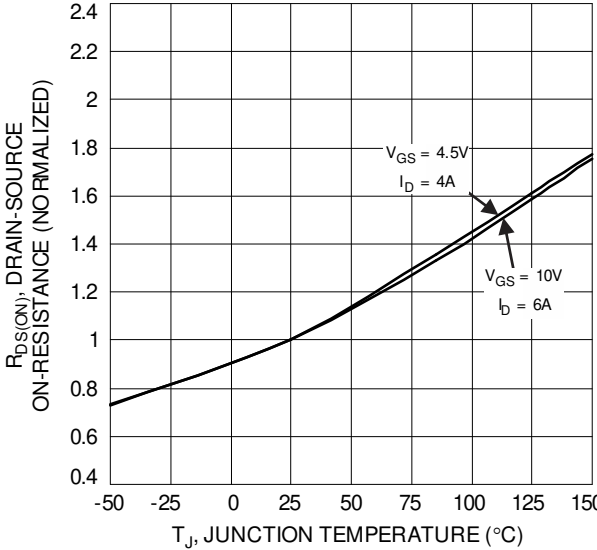
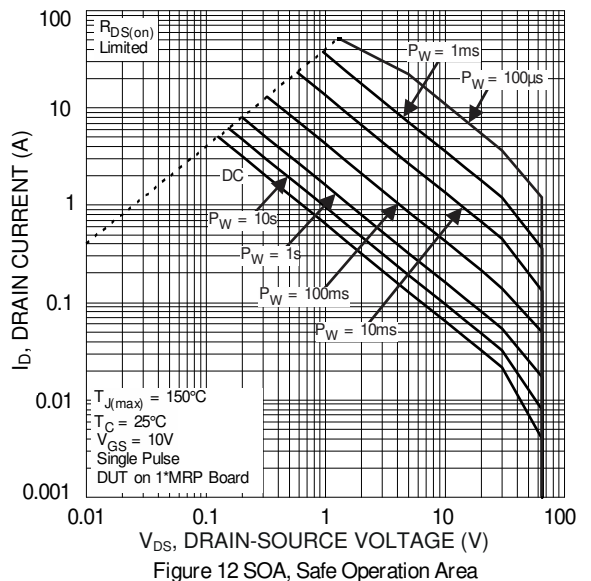
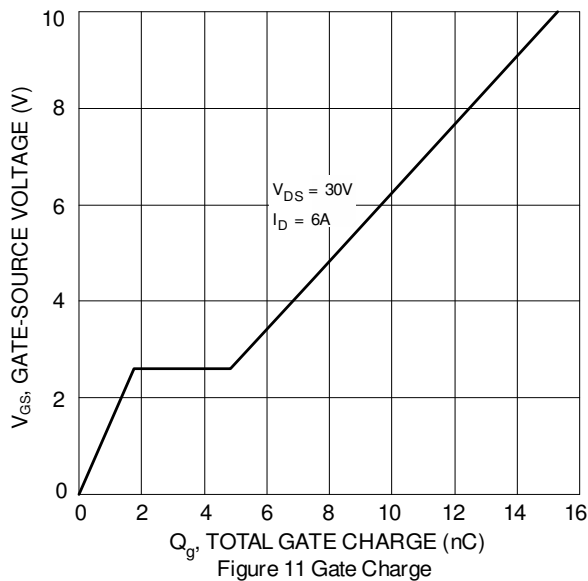
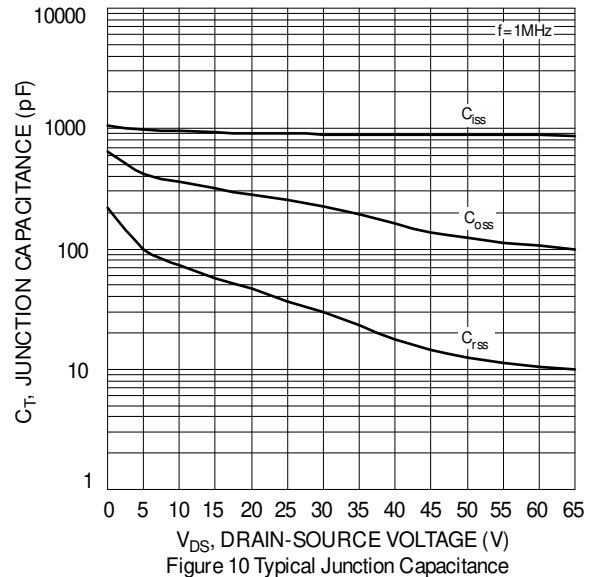
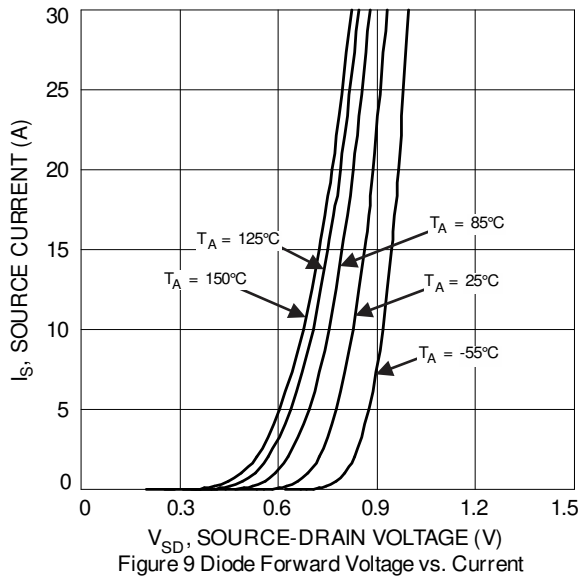
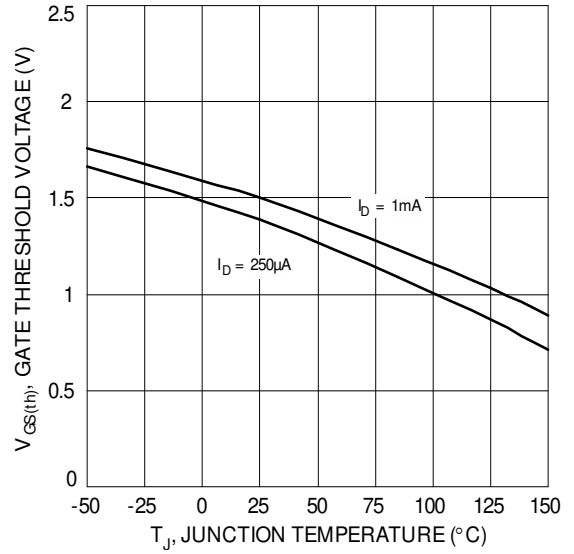
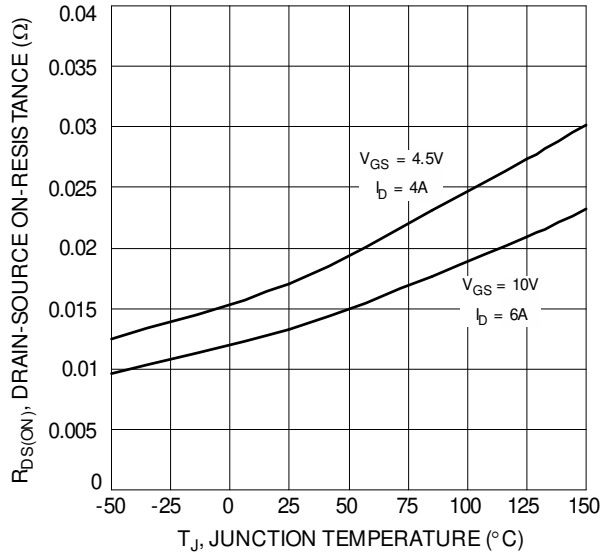
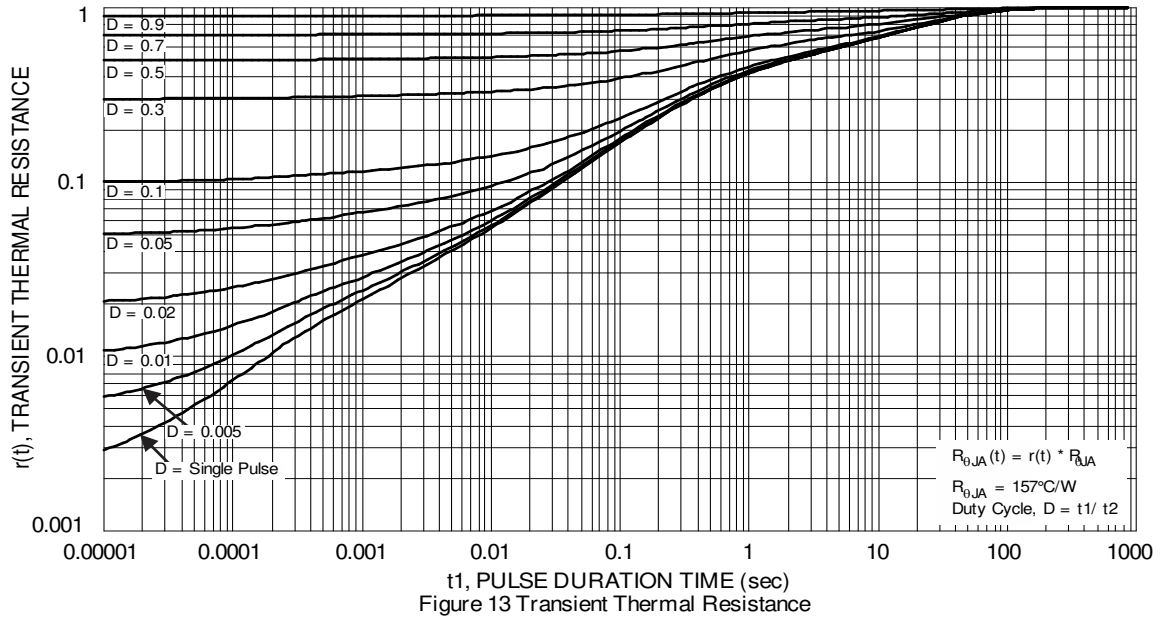


Figure 6 On-Resistance Variation with Temperature

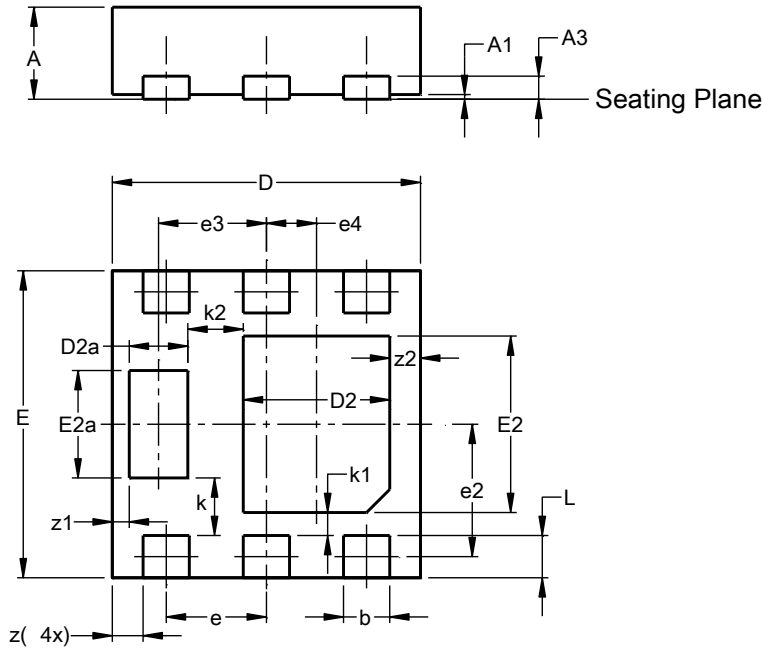




**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**U-DFN2020-6 (Type F)**

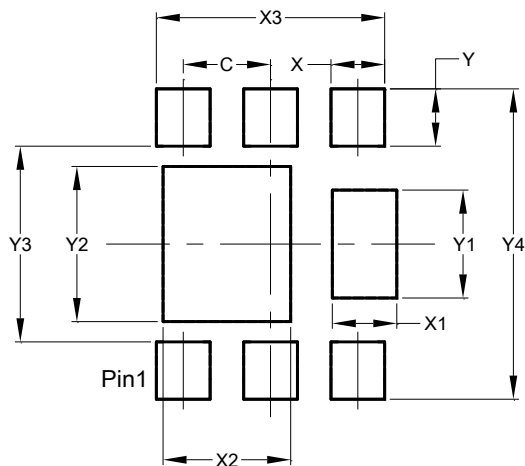


U-DFN2020-6 (Type F)			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0.00	0.05	0.03
A3	-	-	0.15
b	0.25	0.35	0.30
D	1.95	2.05	2.00
D2	0.85	1.05	0.95
D2a	0.33	0.43	0.38
E	1.95	2.05	2.00
E2	1.05	1.25	1.15
E2a	0.65	0.75	0.70
e	0.65 BSC		
e2	0.863 BSC		
e3	0.70 BSC		
e4	0.325 BSC		
k	0.37 BSC		
k1	0.15 BSC		
k2	0.36 BSC		
L	0.225	0.325	0.275
z	0.20 BSC		
z1	0.110 BSC		
z2	0.20 BSC		
<b>All Dimensions in mm</b>			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**U-DFN2020-6 (Type F)**



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300

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