



DMHT6016LFJ

60V N-CHANNEL ENHANCEMENT MODE MOSFET H-BRIDGE

Product Summary

BV _{DSS}	Rds(on)	Ι _D T _A = +25°C
60V	22mΩ @ V _{GS} = 10V	10.6A
60 V	30mΩ @ V _{GS} = 4.5V	8.7A

Description

This new generation complementary MOSFET H-Bridge features low on-resistance achievable with low gate drive.

Applications

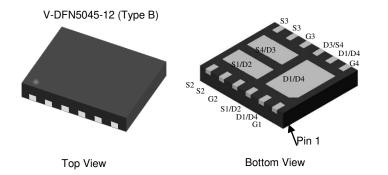
- Motor Control
- DC-DC Converters
- Power Management

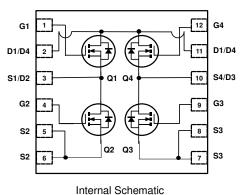
Features

- Thermally Efficient Package Cooler Running Applications
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: V-DFN5045-12 (Type B)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)





Ordering Information (Note 4)

P		
Part Number	Case	Packaging
DMHT6016LFJ-13	V-DFN5045-12 (Type B)	3,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

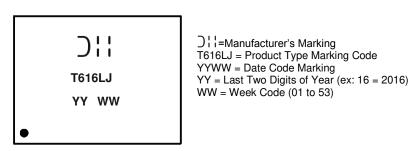
 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:





Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note C) V 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	۱ _D	10.6 8.5	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	۱ _D	14.8 11.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	60	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	2	А
Avalanche Current (Note 7) L=0.1mH			I _{AS}	15.3	А
Avalanche Energy (Note 7) L=0.1mH			E _{AS}	11.7	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	1.16	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Р	108	°C/W	
Thermal Resistance, Junction to Amblent (Note 5)	t<10s	R _{0JA}	56	0/11	
Total Power Dissipation (Note 6)		PD	2.7	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Р	46	°C/W	
Thermal Resistance, Junction to Amblent (Note 6)	t<10s	R _{θJA}	24		
Thermal Resistance, Junction to Case (Note 6)		R _{ejc}	4.4	°C/W	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

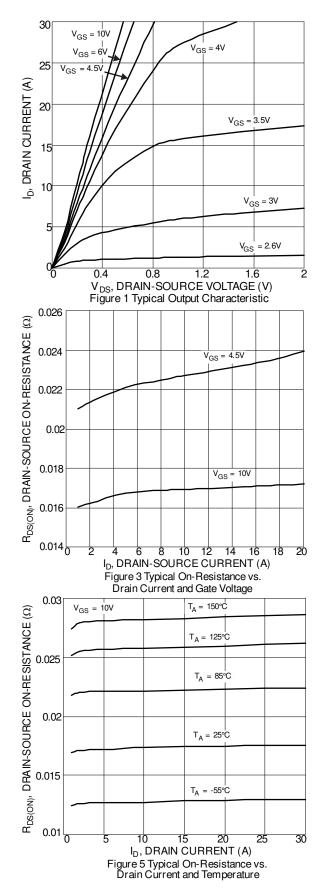
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)				r	r	-	
Drain-Source Breakdown Voltage	BV _{DSS}	60			V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-	—	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance			17	22	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		22.2	30	11122	$V_{GS} = 4.5V, I_D = 6A$	
Diode Forward Voltage	V _{SD}		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{ISS}		864	—		$\label{eq:VDS} \begin{split} V_{DS} &= 30V, \ V_{GS} = 0V, \\ f &= 1 MHz \end{split}$	
Output Capacitance	Coss		282	_	pF		
Reverse Transfer Capacitance	C _{RSS}		27	_			
Gate Resistance	R _G		1.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q _G	_	8.4	—			
Total Gate Charge (V _{GS} = 10V)	Q _G	_	17	_	nC	$V_{DS} = 30V, I_D = 10A$	
Gate-Source Charge	Q _{GS}	_	3.1	—	110		
Gate-Drain Charge	Q _{GD}	_	4.3	_			
Turn-On Delay Time	t _{D(ON)}	_	3.4	_			
Turn-On Rise Time	t _R	_	5.2	_		$\label{eq:VGS} \begin{array}{l} V_{GS} = 10V, \ V_{DS} = 30V, \\ R_G = 6\Omega, \ I_D = 10A \end{array}$	
Turn-Off Delay Time	t _{D(OFF)}	_	13	—	ns		
Turn-Off Fall Time	tF	-	7	_			
Reverse Recovery Time	t _{RR}	_	22	—	ns		
Reverse Recovery Charge	Q _{RR}	_	11	_	nC	I _F = 10A, di/dt = 100A/μs	

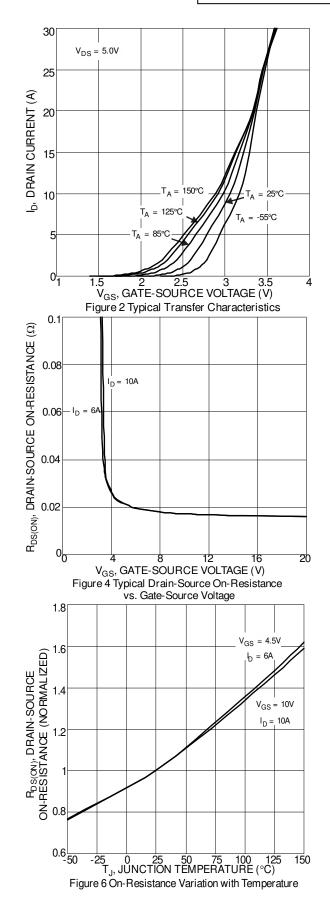
Notes:

Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

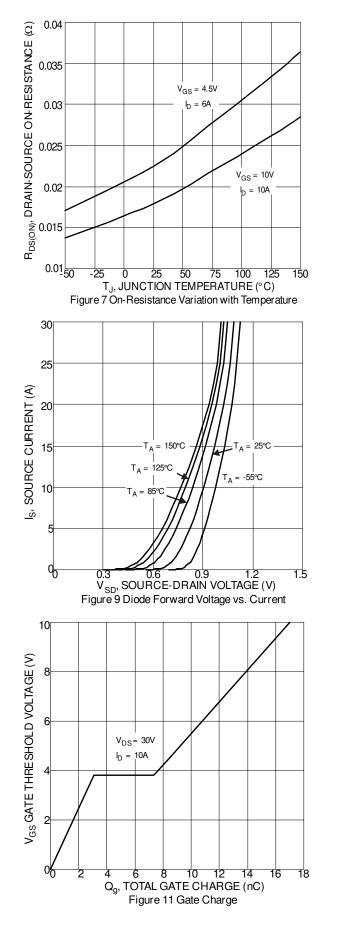
7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$. 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.

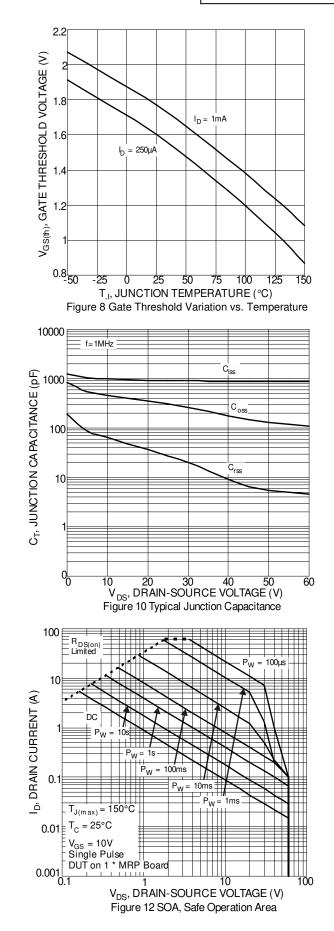






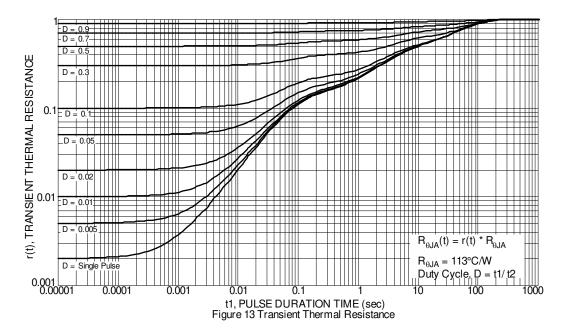








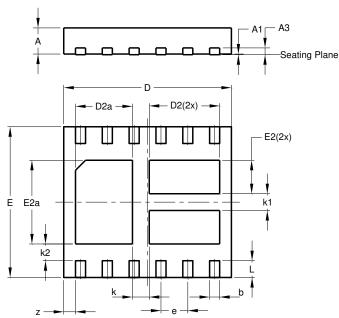






Package Outline Dimensions

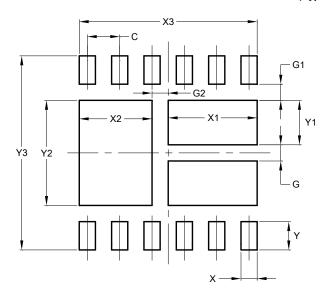
Please see http://www.diodes.com/package-outlines.html for the latest version.



	V-DFN5045-12 (Type B)					
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.25	0.35	0.30			
D	4.95	5.05	5.00			
D2	2.00	2.20	2.10			
D2a	1.60	1.80	1.70			
E	4.45	4.55	4.50			
E2	0.90	1.10	1.00			
E2a	2.40	2.60	2.50			
е	-	-	0.80			
k	-	-	0.50			
k1	_	_	0.50			
k2	_	_	0.50			
L	0.45	0.55	0.50			
z	-	_	0.35			
Al	Dimens	ions in m	າຫ			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



V-DFN5045-12 (Type B)

Dimensions	Value (in mm)			
С	0.800			
G	0.40			
G1	0.40			
G2	0.40			
Х	0.40			
X1	2.20			
X2	1.80			
X3	4.40			
Y	0.700			
Y1	1.100			
Y2	2.600			
Y3	4.800			

V-DFN5045-12 (Type B)



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