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GREEN (5-2008)



### Vishay Semiconductors

## **Silicon NPN Phototransistor**



VEMT2023SLX01 is a silicon NPN epitaxial planar phototransistor in a miniature side looking, surface mount

package (SMD) with dome lens and daylight blocking filter.

Filter bandwidth is matched with 830 nm to 950 nm IR

#### **FEATURES**

- Package type: surface mount
- · Package form: side view
- Dimensions (L x W x H in mm): 2.3 x 2.55 x 2.3
- AEC-Q101 qualified
- · High radiant sensitivity
- Daylight blocking filter matched with 830 nm to 950 nm IR emitters
- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 35^{\circ}$
- Package matched with IR emitter series VSMB2943SLX01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>



- · Detector in automotive applications
- · Photo interrupters
- · Miniature switches
- Counters
- Encoders
- · Position sensors

PRODUCT SUMMARY				
COMPONENT	I <sub>ca</sub> (mA)	φ (deg)	λ <sub>0.5</sub> (nm)	
VEMT2023SLX01	2.7	± 35	790 to 970	

#### Note

**DESCRIPTION** 

emitters.

• Test condition see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMT2023SLX01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	Side view	

#### Note

MOQ: minimum order quantity



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Collector emitter voltage		V <sub>CEO</sub>	20	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		I <sub>C</sub>	50	mA	
Power power dissipation	T <sub>amb</sub> ≤ 75 °C	Pv	100	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C	
Soldering temperature	Acc. reflow profile fig. 8	T <sub>sd</sub>	260	°C	
Thermal resistance junction/ambient	Acc. J-STD-051	R <sub>thJA</sub>	250	K/W	

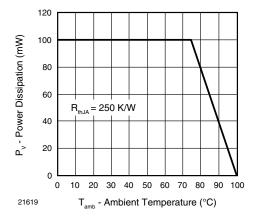


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	I <sub>C</sub> = 0.1 mA	V <sub>CEO</sub>	20			V
Collector dark current	$V_{CE} = 5 \text{ V}, E = 0$	I <sub>CEO</sub>		1	100	nA
Collector emitter capacitance	V <sub>CE</sub> = 0 V, f = 1 MHz, E = 0	C <sub>CEO</sub>		25		pF
Collector light current	$E_e$ = 1 mW/cm <sup>2</sup> , $\lambda$ = 950 nm, $V_{CE}$ = 5 V	I <sub>ca</sub>	1.3	2.7	4.1	mA
Angle of half sensitivity		φ		± 35		deg
Wavelength of peak sensitivity		λρ		860		nm
Range of spectral bandwidth		λ <sub>0.5</sub>		790 to 970		nm
Collector emitter saturation voltage	$I_{\rm C} = 0.05 \; {\rm mA}$	V <sub>CEsat</sub>			0.4	V
Temperature coefficient of Ica	$E_e$ = 1 mW/cm <sup>2</sup> , $\lambda$ = 950 nm, $V_{CE}$ = 5 V	Tk <sub>lca</sub>		1.1		%/K

### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

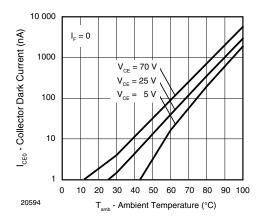


Fig. 2 - Collector Dark Current vs. Ambient Temperature

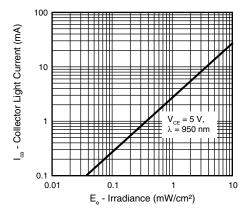


Fig. 3 - Collector Light Current vs. Irradiance

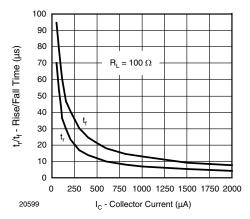


Fig. 4 - Rise/Fall Time vs. Collector Current

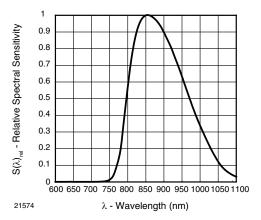


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

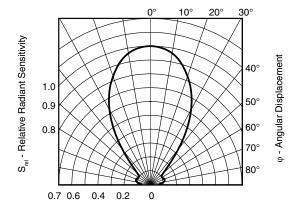


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

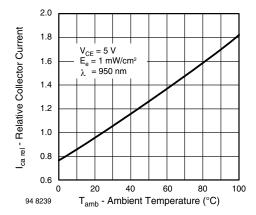


Fig. 7 - Relative Collector Current vs. Ambient Temperature



#### **REFLOW SOLDER PROFILE**

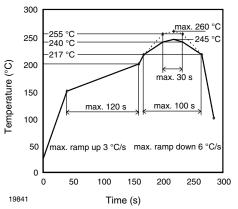


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

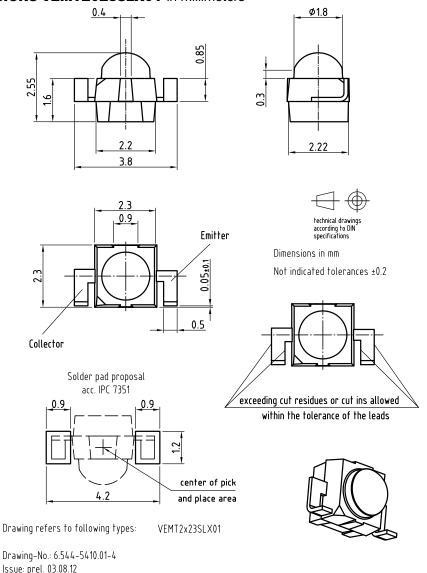
Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

Moisture sensitivity level 2a, acc. to J-STD-020.

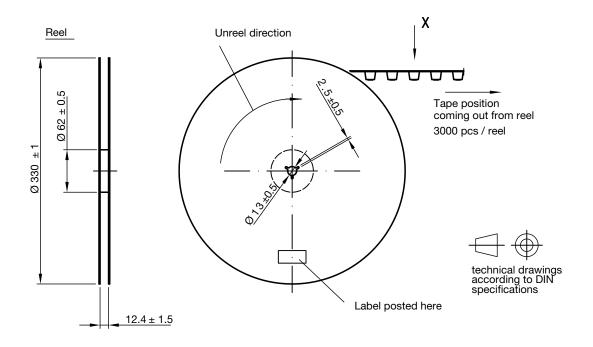
#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40  $^{\circ}$ C (+ 5  $^{\circ}$ C), RH < 5  $^{\circ}$ M.

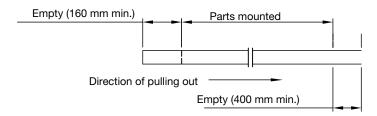
#### **PACKAGE DIMENSIONS VEMT2023SLX01** in millimeters



#### TAPE AND REEL DIMENSIONS VEMT2023SLX01 in millimeters

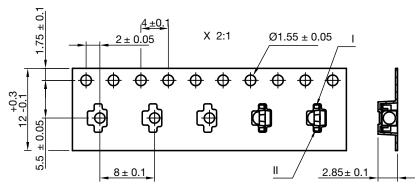


Leader and tailer tape:



### Terminal position in tape

Device	Lead I	Lead II	
VSMB2943SLX01			
VSMF2893SLX01		A	
VSMB2948SL	Cathode	Anode	
VEMD2023SLX01			
VEMD2523SLX01			
VEMT2023SLX01		F:44	
VEMT2523SLX01	Collector	Emitter	
VSMY2853SL	Anode	Cathode	



Drawing refers to following types: see table

Reel dimensions and tape

Drawing-No.: 9.800-5123.01-4

Issue: 2; 19.02.13



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